

## PYE - P123BQ

Transistor	Emitter	Base	Collector			
			V	V	V	mA
TR1 V6/R4M <sup>1</sup>	..	0.5	0.6	2.4	0.5	
TR2 V6/R4 <sup>2</sup>	..	0.7	0.8	6.0	0.7	
TR3 V6/R2 <sup>3</sup>	..	1.2	1.28	5.3	0.45	
TR4 V10/50B <sup>4</sup>	..	0.42	0.58	5.3	1.9	
TR5 V10/30A <sup>5</sup>	..	0.024	0.2	5.95	1.2	
TR6 V10/30A <sup>5</sup>	..	0.024	0.2	5.95	1.2	

<sup>1</sup> V6/R8M

<sup>2</sup> V6/R8

<sup>3</sup> V6/R4

<sup>4</sup> OC71

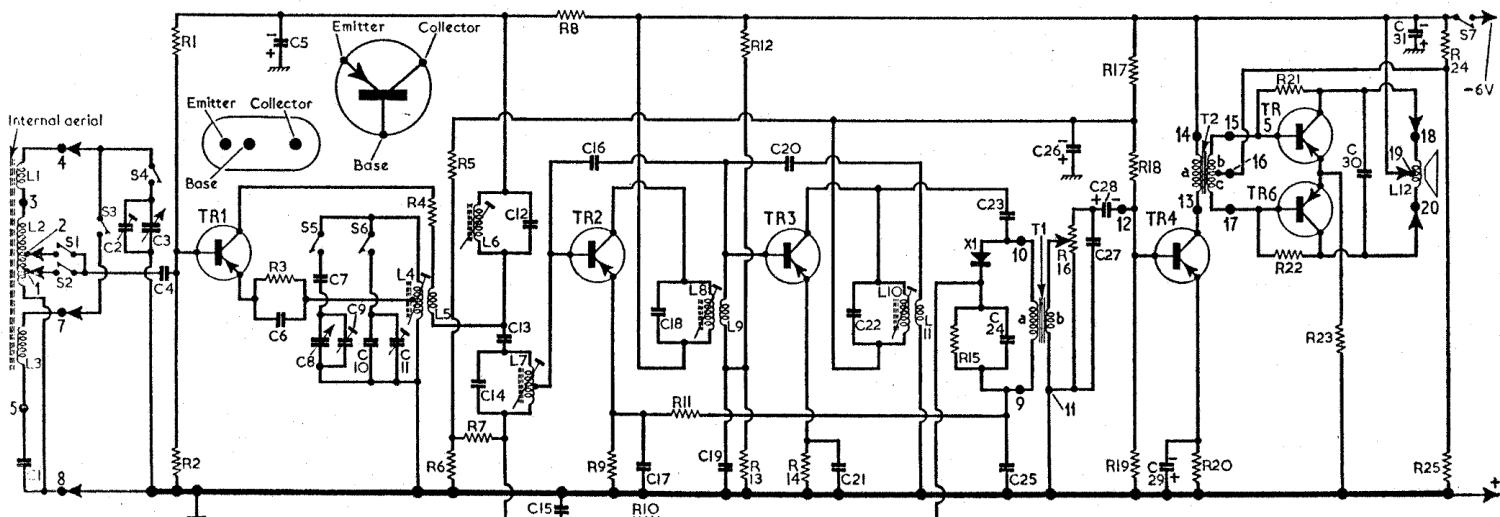
<sup>5</sup> V10/50B or OC72

} Alternative types.

Capacitors				Resistors				Other Components			
C1	3,300pF	D1	C27	0.1μF	D1	R21	15kΩ	D2	L1	—	B1
C2	35pF	A1	C28	5μF	D2	R22	15kΩ	D2	L2	—	C1
C3	523pF <sup>b</sup>	A1	C29	50μF	D2	R23	10kΩ	D2	L3	—	D1
C4	0.01μF	A2	C30	0.5μF	D2	R24	8.2kΩ	D2	L4	(total) 2.5	B2
C5	25μF	A2	C31	50μF	A1	R25	120Ω	D2	L5	—	B2
C6	0.01μF	A2	R1 <sup>c</sup>	47kΩ	A2	L6	3.0	B2	R7	15kΩ	E4
C7	790μF	A2	R2 <sup>c</sup>	15kΩ	A2	L7	(total) 1.4	B2	C13	1kΩ	A2
C8	523pF <sup>b</sup>	A2	R3	1kΩ	A2	L8	1.2	B2	C14	1,200pF	B2
C9	35pF	A2	R4	100Ω	B2	L9	0.2	B2	C15	0.25μF	A2
C10	895pF	B2	R5	56kΩ	A2	L10	0.6	C2	C16 <sup>d</sup>	—	F4
C11	50pF	B2	R6	22kΩ	A2	L11	0.1	C2	C17	0.1μF	B1
C12	1,200pF	B2	R7	15kΩ	A2	L12	110.0	{(total)}	C18	0.002μF	B2
C13	15pF	B2	R8	6.8kΩ	A2	T1 { <sup>a</sup>	872.0	D2	C19	0.1μF	C2
C14	1,200pF	B2	R9	1kΩ	B1	{ <sup>b</sup>	145.0	D2	C20 <sup>e</sup>	—	F4
C15	0.25μF	A2	R10	15kΩ	C2	T2 { <sup>a</sup>	298.0	C1	C21	0.1μF	C1
C16 <sup>d</sup>	—	F4	R11	330Ω	C2	{ <sup>b</sup>	77.0 total	C1	C22	3,900pF	C2
C17	0.1μF	B1	R12	68kΩ	C2	S1-S6	—	B1	C23	2,200pF	C2
C18	0.002μF	B2	R13	22kΩ	C2	S7	—	D1	C24	1μF	C2
C19	0.1μF	C2	R14	2.7kΩ	C1				C25	0.1μF	D2
C20 <sup>e</sup>	—	C1	R15	15kΩ	C1				C26	50μF	D1
C21	0.1μF	C1	R16	3kΩ	D1				R17	220Ω	D2
C22	3,900pF	C2	R17	1kΩ	C1				R18	47kΩ	D2
C23	2,200pF	C2	R19	10kΩ	D2				R19	220Ω	D2
C24	1μF	C2	R20	220Ω	D2				R21	15kΩ	D2
C25	0.1μF	D2							R22	15kΩ	D2
C26	50μF	D1							R23	10kΩ	D2

<sup>a</sup> See "Neutralizing Procedure" above. <sup>b</sup> May be 150kΩ. <sup>c</sup> Omitted when R1 is 150kΩ. <sup>d</sup> Approximate D.C. resistance in ohms; a dash indicates very low resistance. <sup>e</sup> Swing value, min. to max.

Intermediate frequency 315kc/s.



### CIRCUIT ALIGNMENT

- Remove chassis from carrying case, leaving the battery and frame aerial leads connected.
- Connect output of signal generator between chassis and junction of S2, C4.
- Switch receiver to M.W. and turn gang to maximum capacitance.
- Feed in a 315kc/s signal and adjust the cores of L10 (C2), L8 (B2), L7 (B2) and L6 (B2) for maximum output. Repeat these adjustments until no further improvement results.
- Tune receiver to 500m, feed in a 600kc/s signal and adjust the core of L4 (B2) for maximum output.
- Tune receiver to 200m, feed in a 1,500kc/s signal and adjust C9 (A2) for maximum output.

- Repeat this adjustment and step 5 until calibration is correct.
- Switch receiver to L.W., feed in a 200kc/s signal and adjust C11 (B2) for maximum output.
  - Replace receiver in carrying case. Connect output of signal generator to a loop of wire and place the loop about 12in from the L1 end of the ferrite rod aerial.
  - Tune receiver to 500m, feed in a 600kc/s signal and adjust the position of L3 on the ferrite rod for maximum sound output.
  - Tune receiver to 200m, feed in a 1,500kc/s signal and adjust C2 (A1) for maximum output.

- Repeat the adjustments in steps 8 and 9 until calibration is correct and then seal L1 in position with polystyrene cement. Disconnect signal generator.
- Connect 10,000 ohms-per-volt meter, switched to 2.5V range, between TR2 emitter and chassis.
- While receiving L.W. Light programme, adjust the position of L3 on the ferrite rod for minimum reading on meter. Finally seal L3 in position with polystyrene cement. A deflection down to 0.1V should be obtainable.