

Resistors

R1	2.2k Ω	F5
R2	1M Ω	F5
R3	47k Ω	F5
R4	220 Ω	F4
R5	1k Ω	F5
R6	47k Ω	F4

*L3 and L5 in series should read 6.2 Ω .

†L4 and L6 in series should read 5.8 Ω .

‡Approximate d.c. resistance in ohms.

§Located in f.m. tuner.

R7	33k Ω	F5
R8	220 Ω	F4
R9	47k Ω	F5
R10	100k Ω	F5
R11	1k Ω	F5
R12	150 Ω	F4
R13	47 Ω	F4
R14	33k Ω	F5
R15	100k Ω	F5
R16	2.2M Ω	F5
R17	47k Ω	F5
R18	1M Ω	F5
R19	1M Ω	F5
R20	1M Ω	D1
R21	220 Ω	D1
R22	10M Ω	F4
R23	47k Ω	G5
R24	220k Ω	G4
R25	1M Ω	E4
R26	820 Ω	F5
R27	47k Ω	F4
R28	500k Ω	A1
R29	150 Ω	F4
R30	47k Ω	F5
R31	2.2k Ω	F5
R32	220 Ω	†
R33	4.7k Ω	†
R34	470k Ω	†
R35	6.8k Ω	†
R36	800 Ω	G4
R37	1M Ω	D1
R38	220 Ω	D1
R39	10M Ω	G4
R40	220k Ω	G4
R41	47k Ω	G5
R42	500k Ω	A1
R43	150 Ω	G5
R44	47k Ω	F5
R45	2.2k Ω	F5

Capacitors

C1	1,000pF	E4
C2	3,000pF	E4
C3	140pF	E5
C4	2,200pF	F4
C5	0.01 μ F	F5
C6	0.01 μ F	F4
C7	250pF	C2
C8	15pF	C2
C9	250pF	C2
C10	15pF	C2
C11	100pF	F5
C12	420pF	F5
C13	420pF	E4
C14	420pF	F5
C15	3,300pF	F4
C16	0.01 μ F	F5
C17	250pF	B2
C18	0.047 μ F	F5
C19	0.02 μ F	F4
C20	500pF	B2
C21	36pF	B2
C22	200pF	G5
C23	100pF	F5
C24	5 μ F	G5
C25	200pF	F5
C26	1,000pF	F5
C27	100pF	F5
C28	140pF	D1
C29	0.047 μ F	F4
C30	8 μ F	G4
C31	0.033 μ F	G4
C32	0.01 μ F	B2
C33	5,000pF	C1
C34	50 μ F	G4
C35	0.1 μ F	F5
C36	0.01 μ F	D2
C37	1,000pF	†
C38	10pF	†

C39	1,000pF	†
C40	180pF	†
C41	4.7pF	†
C42	75pF	†
C43	4.7pF	†
C44	4.7pF	†
C45	13pF	†
C46	75pF	†
C47	180pF	†
C48	20pF	†
C49	10pF	†
C50	20pF	†
C51	32 μ F	G4
C52	32 μ F	G4
C53	140pF	D1
C54	0.047 μ F	G4
C55	0.033 μ F	G4
C56	24 μ F	G4
C57	0.01 μ F	A1
C58	5,000pF	B2
C59	50 μ F	G4
C60	0.1 μ F	F5
C61	5pF	F4
C62	4,700pF	F5
C63	0.01 μ F	D2
C64	0.01 μ F	F5
C65	0.01 μ F	F4
TC1	—	E5
TC2	—	E5
TC3	—	E5
TC4	—	E5
VC1	—	D2

L5	*	C2
L6	†	C2
L7	—	F4
L8	5.8	B2
L9	3.7	B2
L10	2.7	B2
L11	—	B2
L12	—	B2
L13	470.0	B2
L14	—	B2
L15	—	†
L16	—	†
L17	—	†
L18	—	†
L19	—	†
L20	—	†
L21	—	†
L22	260.0	A2
L23	—	A2
L24	28.2	A2
L25	470.0	B3
L26	—	B3
L27	—	—
L28	—	—

Miscellaneous

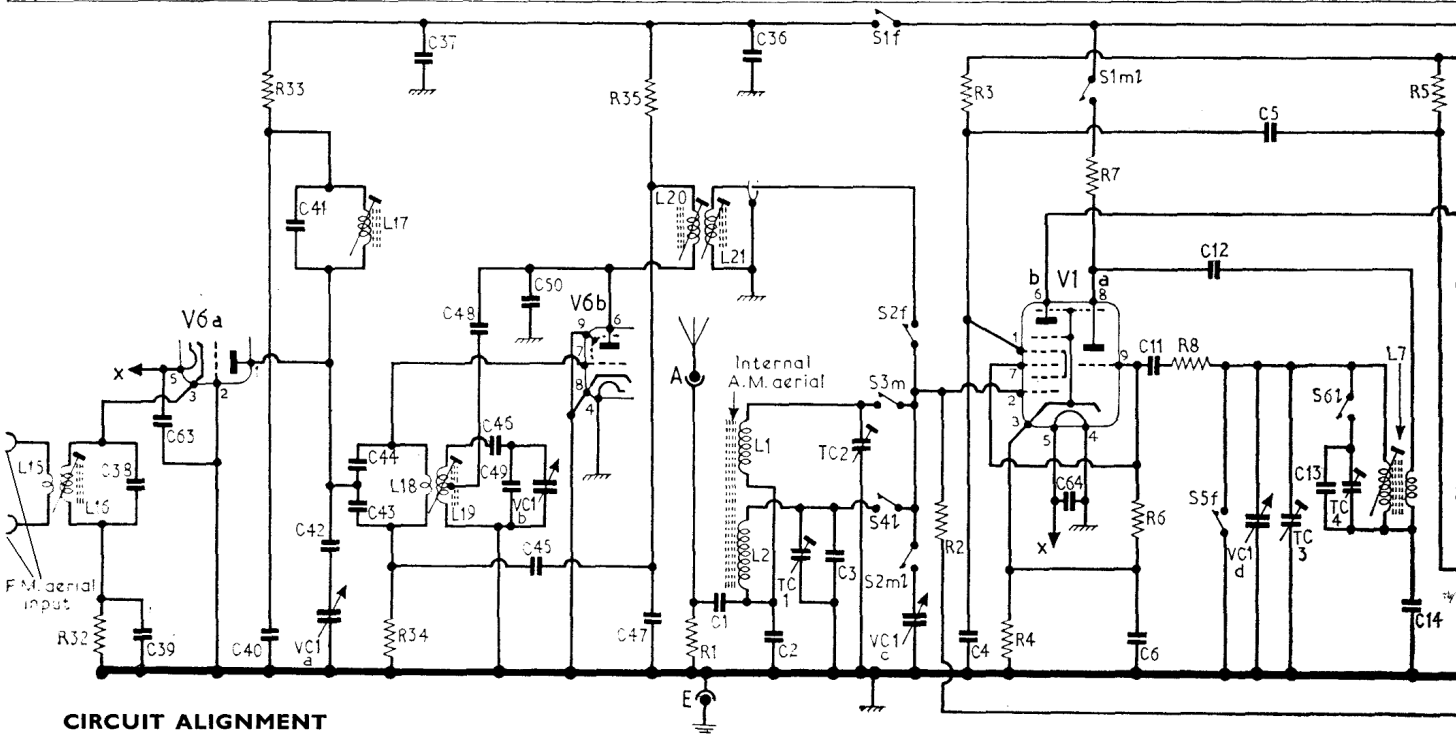
PL1	6.3V 0.15A	B1
PL2	—	C1
S1-S10	—	F4
S11, S12	—	A2

Coils

L1	—	D3
L2	—	C1
L3	*	C2
L4	†	C2

RGD - 210

C	38,39,63	40 41 42,VC1a,43,44,37	48,46,49,50,45,VC1b	47	1	36,2	TC1	3	TC2	VC1c	4	64	6	11	12	VC1d,5,TC3,13,TC4	14	7
R	32	33	34	35	1					2	3	4	7	6	8			5
L	15,16		17	18,19		20,21	1,2											7



CIRCUIT ALIGNMENT

A.M. Circuits

Equipment Required.—An a.m. signal generator and dummy aerial; an output meter matched to 3 Ω impedance or an a.c. voltmeter; a 0.1 μ F capacitor.

- 1.—Connect the output meter in place of one of the loudspeakers or, alternatively, connect the a.c. voltmeter switched to a suitable range across a loudspeaker. Connect the signal generator via the 0.1 μ F capacitor to V1 pin 2.
- 2.—Switch receiver to m.w. and turn the tuning gang to maximum capacitance. Set the volume control at maximum.
- 3.—Feed in a 470kc/s modulated signal and adjust the cores of L9, L8, L4 and L3 for maximum audio output.
- 4.—Connect the signal generator via the dummy aerial to the a.m. aerial and earth sockets. Tune the receiver to 500m, feed in a 600kc/s signal and adjust L7 and L1 (ferrite rod winding) for maximum output.

- 5.—Feed in a 1,500kc/s signal. Tune receiver to 200m and adjust TC3 and TC2 for maximum output.

- 6.—Repeat operations 4 and 5 until there is no further improvement.

- 7.—Switch receiver to l.w. and tune to 1,300m. Feed in a 230kc/s signal and adjust TC4 and TC1 for maximum output.

- 8.—Tune receiver to 1,800m. Feed in a 167kc/s signal and adjust L2 (ferrite rod winding) for maximum output.

- 9.—Repeat operations 7 and 8 until there is no further improvement.

La suite des informations techniques manque sur le document original

RGD - 210

SWITCH CODING

m = closed on M.W.
l = closed on L.W.
f = closed on F.M.
g = closed on Gram.
r = closed on Radio

TC3,13,TC4	14	7,8	62	9,10	15	16,19
	5				9	12
	7		3,4,5,6			

17,18		52,20,21,61,23,25,51,22,26,27,24				28,53,54,29,30		55,31		57,32		56,58,33,59,34		65		60,35		C						
11		10		13		36		15,17,14,18,19,16		20		37		39,22		40		24		23,25,28,42,41,27,26,43,29		44,45,38,30,31,21		R
22,23,24		8,9,10,11,12																		25,26,13,14		27,28		L

