

SECTION 3

CIRCUIT DESCRIPTION

3.0 ATTENUATOR AND VERTICAL INPUT AMPLIFIER Figures 1A & 1B.

CH1 and CH2 are identical and the following description covers both channels.

The input to the attenuator is connected via C901 to the input socket SK901 on AC, directly to the input socket on DC or to ground on GND. The capacity compensated attenuator is switched by S902 wafers 1 in steps of 100, 10 and 1 and the output is fed to the gate of TR601 via C601 and R602 which, together with D601, protect the input against overloads up to ± 400 V. TR601 is a F.E.T. source follower whose output DC level is adjusted to zero by R604 and R608. The output is then attenuated in steps of 5, 2 and 1 selected by S902 wafers 2 F and fed to the base of TR603 which together with TR604 forms a longtailed pair. The diodes D602 and D603 in the emitters increase the signal handling capacity without affecting the change in gain which remains directly proportional to current and, therefore, to supply voltage. This compensates for the CRT sensitivity which is inversely proportional to supply voltage and hence the overall vertical sensitivity remains independent of supply voltage. Vertical shift is fed to the base of TR604. The gain of the channel is set up by R613 which controls the total stage emitter current. The collector currents of TR603 and TR604 pass through thermal compensating networks R611, C602 and R614, C603 and also through grounded base stage TR602 to the output stages. TR602 isolates the channel switching waveforms from the input circuit.

3.1 VERTICAL OUTPUT AMPLIFIER

Figure 2

The collector currents of the selected channel pass through diodes D637 and D638 for Channel 1, or D642 and D643 for Channel 2, to the bases of shunt feedback stages TR633 and TR636. The output of TR633 and TR636 are fed to the bases of TR634 and TR635, a longtailed pair whose collectors feed the Y plates of the CRT. C641 and R671 in the emitter circuit are adjusted for the best pulse response and TR631 compensates for the temperature coefficient of the input stages. The appropriate channel is selected either manually by S631 and S632 which are ganged to the vertical shift controls, or automatically by the Chop/Alternate circuit TR631 and TR632. On EXT X and Time/Div speeds of 2 ms/div and slower, TR631 and TR632 operate as a free-running multivibrator at a frequency of approx. 100 kHz. When TR632 conducts D635 and D636 conduct and turn off D637 and D638 thus disconnecting Channel 1. Conversely when TR631 conducts D639 and D641 conduct and turn off D642 and D643 thus disconnecting Channel 2. C632 and C634 feed pulses to the unblanking amplifier to blank out the trace during the transitions. On Time/Div speeds above 2 ms/div, TR631 and TR632 operate as a bistable whose state is changed at the end of each time base sweep by an edge from the emitter of TR34. The network L631, C640, R640 in CH1 and L632, C649, R669 in CH2 delay the transitions until the trace is blanked. The vertical output amplifier current supplies the +13 V line which is decoupled by C645 and also the +7.5 V line which is stabilized by zener diode D644. The +7.5 V is used as a reference for the -7.5 V line which is a shunt stabilizer circuit consisting of TR637, R659, R666, R667 and R668. R658 compensates for variations in the -13 V line and reduces the effect of supply voltage variations on the -7.5 V line.

3.2 TRIGGER CIRCUIT Figures 3 & 4

This consists of a longtailed pair TR23 and TR25 similar to the Vertical Amplifier input stage but with temperature compensation in its emitter circuit. On the CH2 position the input is connected to CH2 and EXT X position via C20, C21 and R23 to the TRIG/EXT X socket. On all other positions of the Time/Div switch the input is connected to S20 which selects either the EXT TRIG socket, CH1 or CH2 signal. The output from the appropriate collector, selected either by the Polarity switch S21 or the Time/Div switch S1, is connected via diode D27 or D31 to the emitter of common base stage TR26. The collector of TR26 is connected via D54, D33 and D34 to the input of the Horizontal Amplifier on CH2 and EXT X positions of the Time/Div switch S1. On other positions of the Time/Div switch the collector of TR26 is AC coupled via D32 to the base of TR21, a shunt feedback stage.

On the AC position of S22 the output of TR21 is connected via diodes D58 and D59, which limit the voltage swing, to the input of the Schmitt trigger circuit TR22 and TR24. The Level control R21 allows the mean DC level of the output of TR21 to be varied so that any portion of the trigger signal can be set at the triggering level of the Schmitt. When the Level control is in the Auto position the range of the Level control is restricted by the addition of R20 in series with R22. This reduces the Level control range from more than 10 divisions to approximately 2.5 divisions. On the TV position of S22 the output of TR21 is connected via diode D57 to the sync separator TR38 which conducts only during the sync pulses and is cut off during the video signal. On Time/Div speeds of 100 μ s and slower the output of TR23 is integrated by R129 and C53. This gives a waveform where the field pulses are of greater amplitude than the line pulses and are fed via TR25 to the input of the Schmitt trigger circuit. On Time/Div speeds of 50 μ s and faster C63 is disconnected from ground by cutting off diode D56. The line and field pulses are then of equal amplitude and are fed via TR39 to the input of the Schmitt trigger circuit. On TV diodes D58 and D59 are cut off and the Level control is disconnected by switching R22 to -13V. The Schmitt trigger circuit TR22 and TR24 provides a constant amplitude trigger signal at the collector of TR24 to the timebase and bright-line auto circuit.

TR30 provides a positive going gating pulse to the base of TR24 to inhibit the Schmitt trigger circuit during the timebase sweep. This prevents trigger pulses being produced during the sweep and breaking through into the Vertical Amplifier. To prevent the triggering pulses which are produced by the gating waveform being fed to the bright-line AUTO circuit a negative differentiated pulse is fed from the collector of TR30 to the anode of D43 cutting it off before the arrival of the positive trigger pulse. The trigger pulse is thus prevented from reaching the bright-line circuit, triggering it and interrupting the free running of the timebase. The gating is removed on timebase speeds below 1 ms/div by removing the HT supply to TR30 by means of S1 - 2F.

3.3. TIMEBASE, UNBLANKING AND BRIGHT LINE AUTO CIRCUIT Figure 4

3.3.1 The differentiated positive pulse from the trigger circuit is fed via D36 to the base of TR27 which together with TR29 forms a bistable. The positive pulse turns on TR27 which in turn cuts off the clamping transistor TR34. The timebase, a F.E.T. Miller circuit, then runs up linearly charging up the hold-off capacitor via D47 and resetting the bistable via R114. When TR27 cuts off, TR34 con-

ducts and discharges the timing capacitor until D45 conducts and reduces the current in TR34 to the value required by the timing resistor. At this point the flyback stops. During the flyback the hold-off capacitor discharges through R114 until D48 conducts. At this point the action is complete and the timebase can be triggered by the next triggering pulse.

- 3.3.2 If the Trig level control is in the Auto position and no trigger pulses are present, TR34 and D49 conduct and reduce the potential at the anode of D48. This allows the hold-off capacitor to discharge further and re-trigger the bistable. The timebase then free-runs. If trigger pulses are present at the anode of D43 the positive pulses trigger the monostable TR32 and TR35. When triggered at frequencies above about 10 Hz the average collector current of TR35 is low and D49 is cut off returning the timebase to the normal triggered condition.
- 3.3.3 The collector current of TR29 which is cut off during the sweep is fed to the input of TR28. The collector of TR28 goes negative at the beginning of the sweep until diode D38 conducts and clamps the collector potential at about 3 V, thus unblanking the sweep. When TR29 conducts at the end of the sweep TR28 collector goes positive blanking the trace. On chop positions of the sweep negative edges from the chop circuit are fed to the base of TR28 to blank the trace during the transitions.

3.4 HORIZONTAL AMPLIFIER Figure 3

This consists of a shunt feedback stage TR31 with switched feedback resistors to give X5 expansion. The sweep and horizontal shift currents are mixed at the input of the shunt feedback stage and the output is fed to a longtailed pair, TR33 and TR37, which feeds the horizontal deflection plates of the CRT. D44 clamps the collector of TR33 and prevents it from bottoming.

On the EXT X and CH2 positions of the Time/Div switch the input of TR31 is switched to the collector of TR26 via D33, D34 and D54 and the X5 feedback resistor is shorted out by diodes D33, D34, D35 and D37. The trace is then deflected horizontally via the EXT X socket or from Channel 2. On these positions the gate of TR36

is connected to the cathode of D47 which clamps the output of the timebase at about +1 V and turns off TR29 thus unblanking the trace.

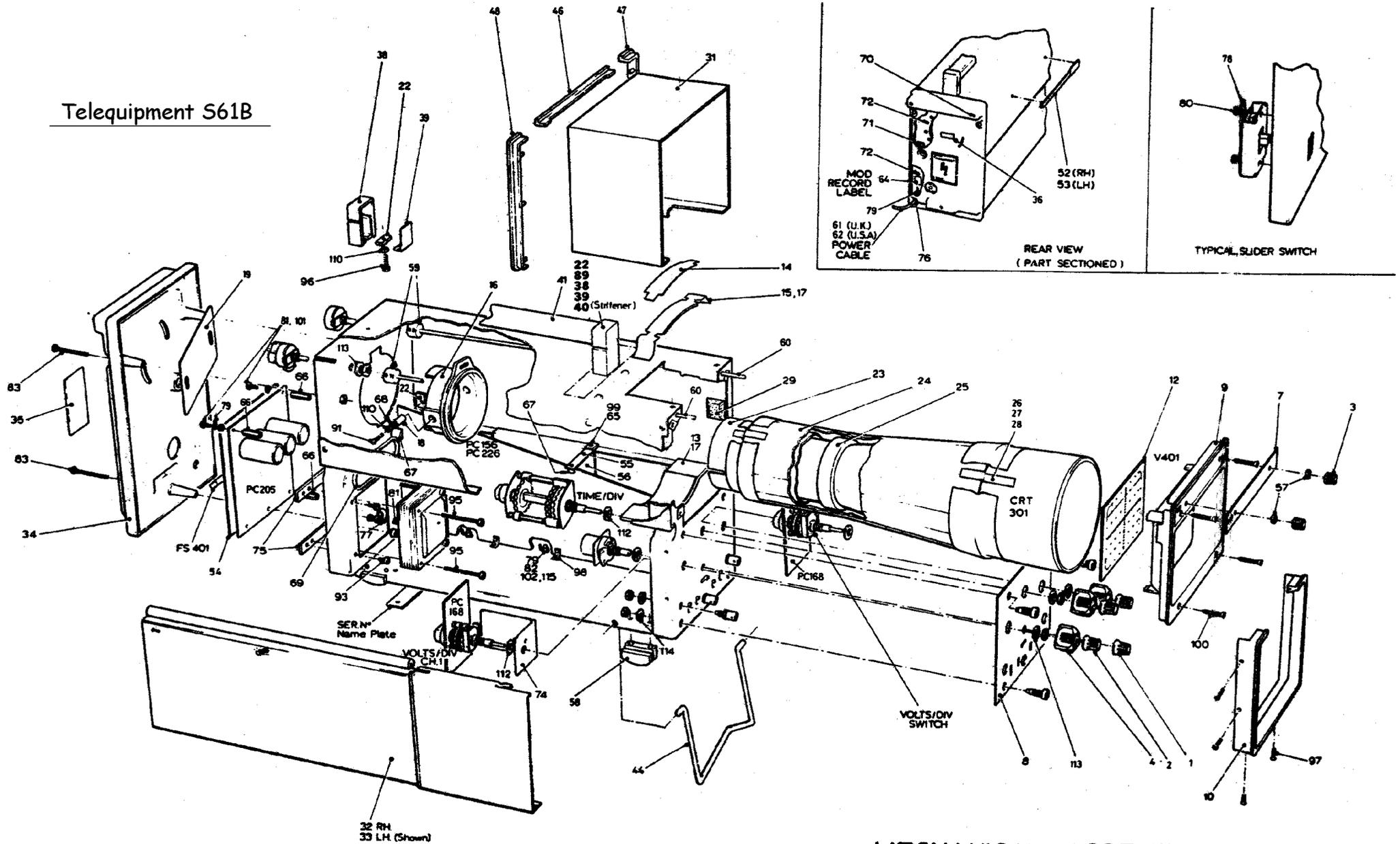
3.5 CRT CIRCUIT AND POWER SUPPLY

Figure 5

The supply voltage is fed via the power ON/OFF switch S401, the fuse FS401 and the voltage selector SK401 to the appropriate primary tapping on T401.

- 3.5.1 The -13 V line is obtained from a full wave rectifier circuit consisting of D405, D406, C402 and R403.
- 3.5.2 The +117 V line is obtained from a bridge rectifier D401, D402, D403 and D404 with reservoir capacitor C403 and smoothed by R406 and C408 to provide the +110 V line and by R407 and C406 to provide the +80 V line.
- 3.5.3 The -1050 V line is obtained from a half wave rectifier circuit consisting of D407, D409, C411 and C412.
- 3.5.4 The +2500 V line is obtained from a voltage doubler circuit consisting of D408, D409, C401 and C404.
- 3.5.5 The calibrator circuit provides a 0.5 V squarewave at supply frequency. It is produced by a diode switching circuit in which a current derived from the stabilized -7.5 V line is passed through D413 and R408 during negative half cycles of the AC voltage from the 13 V transformer winding. On positive half cycles D413 is cut off and the current passes through D412 and R402. The output voltage is set by R405. The temperature coefficient of the diode D413 is compensated for by the negative coefficient of the output resistor R408 giving an output voltage which is nominally independent of temperature.
- 3.5.6 The grid voltage of the CRT is derived from a zener diode connected between the CRT cathode and the -1050 V supply. The voltage is varied by the intensity control R301. The intensity of the trace may be varied by signals fed via the Z Mod socket SK301 and C301 to the grid of the CRT.

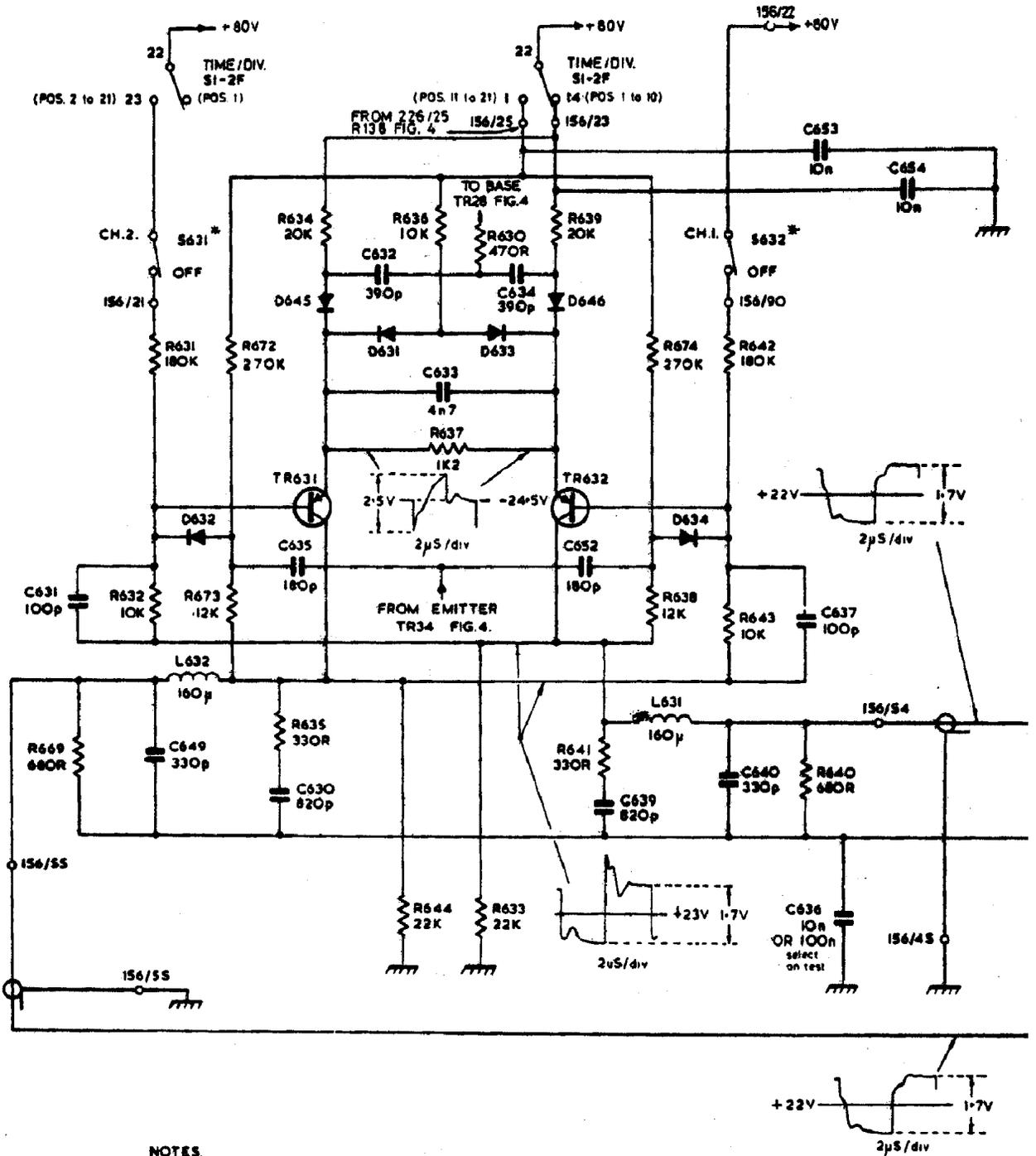
Telequipment S61B



MECHANICAL ASSEMBLY
D61A

RESISTORS	649	631 632	672 673	634	644	633 636 637	639	641	674 638	642 643	640
CAPACITORS	631	649	630	635	632	633	634	652 639	640	637	654
MISC.		L632 SI-2F S631 D632	TR631	D645 D631		D633 D646	TR632 SI-2F	L631 D634		S632	

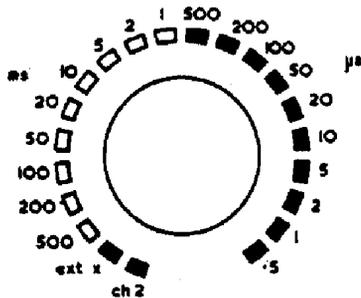
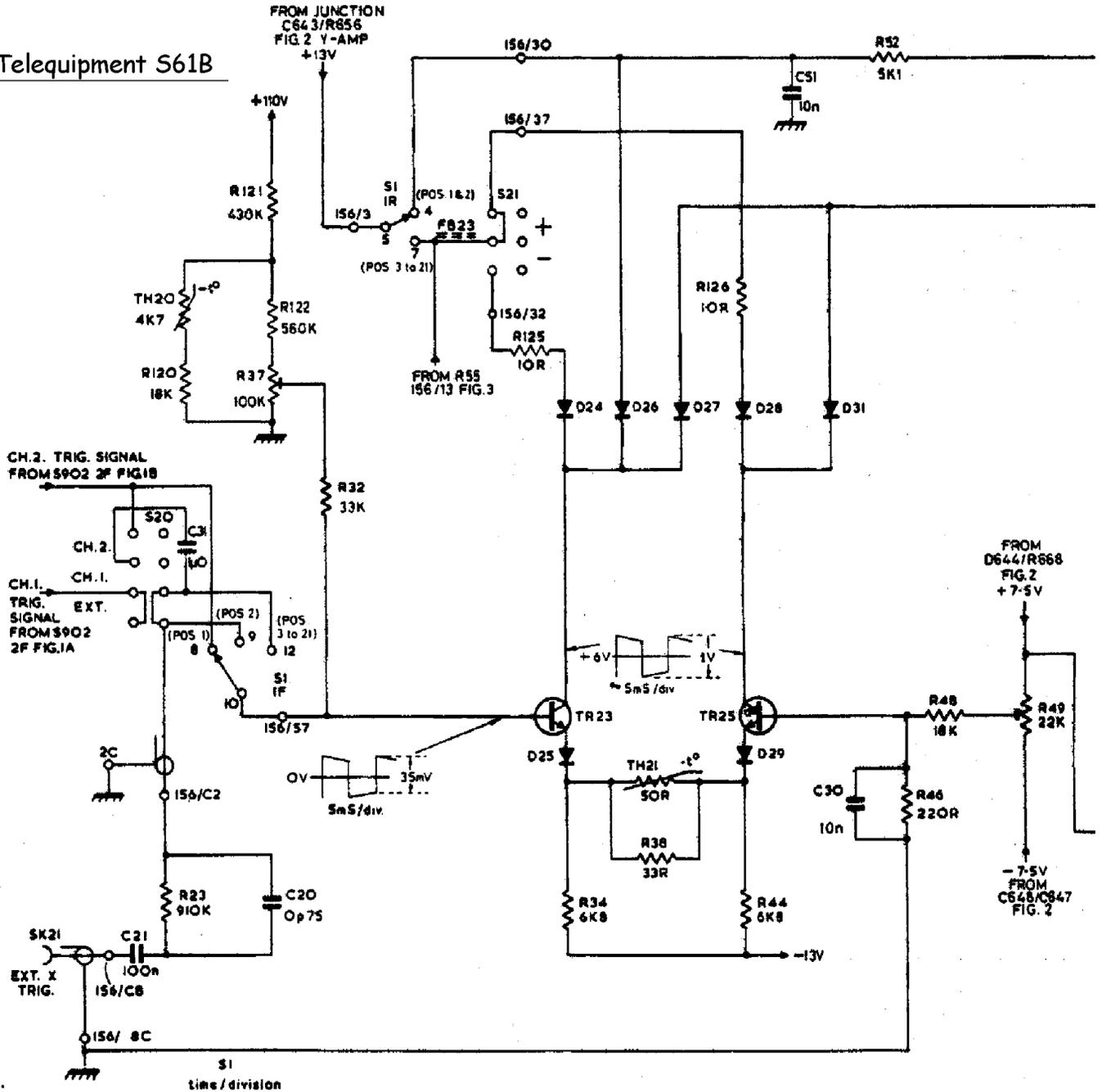
Telequipment S61B



- NOTES.
- * DENOTES COMPONENTS NOT MOUNTED ON P.C. BOARD.
 - 156/55 DENOTES P.C. BOARD/EYELET OR TERMINAL No. CONNECTION.
 - ALL WAVEFORMS MEASURED WITH CAL WAVEFORM FED TO BOTH CHANNELS VOLTS/DIV SET TO 0.1 TRIG SELECTOR TO CH.1 AND TIME/DIV TO 2ns.

RESISTORS	23	37	32	34	38	44	46	46	49
	120	121	122	123		126	52		
CAPACITORS			20				51		
	21	3						30	
MISC.	TH20			FB23	TR23	TH21	TR25		
	SK21	S20	SI-IF	SI-IR	D24	D26	D28	D29	D31
					D25	D27	D26	D29	

Telequipment S61B

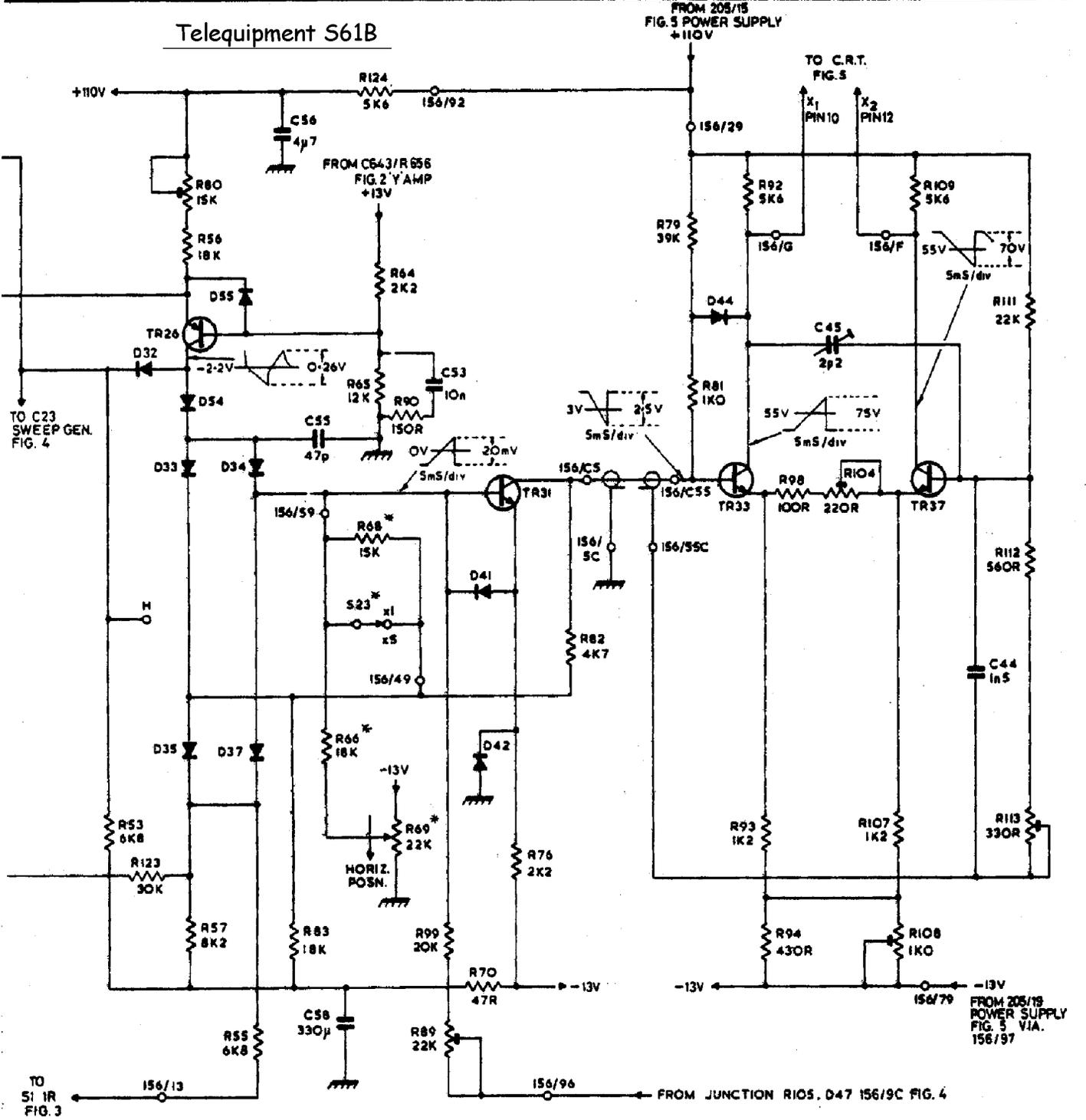


FRONT PANEL MARKING

NOTES.

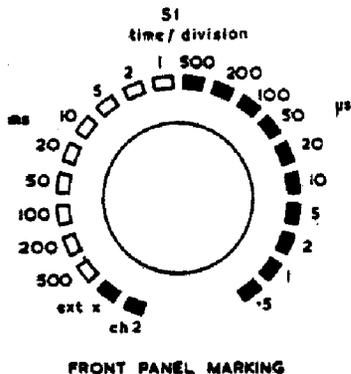
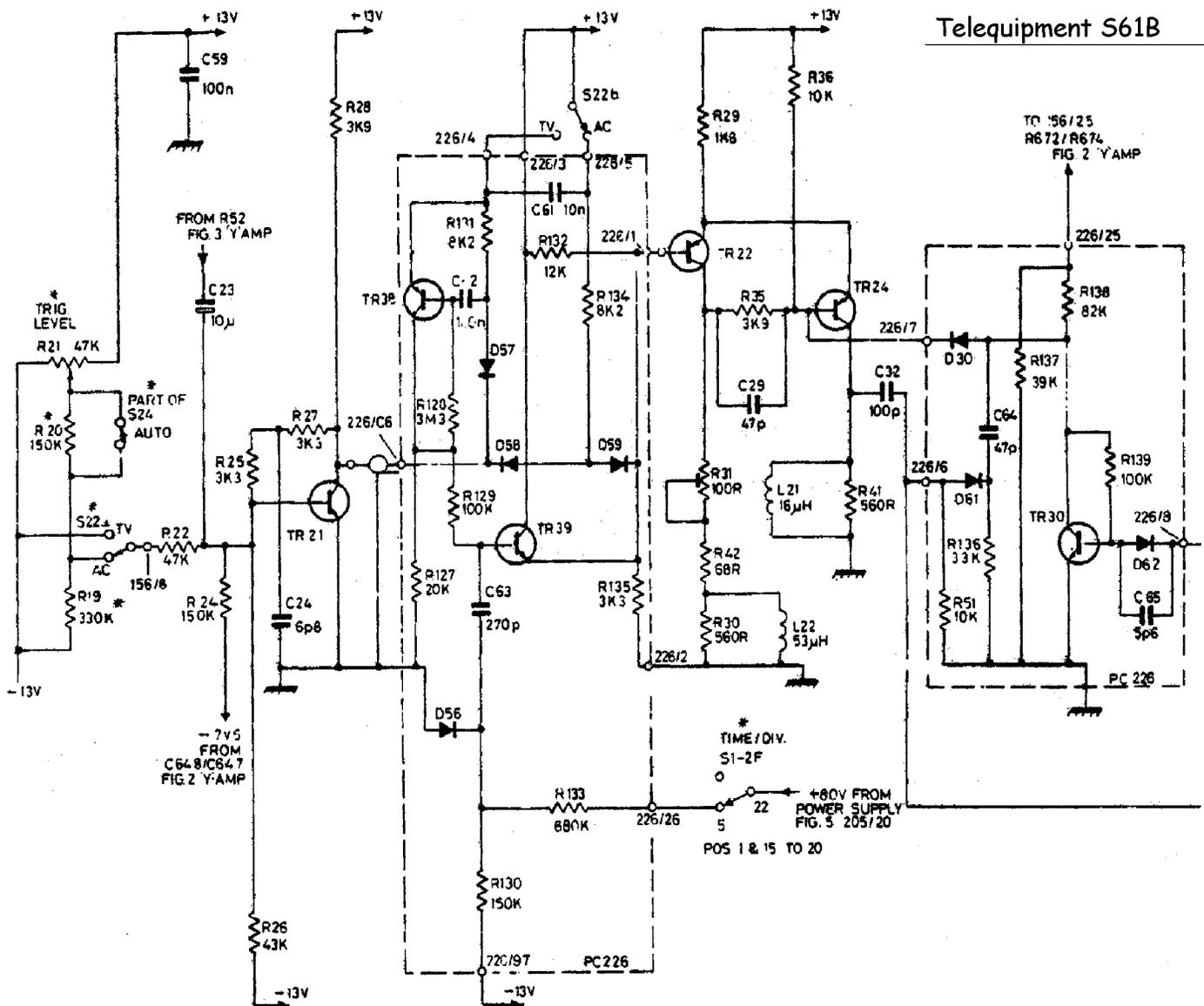
- * DENOTES COMPONENTS NOT MOUNTED ON P.C. BOARD.
- 156/C8 DENOTES P.C. BOARD/EYELET OR TERMINAL No. CONNECTION
- SWITCH SHOWN IN FULLY ANTI-CLOCKWISE POSITION.
- FOR S1 SWITCH WAFERS SEE FIG. 6.
- ALL WAVEFORMS MEASURED WITH CAL WAVEFORM FED TO BOTH CHANNELS VOLTS/DIV. TO CH1 TRIG SELECTOR TO CH1 AND TIME/DIV TO 2ns.

83	80	85	84	84	89	76	82	79	92	104	109	111
	86	83		84	89			81	93		107	112
	87			85	90	70			94	98	108	113
123		86		88	90							
		88		88	90					48		44
		88		88	90							
D32	TR26	D55		D41				TR33				TR37
	D54	D34		TR31				D44				
	D33	D37	S23	D42								
	D35											



RESISTORS	22	24	25	27	28	127	128	130	132	134	135	29	35	36	41	51	136	137	138	139
	21	19	28				129	131	133			31								
	20											42								
	30																			
CAPACITORS	59	24				62	63		61			29		32		64				65
	23																			
MISC.					TR21		TR 38	O57	TR 39	S22b	TR22	L21	TR24		D30	TR30	D62			
	S24						O56	O58	O59	S1-2F	L22				O61					
	S22a																			

Tequipment S61B

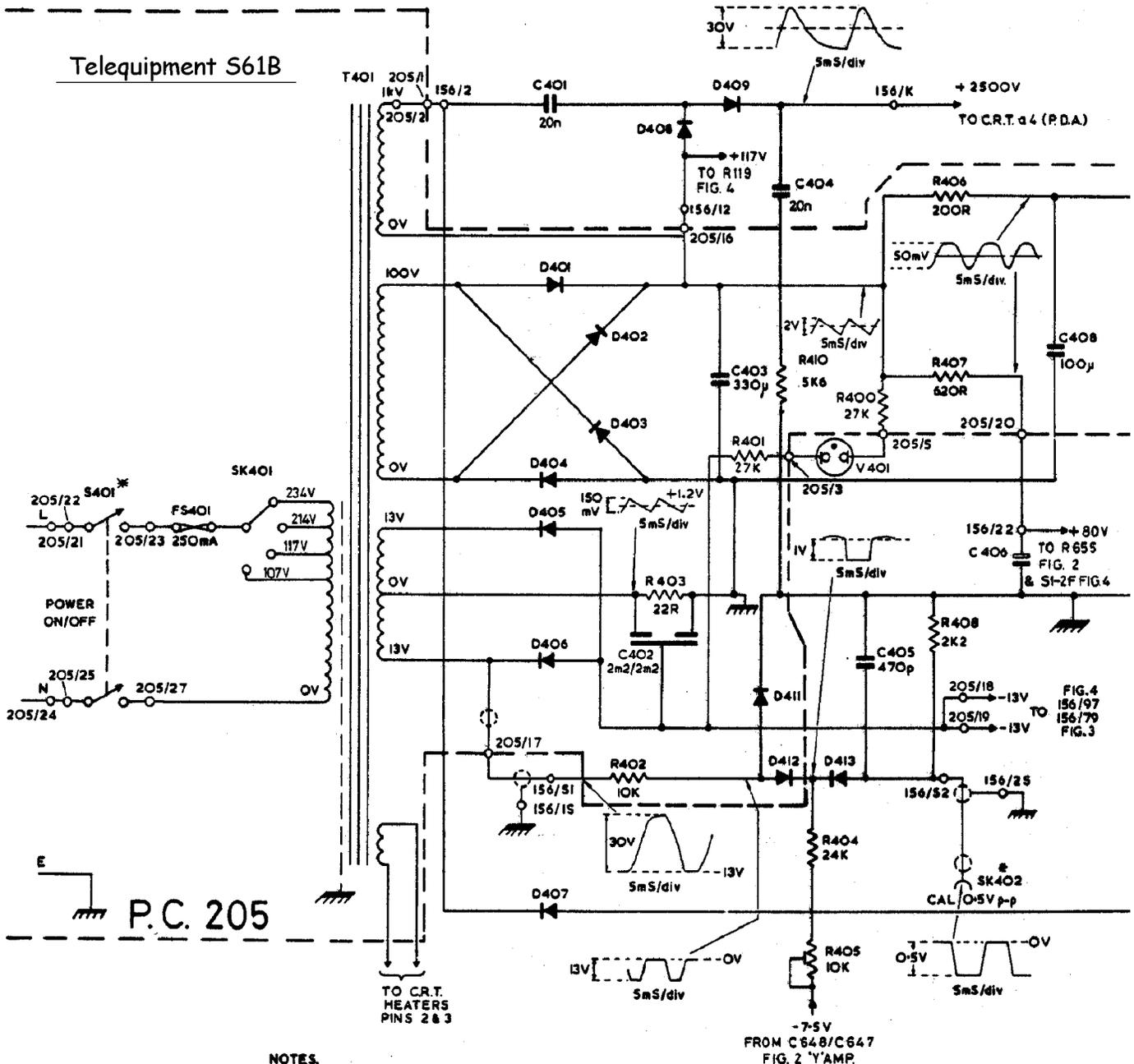


NOTES

- * DENOTES COMPONENTS NOT MOUNTED ON PC BOARD.
- 156/6C DENOTES PC BOARD/EYELET OR TERMINAL No. CONN.
- SWITCH SHOWN IN FULLY ANTI-CLOCKWISE POSITION.
- FOR S1 SWITCH WAFERS SEE FIG. 6.
- ALL WAVEFORMS MEASURED WITH CAL. WAVEFORM FED TO BOTH CHANNELS, VOLTS/DIV. SET TO 0.1, AND TRIG. SELECTOR TO CH.1. AND TIME/DIV. TO 2ns.
- PUSH PULL SWITCH S24 IS SHOWN IN THE OUT POSITION

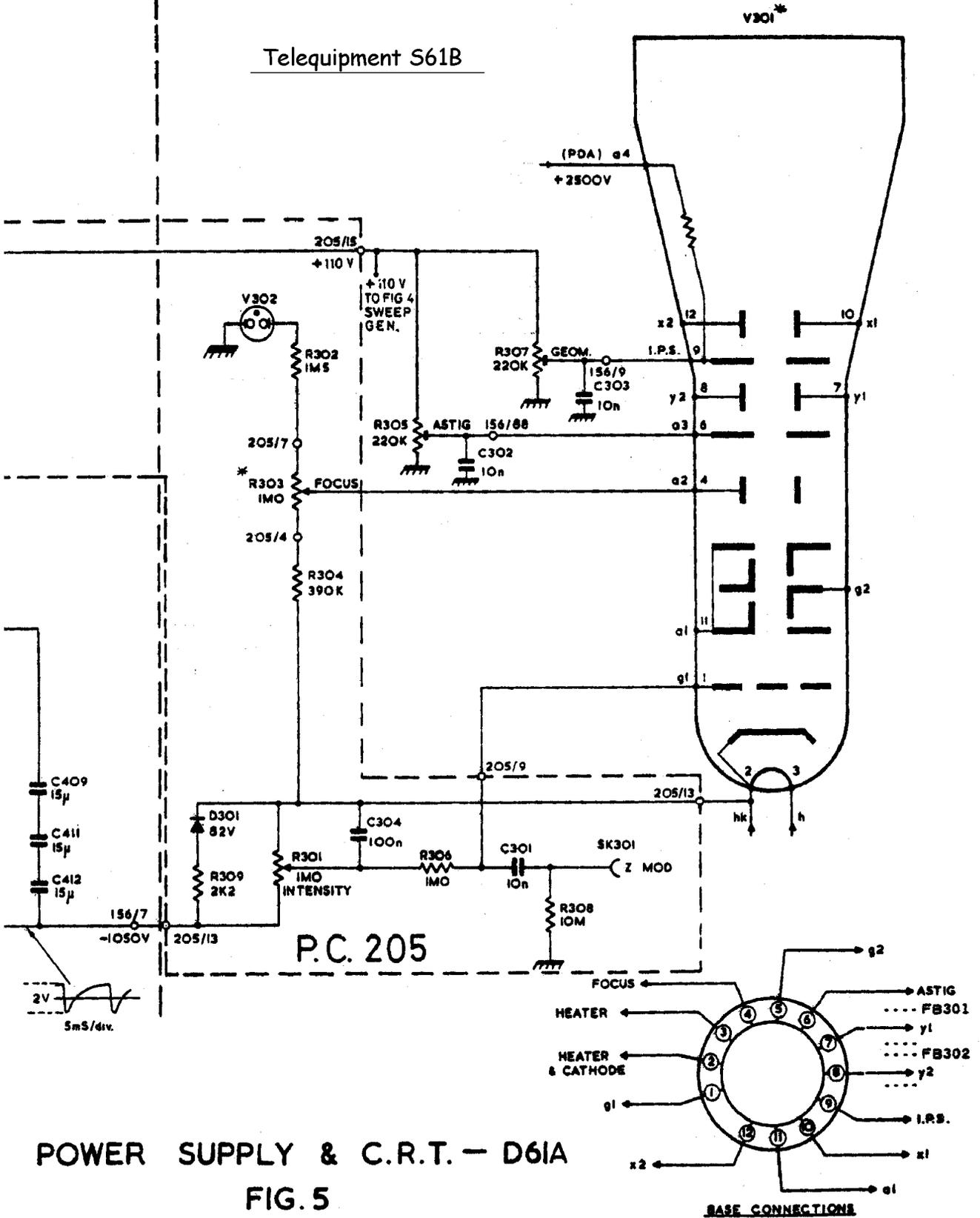
RESISTORS			402	403	404 405 410	400	406 407 408	
					401			
CAPACITORS			401	402	403	404	405 406 408	
MISC.		FS401	SK401	T401	D401 D404 D405 D406	D402 D403 D407	D408 D409 D411 D412	D413 V401 SK402
	S401							

Telequipment S61B



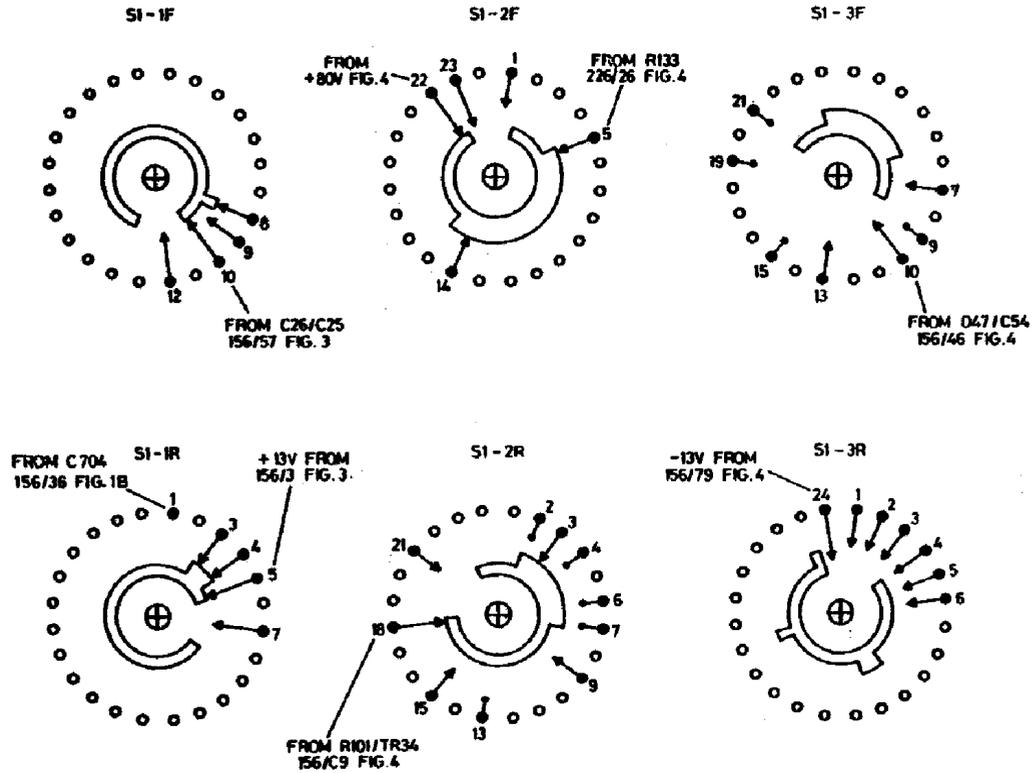
- NOTES.**
- * DENOTES COMPONENTS NOT MOUNTED ON P.C. BOARD.
 - 156/2 DENOTES P.C. BOARD/EYELET OR TERMINAL No. CONNECTION
 - ALL WAVEFORMS MEASURED WITH CAL. WAVEFORM FED TO BOTH CHANNELS, VOLTS/DIV. SET TO 0-1, AND TRIG. SELECTOR TO CH.1. AND TIME/DIV. TO 2MS.

	301 302 303 304	305 306	307 308
409 411 412	309	304	302 303 301
	D301 V302	SK301	V301 FB301 FB302



Telequipment S61B

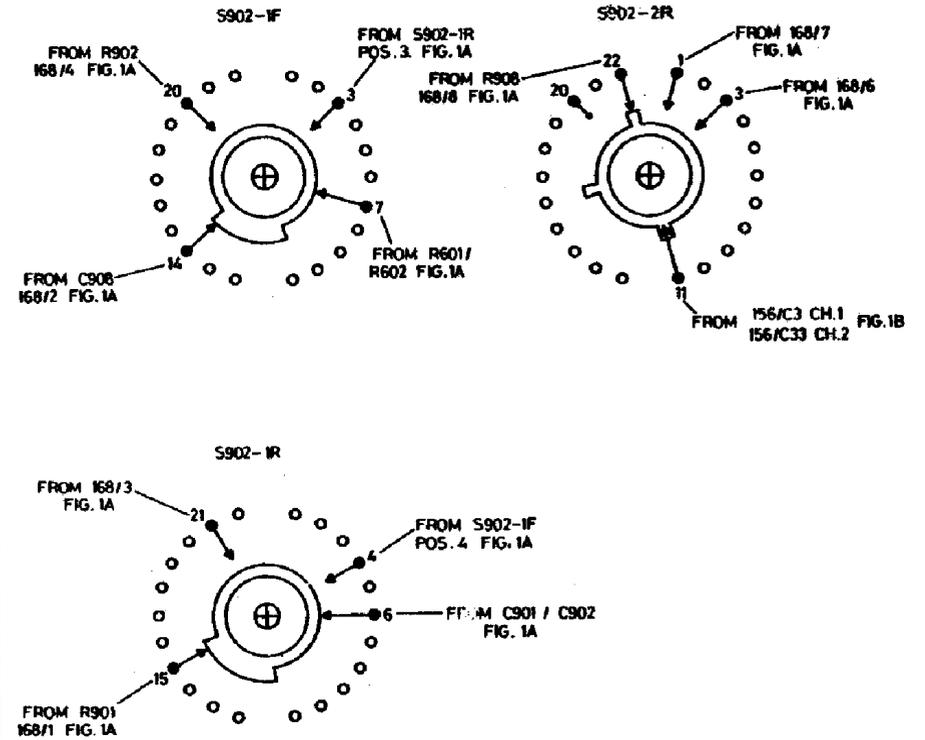
FOR RELATED CIRCUITS SEE FIG'S 3 & 4



- NOTES.
 1. SWITCH IS SHOWN IN FULLY ANTICLOCKWISE POSITION.
 2. No. OF POSITIONS 21.

TIME/DIV. SWITCH D61A

FOR RELATED CIRCUITS SEE FIG'S 1A & 1B



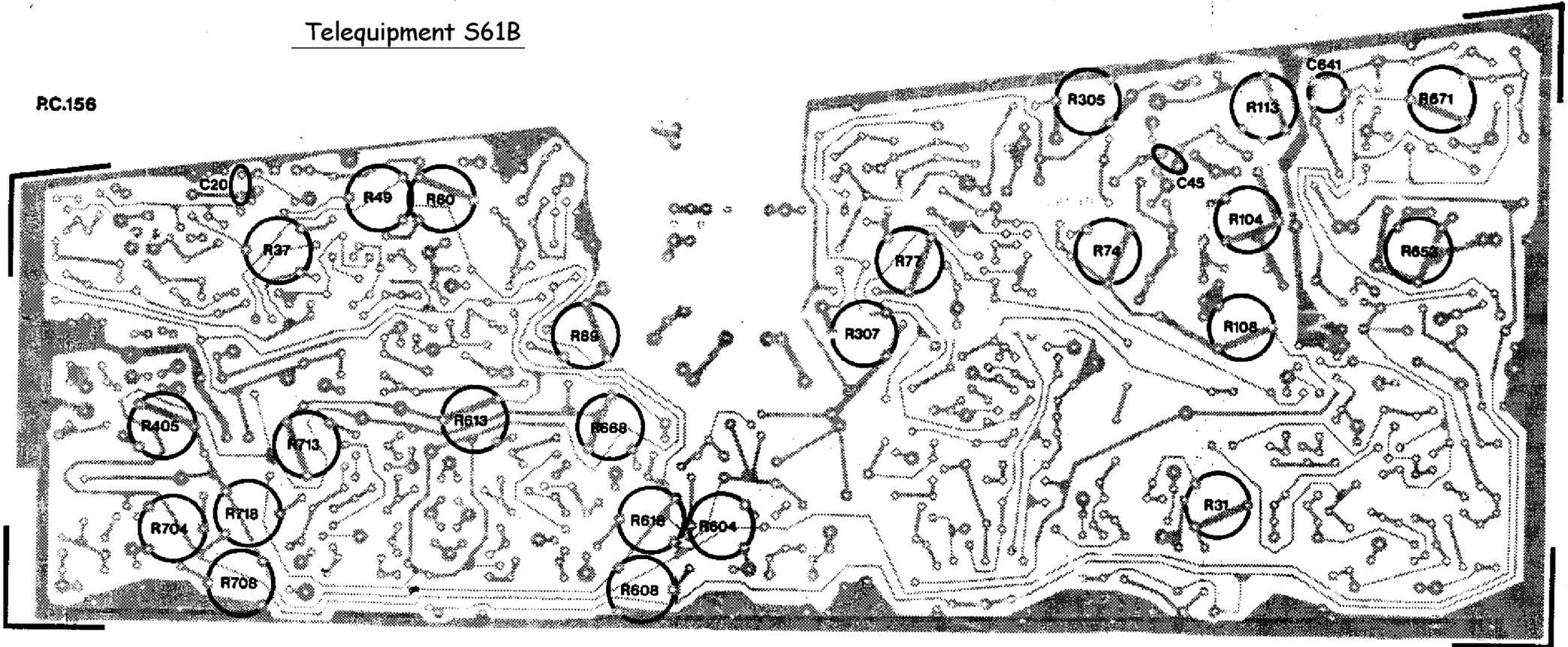
- NOTES.
 1. SWITCH IS SHOWN IN FULLY ANTICLOCKWISE POSITION.
 2. No. OF POSITIONS 9.

VOLTS/DIV. SWITCH-MK 2

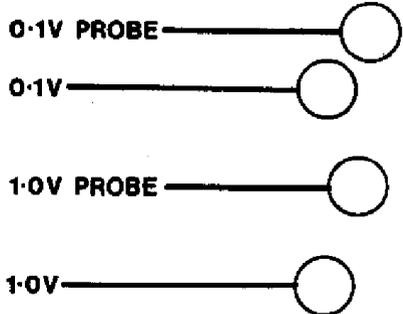
FIG. 6

Telequipment S61B

RC.156



CH1



ATTENUATOR TRIMMER POSITIONS

CH 2

