

PERDIO**“ MINI 66 ”****Model PR33**

General Description: Six-transistor (plus diode), M.W. plus one L.W. station pocket receiver with ferrite slab aerial and external aerial socket. Jack socket provided for earphone. Output 100 mW. Speaker impedance 80 ohms.

Power Supply: 9-volt battery (PP3, T6003, DT3). No-signal consumption 6–8 mA.

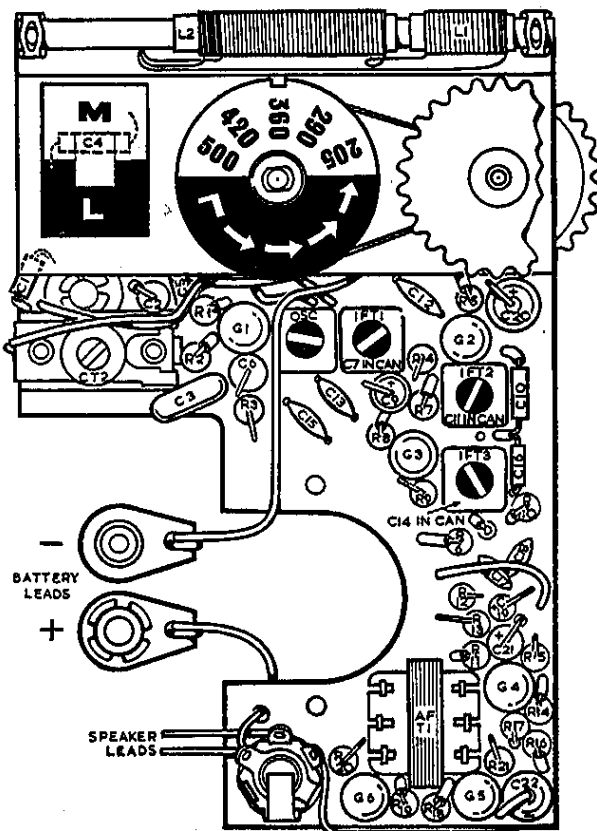
Wavebands: M.W. 185–555 m.; L.W. one pre-set wavelength (1500 m. in United Kingdom).

Transistor Analysis: Either Mullard or Impex transistors are fitted. (G1) OC44M or S50T (red spot); (G2) OC45M or S51T (yellow line); (G3) OC45M or S51T (yellow spot); (G4) OC81D or S10T (brown spot); (G5, G6) OC81 or S11T (orange spot). Diode: OA91.

Following approximate working voltages measured with set on M.W. and no signal input using 9-volt battery.

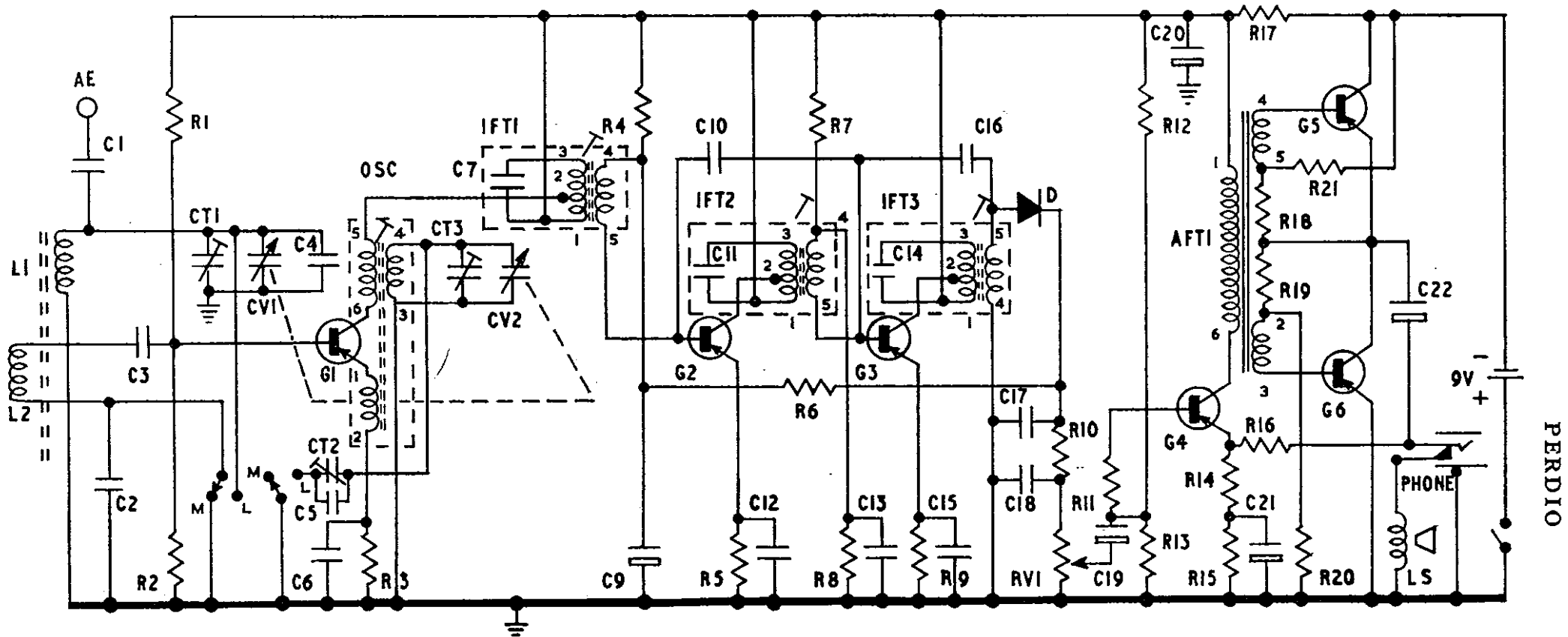
	G1	G2	G3	G4	G5	G6
Emitter	0.85	0.5	0.85	0.85	4.5	0
Base	0.8	0.6	0.95	0.9	4.6	0.15
Collector	6.7	6.7	6.7	6.0	9.0	4.5

Dismantling: Switch off. Place coin in insert at bottom of case and twist, then remove back section of case. Remove 8BA screw on front face. Slacken speaker clamp. Gently tilt printed board from case.



Alignment Summary: To align third I.F.T. remove self-tapping screw holding speaker at centre of printed board and swivel speaker away from I.F.T. I.F. 470 kc/s. (to mixer base via 0.1 μ F., top of R2 next to CT2 on component side). M.W. 540 kc/s. (oscillator coil); 1620 kc/s. (CT3); 600 kc/s. (L1/L2); 1300 kc/s. (CT1). L.W. inject required signal and adjust CT2.

COMPONENT LAY-OUT



CIRCUIT DIAGRAM—PERDIO "MINI 66" MODEL PR33

Capacitors.

C1	8 pF.
C2	1400 pF.
C3	0.047
C4	8 pF.
C5	180 pF.
C6	0.01
C7	200 pF.
C9	10 (16 v.)
C10	56* pF. or 96 pF.
C11	200 pF.
C12	0.047

C13	0.047
C14	200 pF.
C15	0.047
C16	18* pF. or 24 pF.
C17	0.01
C18	0.01
C19	5 (2.5 v.)
C20	80 (6.4 v.)
C21	32 (2.5 v.)
C22	64 (10 v.)
CT2	3-40 pF.

Resistors.

R1	56k
R2	10k
R3	2.7k
R4	68k
R5	680
R6	8.2k
R7	22k
R8	4.7k
R9	1k
R10	330
R11	1k
R12	47k
R13	10k
R14	15
R15	1k
R16	10k
R17	680
R18	100
R19	2.7k
R20	100
R21	2.7k
RV1	5k (semi-log.)

D.C. Resistances.

L1	5-1
L2	very low
Oscillator coil (Impex transistors)	
pins 3-4	3
1-2	very low
5-6	very low
IFT1, 2 (Impex transistors)	
pins 4-5	very low
1-2	1.6
2-3	4.6

IFT1, 2 (Mullard transistors)

pins 4-5	very low
1-2	1.25
2-3	4
IFT3	
pins 4-5	very low
1-2	1.2
2-3	5
AFT1	
pins 1-6	600
2-3	480
4-5	480
LS	80

*When Mullard transistors are fitted.