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PROPRIETARY NOTICE

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FOR YOUR SAFETY

Before undertaking any maintenance procedure, whether it be a specific troubleshooting or maintenance procedure described herein or an exploratory procedure aimed at determining whether there has been a malfunction, read the applicable section of this manual and note carefully the WARNING and CAUTION notices contained therein.

The equipment described in this manual contains voltages hazardous to human life and safety and which is capable of inflicting personal injury. The cautionary and warning notes are included in this manual to alert operator and maintenance personnel to the electrical hazards and thus prevent personal injury and damage to equipment.

If this instrument is to be powered from the AC Mains through an autotransformer (such as a Variac or equivalent) ensure that the instrument common connector is connected to the ground (earth) connection of the power mains.

Before operating the unit ensure that the protective conductor (green wire) is connected to the ground (earth) protective conductor of the power outlet. Do not defeat the protective feature of the third protective conductor in the power cord by using a two conductor extension cord or a three-prong/two-prong adapter.

Maintenance and calibration procedures contained in this manual sometimes call for operation of the unit with power applied and protective covers removed. Read the procedures carefully and heed Warnings to avoid "live" circuit points to ensure your personal safety.

Before operating this instrument.

- 1. Ensure that the instrument is configured to operate on the voltage available at the power source. See Installation section.
- 2. Ensure that the proper fuse is in place in the instrument for the power source on which the instrument is to be operated.
- 3. Ensure that all other devices connected to or in proximity to this instrument are properly grounded or connected to the protective third-wire earth ground.

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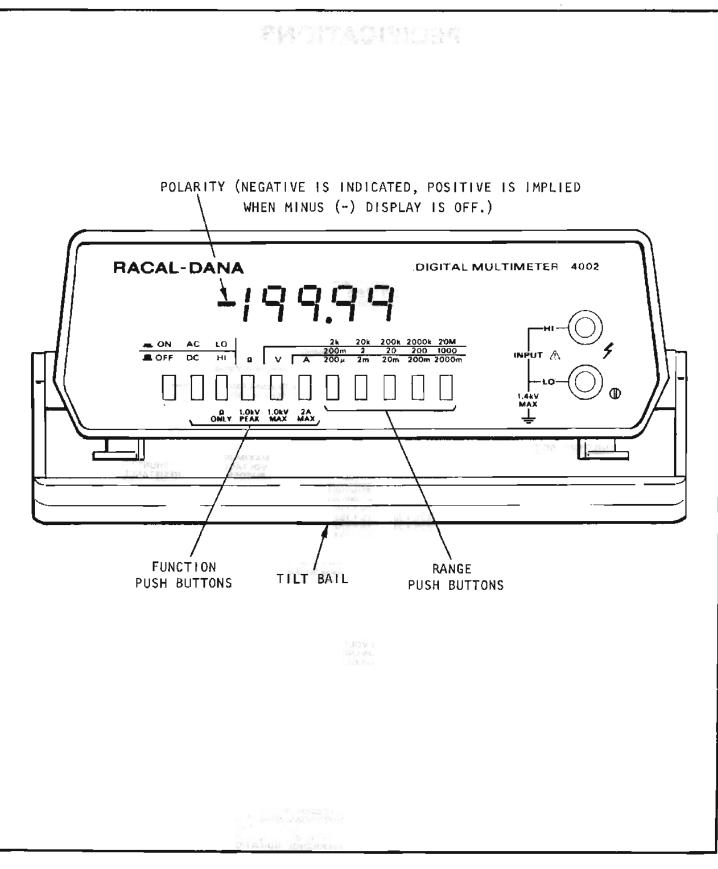
INSTRUCTION MANUAL

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ANGEREADING $\pm (\% rdg + digits)$ ONRANGE $\pm (\% rdg + digits)/^{O}C$ APPLIED CURRENHI Ω LO Ω HI Ω LO Ω HI Ω LO Ω HI Ω LO Ω HI Ω LO Ω 2 k Ω 1,99990.04% + 1d0.15% + 15d-0.2V-0.02% + 2d-100µA20 k Ω 199,990.04% + 1d0.15% + 15d2V0.2V0.003% + 0.2d0.02% + 2d10µA1µA200 k Ω 199,990.04% + 1d0.15% + 15d2V0.2V0.003% + 0.2d0.02% + 2d10µA1µA200 k Ω 199,990.04% + 1d0.15% + 15d2V0.2V0.003% + 0.2d0.02% + 2d10µA1µA200 k Ω 199,990.10% + 1d-2V-0.02 % + 0.2d0.1µA-200 k Ω 19,9990.10% + 1d-2V-0.02 % + 0.2d0.1µA-200 k Ω 19,9990.00% + 1d-2V-0.02 % + 0.2d0.1µA-200 k Ω 19,9990.00% + 0.2d0.00% + 0.2d0.02% <t< td=""><td>emperat DC 10</td><td>2mA 20mA 200mA 2000mA n Input: 2A, : ture Coefficien 20.01% + 0.2 di</td><td>1,999 19,99 199,99 199,9 1999,9 250V DC or r t (0^o~18^o and igits)/^oC.</td><td>9 (9 (9 (9 (9 (9 (9 (9 (9 (9 (</td><td>3.2% + 2d 3.2% + 2d 3.2% + 2d 3.2% + 2d 3.2% + 2d</td><td>1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog DC:</td><td>0.2 0.2 0.2 0.2 0.6 actor: 3 Settling Time: 1 second to withi</td><td>t ∨ t ∨ t5∨ t ∨ t v n 1 digit of fin</td><td>100 Ω 10 Ω 1 Ω 0,1 Ω</td><td>a.</td></t<>	emperat DC 10	2mA 20mA 200mA 2000mA n Input: 2A, : ture Coefficien 20.01% + 0.2 di	1,999 19,99 199,99 199,9 1999,9 250V DC or r t (0 ^o ~18 ^o and igits)/ ^o C.	9 (9 (9 (9 (9 (9 (9 (9 (9 (9 (3.2% + 2d 3.2% + 2d 3.2% + 2d 3.2% + 2d 3.2% + 2d	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog DC:	0.2 0.2 0.2 0.2 0.6 actor: 3 Settling Time: 1 second to withi	t ∨ t ∨ t5∨ t ∨ t v n 1 digit of fin	100 Ω 10 Ω 1 Ω 0,1 Ω	a.
$\frac{2 k \Omega}{20 k \Omega} = \frac{1.9999}{199.99} = \frac{0.15\% + 15d}{0.15\% + 15d} = \frac{0.27}{200 k \Omega} = \frac{0.02\% + 2d}{0.003\% + 0.2d} = \frac{0.02\% + 2d}{0.02\% + 2d} = \frac{100 \mu A}{10 \mu A} = \frac{100 \mu A}{1000 k \Omega} = \frac{199.99}{199.99} = \frac{0.04\% + 1d}{0.15\% + 15d} = \frac{27}{27} = \frac{0.27}{0.02\% + 0.2d} = \frac{0.02\% + 2d}{0.02\% + 2d} = \frac{100 \mu A}{10 \mu A} = \frac{1000 \mu A}{1000 k \Omega} = \frac{199.99}{0.10\% + 1d} = \frac{27}{27} = \frac{0.27}{0.02\% + 0.2d} = \frac{0.02\% + 2d}{0.03\% + 0.2d} = \frac{100 \mu A}{0.1 \mu A} = \frac{1000 \mu A}{200 \mu M} = \frac{1000 \mu M}{200 \mu M} = 1000 $	emperat DC -((AC <u>-</u> (2mA 200mA 200mA 2000mA n Input: 2A, : ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi	1,999 19,99 193,91 1999,1 250V DC or r t (0 ⁰ -18 ⁰ and (gits)/ ⁰ C, ts)/ ⁰ C.	9 9 1 9 1 9 1 9 1 1 9 1 1 9 1 1 9 1 1 9 1 1 9 1 9	0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d ected).	1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Anaiog OC: AC:	0.2 0.2 0.2 0.2 0.2 0.6 actor: 3 Settling Time: 1 second to withi 2.5 seconds to wi TEMPERATURE	V V SV V n 1 digit of fin thin 10 digits of COEFFICIEN	100 Ω 10 Ω 1 Ω 0,1 Ω ial reading. of final reading	-
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	emperat DC ∿(AC ≟(2mA 200mA 200mA 2000mA n Input: 2A, 3 sure Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM	1,999 19,99 19,99 1999, 1999, 250V DC or r t (0°-18° and gits)/°C, ts)/°C, ts)/°C,	99999999999999999999999999999999999999	0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d ected). MAXIMUN ACROSS	1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog DC: AC: 1 VOLTAGE UNKNOWN	0.2 0.2 0.2 0.2 0.2 0.2 0.6 actor: 3 Settling Time: 1 second to withi 2.5 seconds to wi TEMPERATURE 0°-18° and	v SV SV V n 1 digit of fin min 10 digits o COEFFICIENT 28 ⁰ -55 ⁰ C	100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM	INAL
$\begin{array}{llllllllllllllllllllllllllllllllllll$	ESIST	2mA 200mA 200mA 2000mA n Input: 2A, 3 sure Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM	1,999 19,99 19,99 1999, 1999, 250V DC or r t (0°-18° and grts)/°C. ts)/°C. ts)/°C. ts)/°C.	9 9 13 14 28 ⁰ -55 ⁰ C): 14 28 ⁰ -55 ⁰ C): 14 28 ⁰ C 14 digits]	MAXIMUM ACROSS ON	1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Anaiog DC: AC: NCNUN ANGE	TEMPERATURE 0°-18° and + 1% rdg +	v v SV V v v v v v v v v v v v v v v v v	100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM APPLIED	
$\begin{array}{llllllllllllllllllllllllllllllllllll$	emperat DC ((AC <u>(</u>) ESIST,	2mA 200mA 200mA 2000mA n Input: 2A, 3 ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM READING	1,999 19,99 19,99 1999, 1999, 250V DC or r t (0°-18° and gits)/°C, (s)/°C, (s)/°C, (s)/°C, (s)/°C, (s)/°C, (s)/°C,	-28 ⁰ C + digits) 0,15% + 15c	MAXIMUN ACROSS ON F <u>HL Ω</u>	1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog DC: AC: 1 VOL TAGE UNKNOWN KANGE LO Ω 0.2V	$\frac{0.2}{0.2}$ actor: 3 Settling Time: 1 second to within 2.5 seconds to within $\frac{\text{TEMPE RATURE}}{0^{9}-18^{9}}$ and $\frac{+(\% rdg + \frac{HL\Omega}{T})}{T}$	$COEFFICIENT28°-55°Cdigits)/°CLO \Omega0.02% + 2d$	100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM APPLIED <u>HIΩ</u> —	11NAL CURREN <u>LOΩ</u> 100μΑ
aximum Allowable Input: 450V rms sustained, 1kV DC or peak AC momentary, aximum Open-Circuit Voltage: 5 volts. Settling Time: 1 second to within 1 digit of final reading except 2 seconds on the 20MΩ range. ENERAL ISPLAY: Five 0.5" LED digits, appropriate decimal position on version. DNVERSION PERIOD: 400 milliseconds. NVIRONMENT: Depariting: 0°C ro 55°C; 0% to 80% relative humidity up to 40°C. POWER: 105 125 or 210-250 volts (switch selected), 90-110V available. 50-60Hz, 7 watts. Optional 6 hour battery pack, (Option D6). DIMENSIONS, WEIGHT: B5mm high x 235mm wide x 275mm deep (3-1/2 in, x 9-1/4 in, x 10-3/4 jn.). Net weight: 1,7kg, (3 lbs., 13 oz). 0% to 80% relative humidity up to 40°C.	emperat DC ((AC (EESIST) ANGE 2 k Ω 20 k Ω 20 k Ω	2mA 200mA 200mA 2000mA n Input: 2A, 3 ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM READING 1,9999 19.999 19.999	1,999 19,99 199,9 1999,	9 9 10 9 112 months) 28 ⁰ -55 ⁰ C): 128 ⁰ -55	MAXIMUM ACROSS ON F HI Ω 1 - 2V 1 2V	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Anaiog OC: AC: AC: UNKNOWN KANGE LO <u>Ω</u> 0.2V 0.2V	$\frac{0.2}{0.2}$ actor: 3 Settling Time: 1 second to withi 2.5 seconds to wi $\frac{\text{TEMPERATURE}}{0^{\circ}-18^{\circ} \text{ and}}$ $\frac{\pm (\% \text{ rdg} + \frac{\text{HI} \Omega}{-18^{\circ}})$ 0.003% + 0.2d	2 V 2 V 2 V 2 V 2 V 2 V 3 V n 1 digit of fin thin 10 digits of COEFFICIEN 28°-55°C digits)/°C LOΩ 0.02% + 2d 0.02% + 2d	100 Ω 10 Ω 1 Ω 0.1 Ω hat reading. of final reading T NOM APPLIED - 100μA	11NAL CURREN <u>LOΩ</u> 100μΑ 10μΑ
ENERAL POWER: 105 125 or 210-250 volts (switch selected), 90-110V ISPLAY: Five 0.5" LED digits, appropriate decimal position and polarity indication. POWER: 105 125 or 210-250 volts (switch selected), 90-110V and polarity indication. Option 06). DNVERSION PERIOD: 400 milliseconds. Dimensions, WEIGHT: B5mm high x 235mm wide x 275mm deep (3-1/2 in, x 9-1/4 in, x 10-3/4 in.). NVERSION PERIOD: 0°C ro 55°C; OVERRANGE INDICATION: Display blinks all zeros above 1999 counts.	emperat DC ((AC (EESIST, 20 kΩ 20 kΩ 200 kΩ 900 kΩ	2mA 200mA 200mA 2000mA n Input: 2A, 3 ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM READING 1,9999 19,999 1999.9	1,999 19,99 199,9 1999, 250V DC or r t (0°-18° and gits)/°C. (s)/°C. (s)/°C. ±(% rdg <u>+I(%</u> rdg <u>+I(%</u> rdg 0.04% + 1d 0.04% + 1d	9 9 10 9 112 months) 28 ⁰ -55 ⁰ C): 128 ⁰ -55	MAXIMUN ACROSS ON F HL Ω 1	1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Anaiog DC: AC: NVOL TAGE UNKNOWN RANGE LO Ω 0.2V 0.2V 0.2V	$\frac{0.2}{0.2}$ actor: 3 Settling Time: 1 second to within 2.5 seconds to within $\frac{\text{TEMPE RATURE}}{0^{\circ}-18^{\circ}}$ and $\frac{\pm (\% \text{ rdg } + \frac{11 \Omega}{0.003\% + 0.2d})$ 0.003% + 0.2d 0.003% + 0.2d	2 V 2 V 2 V 2 V 2 V 2 V 3 V n 1 digit of fin thin 10 digits of COEFFICIEN 28°-55°C digits)/°C LOΩ 0.02% + 2d 0.02% + 2d	100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM APPLIED <u>HIΩ</u> - 100μA 10μA	100 <u>LOΩ</u> 100 100 100 10 10 10 10 10 10
ISPLAY: Five 0.5" LED digits, appropriate decimal position and polarity indication. ONVERSION PERIOD: 400 milliseconds. VVIRONMENT: Departing: 0°C to 55°C; 0% to 80% relative humidity up to 40°C.	emperat DC (AC (ESIST) 20 kΩ 20 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ	2mA 200mA 200mA 2000mA n Input: 2A, 3 ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM READING 1,9999 19.999 19.999 19.999 19.999 19.999 19.999 Allowable Inp rms sustained,	$\begin{array}{c} 1.999:\\ 19.99:\\ 19.99:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 100^{-1}\\ 0.0^{-1}\\ C.\\ \\ ACCURACY\\ 18^{0}-\\ (10^{-1})^{-2}\\ C.\\ \\ ACCURACY\\ 18^{0}-\\ 18^{0}-\\ 18^{0}-\\ 10^{-1}\\ 0.04\% + 10\\$	$\frac{12}{28^{\circ}-55^{\circ}C}$ $\frac{12}{28^{\circ}-55^{\circ}C}$ $\frac{12}{28^{\circ}C}$ $\frac{12}{12}$ $\frac{10}{12}$	MAXIMUM ACROSS ON F HLΩ t 2V t 2V t 2V t 2V t 2V t 2V	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog DC: AC: 1 VOL TAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V	$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.6\\ 0.2\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6$	COEFFICIENT 28°-55°C digits)/°C LO Ω 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% + 2d 0.03% + 2d	100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM APPLIED <u>HIΩ</u> 100μA 10μA 1μA 0,1μA	11NAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ -
ISPLAY: Five 0.5" LED digits, appropriate decimal position and polarity indication. ONVERSION PERIOD: 400 milliseconds. VVIRONMENT: Departing: 0°C to 55°C; 0% to 80% relative humidity up to 40°C.	emperat DC (AC (ESIST) 20 kΩ 20 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ	2mA 200mA 200mA 2000mA n Input: 2A, 3 ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM READING 1,9999 19.999 19.999 19.999 19.999 19.999 19.999 Allowable Inp rms sustained,	$\begin{array}{c} 1.999:\\ 19.99:\\ 19.99:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 100^{-1}\\ 0.0^{-1}\\ C.\\ \\ ACCURACY\\ 18^{0}-\\ (10^{-1})^{-2}\\ C.\\ \\ ACCURACY\\ 18^{0}-\\ 18^{0}-\\ 18^{0}-\\ 10^{-1}\\ 0.04\% + 10\\$	$\frac{12}{28^{\circ}-55^{\circ}C}$ $\frac{12}{28^{\circ}-55^{\circ}C}$ $\frac{12}{28^{\circ}C}$ $\frac{12}{12}$ $\frac{10}{12}$	MAXIMUM ACROSS ON F HLΩ t 2V t 2V t 2V t 2V t 2V t 2V	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d 0Crest F Analog OC: AC: 1 VOL TAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V	$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.6\\ 0.2\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6\\ 0.6$	COEFFICIENT 28°-55°C digits)/°C LO Ω 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% + 2d 0.03% + 2d	100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM APPLIED <u>HIΩ</u> 100μA 10μA 1μA 0,1μA	11NAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ -
and polarity indication. DIMENSIONS, WEIGHT: 85mm high x 235mm wide x 275mm deep (3-1/2 in, x 9-1/4 in, x 10-3/4 in.). ONVERSION PERIOD: 400 milliseconds. Deep (3-1/2 in, x 9-1/4 in, x 10-3/4 in.). NVIRONMENT: Net weight: 1,7kg. (3 lbs., 13 oz). Operating: 0°C to 55°C; OVERRANGE INDICATION: Display blinks all zeros above 0% to 80% relative humidity up to 40°C. 19999 counts.	emperat DC {(AC _(EESIST, ANGE 20 kΩ 200 kΩ 2000 kΩ 2000 kΩ 2000 kΩ 2000 kΩ 2000 kΩ	2mA 20mA 200mA 200mA n Input: 2A, : ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM READING 1,9999 19,999 19,999 19,999 19,999 19,999 19,999 19,999 19,999	$\begin{array}{c} 1.999:\\ 19.99:\\ 19.99:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 1999:\\ 100^{-1}\\ 0.0^{-1}\\ C.\\ \\ ACCURACY\\ 18^{0}-\\ (10^{-1})^{-2}\\ C.\\ \\ ACCURACY\\ 18^{0}-\\ 18^{0}-\\ 18^{0}-\\ 10^{-1}\\ 0.04\% + 10\\$	$\frac{12}{28^{\circ}-55^{\circ}C}$ $\frac{12}{28^{\circ}-55^{\circ}C}$ $\frac{12}{28^{\circ}C}$ $\frac{12}{12}$ $\frac{10}{12}$	MAXIMUM ACROSS ON F HLΩ t 2V t 2V t 2V t 2V t 2V t 2V	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d 0Crest F Analog OC: AC: AC: NOULTAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V	$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$	COEFFICIENT 28°-55°C digits)/°C COEFFICIENT 28°-55°C digits)/°C <u>LO Ω</u> 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% + 2d	100 Ω 10 Ω 1 Ω 0.1 Ω hat reading. of final reading T NOM APPLIED HIΩ - 10μA 10μA 10μA 0.1μA of final reading	11NAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ
DNVERSION PERIOD: 400 milliseconds. VVIRONMENT: Derating: 0°C to 55°C; 0% to 80% relative humidity up to 40°C. 0% to 80% relative humidity up to 40°C.	emperation DC ((AC () AC () BESIST, ANGE 2kΩ 20 kΩ 200 kΩ Baximum ENER/	2mA 200mA 200mA 2000mA n Input: 2A.: ture Coefficien 0.01% + 0.2 di 0.07% + 2 digit ANCE MAXIMUM READING 1,9999 19,999 1999,99 1990,99 1900,99 1000,000,0000,000000000000000000000000	1.999 19,99 1999, 1999, 250V DC or r t (0°-18° and gits)/°C. ts)/°C. ts)/°C. +(% rdg <u>+11 Ω</u> 0.04% + 1d 0.04% + 1d 0.04% + 1d 0.04% + 1d 0.04% + 1d 0.04% + 1d 0.04% + 1d	9 9 10 9 112 months) 28 ⁰ C + digits] <u>LO Ω</u> 0.15% + 15c 0.15% + 15c 0.15% + 15c 0.15% + 15c 0.15% + 15c 0.15% + 15c	0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d b.2% + 2d D.2% + 2d b.2% + 2d b.2% + 2d b.1 Ω d <trr> d<!--</td--><td>1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog DC: AC: 1 VOLTAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V</td><td>$\frac{0.2}{0.2}$ actor: 3 Settling Time: 1 second to withi 2.5 seconds to withi $\frac{\text{TEMPE RATURE}}{0^{\circ}-18^{\circ}} \text{ and}$ $\frac{\pm (\% \text{ rdg +})}{1.\% \text{ rdg +}}$ $\frac{\text{HI }\Omega}{0.003\% + 0.2d}$ 0.003% + 0.2d 0.02 % + 0.2d 0.02 % + 0.2d Time: 1 second to conds on the 20MΩ $\frac{105}{20} + 125 \text{ or } 210$</td><td>2 V 2 V 2 V 2 V 2 V 2 V 2 V 2 V</td><td>100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM APPLIED HIΩ 10μΑ</td><td>1004 <u>LOΩ</u> 100μΑ 10μΑ 10μΑ 10μΑ 10μΑ 0.1μΑ σ except 0-110V</td></trr>	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog DC: AC: 1 VOLTAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V	$\frac{0.2}{0.2}$ actor: 3 Settling Time: 1 second to withi 2.5 seconds to withi $\frac{\text{TEMPE RATURE}}{0^{\circ}-18^{\circ}} \text{ and}$ $\frac{\pm (\% \text{ rdg +})}{1.\% \text{ rdg +}}$ $\frac{\text{HI }\Omega}{0.003\% + 0.2d}$ 0.003% + 0.2d 0.02 % + 0.2d 0.02 % + 0.2d Time: 1 second to conds on the 20MΩ $\frac{105}{20} + 125 \text{ or } 210$	2 V 2 V 2 V 2 V 2 V 2 V 2 V 2 V	100 Ω 10 Ω 1 Ω 0,1 Ω nat reading. of final reading T NOM APPLIED HIΩ 10μΑ	1004 <u>LOΩ</u> 100μΑ 10μΑ 10μΑ 10μΑ 10μΑ 0.1μΑ σ except 0-110V
Deparating: 0°C to 55°C; OVERRANGE INDICATION: Display blinks all zeros above 0% to 80% relative humidity up to 40°C. 19999 counts.	emperation DC ((AC () AC () ESIST. ANGE 2 k Ω 20 k Ω 200 k Ω 18ximum ENER/ ISPLAY and pc	2mA 20mA 200mA 200mA n Input: 2A, : ture Coefficien 0.01% + 0.2 di 0.07% + 2 digit ANCE MAXIMUM READING 1,9999 19.999 19.999 19.999 19.999 19.999 19.999 Allowable Inp rms sustained, 0 Open-Circuit AL C Five 0.5" LE plarity indication	1,999 19,99 19,99 1999, 250V DC or r t (0°-18° and g(ts)/°C. ts)/°C. ACCURACY 18°- ±(% r0g <u>HIΩ</u> 0.04% + 1d 0.04% + 1d 0.	9 1 9 1 9 1 9 1 1 28°-55°C): 1 28°C + digits] 1 0.15% + 15c 9 10 10 11 12 15% + 15c 15% + 15c </td <td>0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d b.2% + 2d b.1 Ω d <trr> d<!--</td--><td>1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d 0Crest F Analog OC: AC: 1 VOL TAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V</td><td>$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$</td><td>r 1 digit of fin thin 10 digits of $COEFFICIENT28^{\circ}-55^{\circ}C$ digits)/$^{\circ}C$ $LO \Omega$ 0.02% + 2d 0.02% + 2d 0.03% +</td><td>100 Ω 10 Ω 1 Ω 0.1 Ω hal reading. of final reading T NOM APPLIED <u>HIΩ</u> 10μA 10μA 0.1μA of final reading ch selected), 9 5 hour battery</td><td>UNAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ </td></trr></td>	0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d b.2% + 2d b.1 Ω d <trr> d<!--</td--><td>1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d 0Crest F Analog OC: AC: 1 VOL TAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V</td><td>$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$</td><td>r 1 digit of fin thin 10 digits of $COEFFICIENT28^{\circ}-55^{\circ}C$ digits)/$^{\circ}C$ $LO \Omega$ 0.02% + 2d 0.02% + 2d 0.03% +</td><td>100 Ω 10 Ω 1 Ω 0.1 Ω hal reading. of final reading T NOM APPLIED <u>HIΩ</u> 10μA 10μA 0.1μA of final reading ch selected), 9 5 hour battery</td><td>UNAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ </td></trr>	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d 0Crest F Analog OC: AC: 1 VOL TAGE UNKNOWN KANGE LO Ω 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V 0.2V	$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$	r 1 digit of fin thin 10 digits of $COEFFICIENT28^{\circ}-55^{\circ}C$ digits)/ $^{\circ}C$ $LO \Omega$ 0.02% + 2d 0.02% + 2d 0.03% +	100 Ω 10 Ω 1 Ω 0.1 Ω hal reading. of final reading T NOM APPLIED <u>HIΩ</u> 10μA 10μA 0.1μA of final reading ch selected), 9 5 hour battery	UNAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ
0% to 80% relative humidity up to 40°C. 19999 counts.	ESIST. 2 kΩ 20 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 1000 kΩ 100	2imA 200mA 200mA 2000mA n Input: 2A, : ture Coefficien 0.01% + 0.2 di 0.07% + 2 digi ANCE MAXIMUM READING 1,9999 19,999 19,999 19,999 19,999 19,999 19,999 19,999 19,999 Allowable Inp rms sustained, 0 pen-Circuit AL	1,999 19,99 19,99 1999, 250V DC or r t (0°-18° and g(ts)/°C. ts)/°C. ACCURACY 18°- ±(% r0g <u>HIΩ</u> 0.04% + 1d 0.04% + 1d 0.	9 1 9 1 9 1 9 1 1 28°-55°C): 1 28°C + digits] 1 0.15% + 15c 9 10 10 11 12 15% + 15c 15% + 15c </td <td>0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d b.2% + 2d b.1 Ω d <trr> d<!--</td--><td>1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog 0C: AC: ANGE LO Ω 0.2V 0.2V<!--</td--><td>$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$</td><td>r 1 digit of fin thin 10 digits of COE FFICIENT 28°-55°C digits)/°C LO Ω 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% +</td><td>100 Ω 10 Ω 1 Ω 0,1 Ω 1 Ω 0,1 Ω at reading. of final reading T NOM APPLIED HIΩ 10μΑ</td><td>UNAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ </td></td></trr></td>	0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d b.2% + 2d b.1 Ω d <trr> d<!--</td--><td>1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog 0C: AC: ANGE LO Ω 0.2V 0.2V<!--</td--><td>$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$</td><td>r 1 digit of fin thin 10 digits of COE FFICIENT 28°-55°C digits)/°C LO Ω 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% +</td><td>100 Ω 10 Ω 1 Ω 0,1 Ω 1 Ω 0,1 Ω at reading. of final reading T NOM APPLIED HIΩ 10μΑ</td><td>UNAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ </td></td></trr>	1% + 15d 1% + 15d 1% + 15d 1% + 15d Crest F Analog 0C: AC: ANGE LO Ω 0.2V 0.2V </td <td>$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$</td> <td>r 1 digit of fin thin 10 digits of COE FFICIENT 28°-55°C digits)/°C LO Ω 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% +</td> <td>100 Ω 10 Ω 1 Ω 0,1 Ω 1 Ω 0,1 Ω at reading. of final reading T NOM APPLIED HIΩ 10μΑ</td> <td>UNAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ </td>	$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$	r 1 digit of fin thin 10 digits of COE FFICIENT 28°-55°C digits)/°C LO Ω 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% +	100 Ω 10 Ω 1 Ω 0,1 Ω 1 Ω 0,1 Ω at reading. of final reading T NOM APPLIED HIΩ 10μΑ	UNAL CURREN 100μΑ 10μΑ 1μΑ 0.1μΑ
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	ESIST. 2 kΩ 20 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 200 kΩ 1000 kΩ 200 kΩ	2imA 200mA 200mA 2000mA In Input: 2A, : ture Coefficien 0.01% + 0.2 di 0.07% + 2 digit ANCE MAXIMUM READING 1,9999 19.999 2.0 cr circuit AL 2.5 live 0.5" LE SION PERIOD VMENT: ting: 0°C ro 5: 0% to 80	1.999 19.99 19.99 199.9 250V DC or r t (0°-18° and igits)/°C. (5)/°C. ACCURACY 18°- ±(% rdg HIΩ 0.04% + 1d 0.04%	9 1 9 1 9 1 9 1 9 1 1 28°-55°C): 1 28°C + digits] 1 0.15% + 15c 0.15% + 15c 0.15% + 15c 0.15% + 15c 0.15% + 15c 15c 9 1 10 15% + 15c 9 1 15% + 15c 15c 9 15c 15% + 15c 15c 9 15c 15% + 15c 15c 9 1 16k AC momentation 1 17 1 18 1 19 1 10 1 10 1 10 1 10 1 10 1 10 1 11 1 12 1 13 1 14 1 15 1 15	0.2% + 2d 0.2% + 2d 0.2% + 2d 0.2% + 2d b.2% + 2d dected). MAXIMUM ACROSS ON F HI Ω 1 2V 2V 2V 2V 2V	1% + 15d 1% + 15d 1% + 15d 1% + 15d 1% + 15d 0 Crest F Anaiog 0 C: AC: AC: AC: AC: AC: AC: AC: AC: AC: A	$\begin{array}{c} 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\ 0.2\\$	r 1 digit of fin thin 10 digits of COE FFICIENT 28°-55°C digits)/°C LO Ω 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.02% + 2d 0.03% + 2d 0.02% + 2d 0.03% + 2d 0.02% + 2d 0.02% + 2d 0.03% + 2d 0.02% + 2d 0.03% + 2d 0.03% + 2d 0.03% + 2d 0.03% + 2d 0.03% + 2d 0.02% + 2d 0.03% + 2d 0.03% + 2d 0.03% + 2d 0.02% + 2d 0.03% + 2d 0.02% + 2d 0.03% + 2d 0.02% +	100 Ω 10 Ω 1 Ω 0,1 Ω 1 Ω 0,1 Ω 1 Ω 1 Ω 1 Ω 1 Ω 1 Ω 1 Ω 100 μ A 10 μ A 1	100μA 100μA 10μA 1μA 0.1μA g except 0-110V pack, 275mm bove

INSTRUCTION MANUAL Digital Multimeter Model 4002





INSTRUCTION MANUAL Digital Multimeter Model 4002

SECTION I. GENERAL INFORMATION.

1-1. INTRODUCTION. The Model 4002 DMM is a versatile digital multimeter useful for measurement of ac and dc voltage, ac and dc current and resistance. Ranges and accuracies are listed in the Table of Specifications on Page v. Ranges and functions are selected with pushbuttons on the front of the unit. The 4002 automatically indicates polarity and positions the decimal point.

1-2. WARRANTY INFORMATION. The Warranty is given on the back of the title page of this Instruction Manual. If there is a need to exercise the Warranty, contact the Racal-Dana Representative in your area to determine the proper action to be taken. Racal-Dana maintains service facilities in the United Kingdom and France, as well as in the United States. A listing of Representatives and Service Centers is included in the back of this manual.

1-3. CHANGE NOTICES. Improvements or changes to the instrument which occur after printing of the Instruction Manual will be explained on a Change Notice sheet attached to the inside back cover.

IMPORTANT

The /! symbol can be found in various places in this Instruction Manual. Carefully read the associated CAUTION statements with regard to proper use and handling of the instrument. Damage to the instrument may occur if these precautions are ignored.

The symbol can be found in various places in this Instruction Manual. This symbol indicates those areas on the instrument which are potential shock hazards. Carefully read the associated WARNING statements with regard to proper use and handling of the instrument. Serious personal injury may result if these precautions are ignored.

1-1

INSTRUCTION MANUAL

Digital Multimeter Model 4002

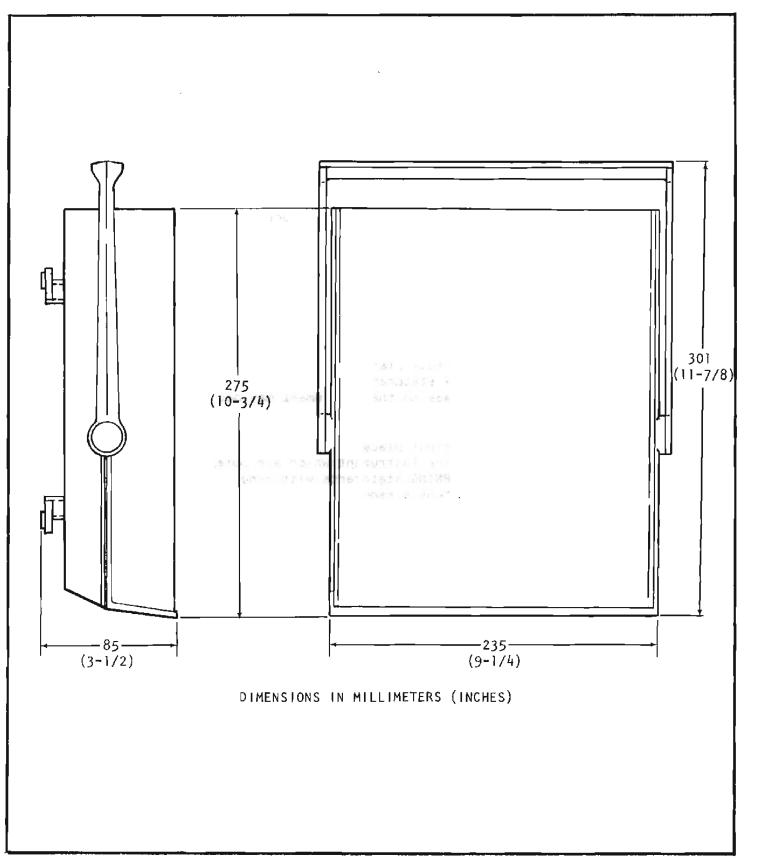
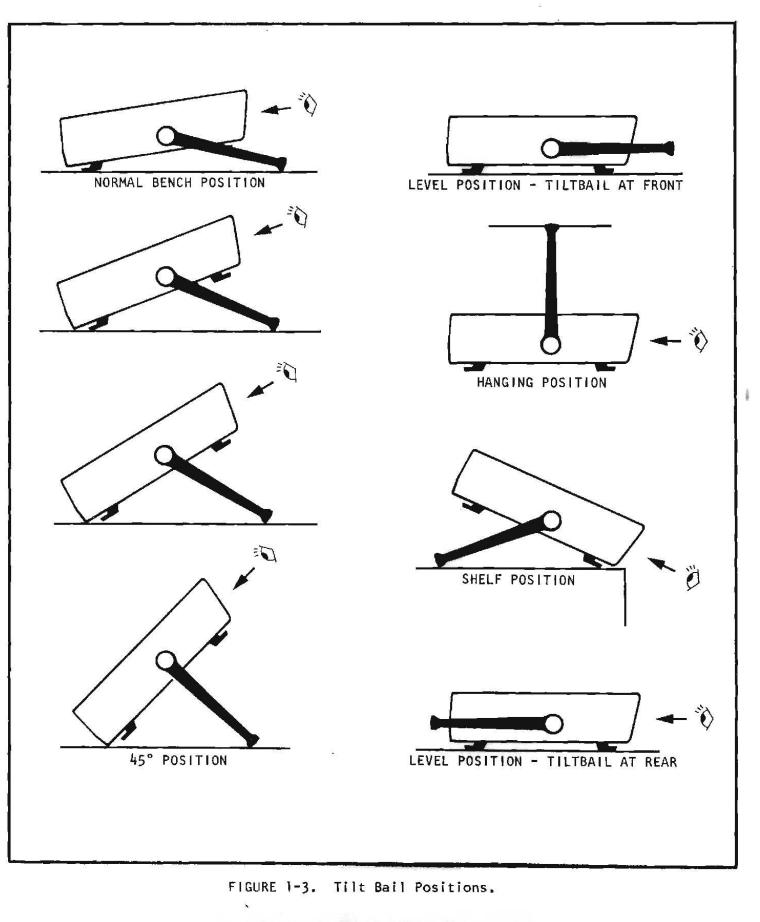


FIGURE 1-2. Dimensional Data



OPERATING INSTRUCTIONS

INSTRUCTION MANUAL **Digital Multimeter** Model 4002

2-5. OPERATION ON BATTERY PACK POWER, NOITARAGOLE, 2" NOITORS MAY also be operated from rechargeable sealed lead-acid batteries contained in the Option 06 Battery Pack. - The Circuits within the battery

2-1. GENERAL. This section provides information needed for incoming inspection and preto maintain accurate readings. Refer to Figure 2-2 and install the batter, acurate noise accurate readings.

INSPECTION. The Model 4002 was carefully inspected both mechanically and electri-2-2. cally before shipment. Upon receiving the instrument, check for any obvious damage which may have occurred during transit. Report any damages to the shipping agent. To verify the electrical specifications, follow the procedures given in Section 3.

PREPARATION FOR USE. The Model 4002 is shipped ready-to-use. The instrument may be 2-3. powered from line voltage or from rechargeable batteries (when the Option 06 Rechargeable Lift off the callbration shield, and save it for later use. (baldatani aletsey yates

OPERATION ON LINE POWER. The Model 4002 DMM is provided with a three-wire line cord 2-4. which mates with third-wire grounded receptacles. Connect the instrument to ac line power as follows: NOTE

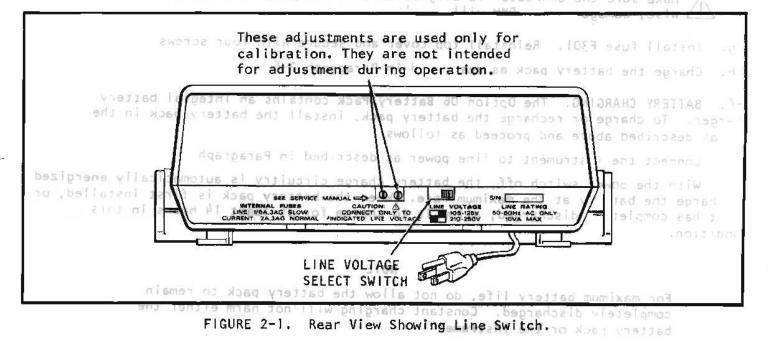
CAUTION Connect only to the line voltage selected. Application of incorrect voltage can damage the instrument.

Set the LINE VOLTAGE switch on the back of the instrument to correspond to the line a. voltage available. Ranges are 105 to 125 volts and 210 to 250 volts ac as shown in e. Install the battery pack in the instrument so that it rests on the plasticls Sanupil

he ground clip must make contact with the upper side of the battery pack plate ground clip must make contact with the upper side of the battery pack plate connector P1004 on the circuit board

Ground the instrument through a properly grounded receptacle before operation. Failure to ground the instrument can result in severe injury or death in the event of short circuit or malfunction.

b. Plug the power cord into a properly grounded outlet. Operate the 4002 DMM as A Make sure the connector is aligned so that all pins mate 7-2 MOITJ32 mi bedirozeb



OPERATING INSTRUCTIONS

2-5. OPERATION ON BATTERY PACK POWER. The Model 4002 DMM may also be operated from rechargeable sealed lead-acid batteries contained in the Option 06 Battery Pack. The battery pack will operate the 4002 DMM for up to 6 hours. Circuits within the battery pack will automatically shut down the instrument when the battery charge is insufficient to maintain accurate readings. Refer to Figure 2-2 and install the battery pack as follows:

WARNING Disconnect the line cord before removing the case cover.

INSTRUCTION MANUAL

Digital Multimeter Model 4002

a. Turn off the power and disconnect the line cord. Remove four screws from the bottom of the case and separate the top cover from the bottom cover.

b. Lift off the calibration shield, and save it for later use. The four plastic spacers must remain in place on the upright studs projecting through the main circuit board.

NOTE

Do not discard the calibration shield. This shield must be installed during calibration, as described in Section 4.

c. Set the BAT/LINE switch to the BAT position shown in Figure 2-2. Note that the battery pack will not operate properly if this switch is not in the BAT position.

d. Remove fuse F301 on the battery pack. allow 201 of 201 are seenad.

e. Install the battery pack in the instrument so that it rests on the plastic spacers. The ground clip must make contact with the upper side of the battery pack plate.

f. Carefully align the battery pack plug with connector Pl004 on the circuit board. Push the plug firmly onto the connector until the lip on the plug engages the lip on the connector to lock the plug in place.

power cord into a properly WOLTUADd outlet. Operate the 4002 JAM

 \triangle Make sure the connector is aligned so that all pins mate properly, otherwise, damage to the DMM will result.

g. Install fuse F301. Reinstall top cover and secure with four screws.

h. Charge the battery pack as described in Paragraph 2-6.

2-6. BATTERY CHARGING. The Option O6 Battery Pack contains an integral battery charger. To charge or recharge the battery pack, install the battery pack in the DMM as described above and proceed as follows:

a. Connect the instrument to line power as described in Paragraph 2-4.

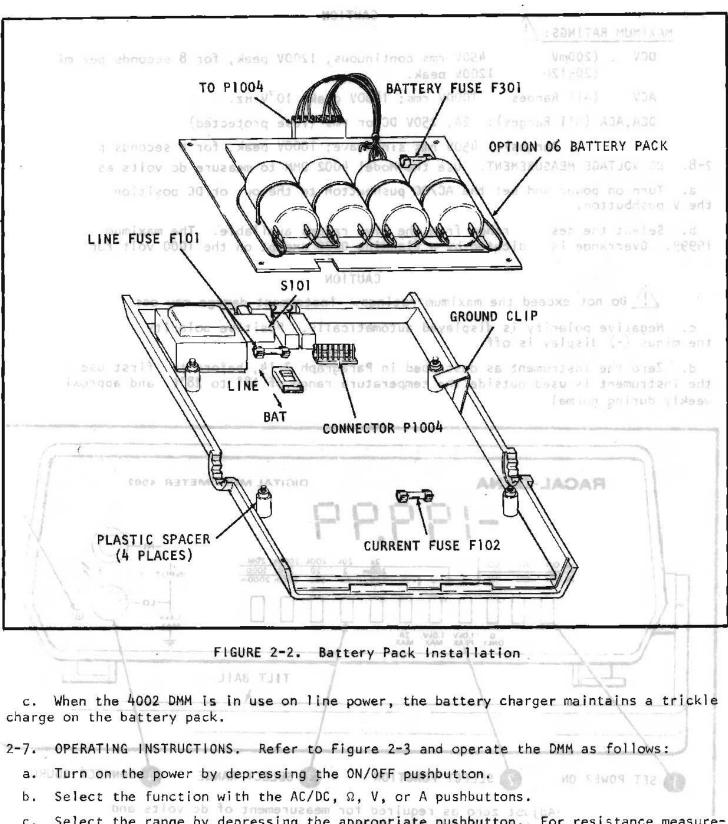
b. With the power switch off, the battery charge circuitry is automatically energized to charge the battery at the maximum rate. When the battery pack is first installed, or if it has completely discharged, allow it to charge for at least 14 hours in this condition.

NOTE OV BHIL

For maximum battery life, do not allow the battery pack to remain completely discharged. Constant charging will not harm either the battery pack or the instrument.

OPERATING INSTRUCTIONS

INSTRUCTION MANUAL Digital Multimeter Model 4002



c. Select the range by depressing the appropriate pushbutton. For resistance measurements only, also set the LO/HI pushbutton as desired.

d. Connect the source to the INPUT terminals. Accessories described in Paragraph 2-15 should be used as required.

CAUTION

MAXIMUM RATINGS: /!

DCV (200mV, 2V): 450V rms continuous; 1200V peak, for 8 seconds per minute. (20-1200V): 1200V peak.

ACV (All Ranges): 1000V rms; 1400V peak; 10⁷V·Hz.

DCA,ACA (All Ranges): 2A, 250V DC or rms (fuse protected).

Ω (All Ranges): 450V rms sine wave; 1000V peak, for 8 seconds per minute.
 2-8. DC VOLTAGE MEASUREMENT. Use the Model 4002 DMM to measure dc volts as follows:

a. Turn on power and set the AC/DC pushbutton to the out or DC position. Depress the V pushbutton.

b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000 except on the 1000 volt range.

CAUTION

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 $(\underline{1})$ Do not exceed the maximum ratings. Instrument damage may occur.

c. Negative polarity is displayed automatically. Positive polarity is implied when the minus (-) display is off.

d. Zero the instrument as described in Paragraph 2-14, before the first use whenever the instrument is used outside the temperature range of 18° to 28°C, and approximately weekly during normal use.

RACAL-DANA - ; G. G. C. A OFF DC HI & V A 200m OFF DC HI & V A 200m OFF DC HI & V A 200m ONLY FEAK MAX MAX	20k 200k 2000k 20M
*Adjust zero as required	3 SELECT RANGE: 4 CONNECT SOURCE for measurement of dc volts and

FIGURE 2-3. Operating Controls. Beilupsi as been ad bluote

INSTRUCTION MANUAL Digital Multimeter

Model 4002

2-9. AC VOLTAGE MEASUREMENT. Use the Model 4002 DMM to measure ac volts as follows:

a. Turn on power and set the AC/DC pushbutton to the in or AC position. Depress the V pushbutton.

CAUTION

!\ Do not exceed the maximum ratings. Instrument damage may occur.

b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000 except on the 1000 volt range. The instrument measures the true root mean square of a signal within the frequency range of 45 to 20k hertz. Maximum crest factor for rated accuracy is 3.

c. Refer to Paragraph 2-13 for TRMS measurements of a signal with both ac and dc components.

2-10. RESISTANCE (Ω) MEASUREMENT. Use the 4002 DMM to measure resistance as follows: a. Turn on power and depress the Ω pushbutton.

CAUTION

(!) Do not exceed the maximum ratings. Instrument damage may occur.

b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 00000. Use the LO/HI pushbutton as follows:

1) Use the HI mode for measurements in the 20k, 200k, 2000k and 20M ohm ranges. Full range voltage drop is 2 volts and is sufficient to cause forward conduction of semiconductor junctions. The HI terminal is positive.

2) Use the LO mode for measurements in the 2k, 20k, 200k and 2000k ohm ranges. Full range voltage drop is 200 millivolts. Depressing 2k automatically selects LO mode; 20M selects HI mode. Maximum open circuit voltage is 5V on all ranges.

c. Zero the instrument as described in Paragraph 2-14 before the first use whenever the instrument is used outside the temperature range of 18° to 28°C, and approximately weekly during normal use.

2-11. DC CURRENT MEASUREMENT. Use the 4002 DMM to measure dc current as follows:

a. Turn on power and set the AC/DC pushbutton to the out or DC position. Depress the A pushbutton.

CAUTION

 \triangle Do notinstall a larger fuse than the one originally supplied (2A). Current fuse Fl02 protects the instrument against overcurrent.

b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000. Overload is protected by fuse Fl02, a 2 amp 3AG normal-blow fuse.

2-12. AC CURRENT MEASUREMENT. Use the 4002 DMM to measure ac current as follows:

a. Turn on power and set the AC/DC pushbutton to the in or AC position. Depress the A pushbutton.

CAUTION

Do not install a larger fuse than the one originally supplied. Current fuse F102 protects the instrument against overcurrent.

b. Select the desired range from the five available ranges. The maximum reading is 19999. Overrange is indicated by a flashing 0000. The instrument measures the true root mean square of a waveform in the frequency range of 45 to 10k hertz. Overload is protected by fuse F102, a 2 amp 3AG fuse.

2-13. TRMS MEASUREMENT. The Model 4002 measures the ac component of a waveform and does not measure the dc component. For ac + dc measurements, use the procedure discussed in a. below.

NOTE

Accuracy is specified for 2000 counts and above. The method of calibrating the converter may yield an offset up to 50 digits with the input shorted. This does not affect the instrument accuracy.

a. Use the 4002 DMM to measure TRMS on a signal which has both ac and dc components as follows:

1. Turn on the power. Measure and record the ac and dc components separately.

2. Compute the rms value from the following equation:

$$E_{RMS} = \sqrt{E_{DC}^2 + E_{AC}^2}$$

b. The crest factor (CF) is the ratio of the peak voltage to the rms voltage as follows:

$$CF = \frac{V_{PEAK}}{V_{RMS}}$$

1. Typical crest factors are as follows:

Sine wave	CF ≖ √2
Square wave	- CF = 1
Triangular wave	$CF = \sqrt{3}$
Positive pulse train (duty cycle for CF = 3	CF = 1/ duty cycle is 0.11)

NOTE

There will be some additional measurement error for signals with a crest factor greater than 3 (CF>3).

2-14. ZERO ADJUSTMENT. The front panel zero adjustment nulls input offset on the 20, 200 and 1200 dc voltage ranges and on all resistance ranges. Typically, this adjustment need not be performed more often than once a week unless the instrument is operated at ambient temperatures outside the range of 18° to 28°C. Zero the instrument as follows:

a. Turn on the power and select LO Ω and the 200k range.

b. Plug in test leads and short them. Adjust the zero adjustment pot (R149) to obtain a reading of 0000 ± 3 digits.

NOTE

The zero adjustment may also be used for lead compensation on a particular Ω range.

musical and anubacSECTION 3. PERFORMANCE VERIFICATION.

meet the following, conditions:

3-1. GENERAL. Performance verification should be performed upon receipt of the instrument to ensure that no damage or misadjustment has occurred during transit. Verification may also be performed whenever there is question of the instrument's accuracy, and following calibration, if desired.

pack has been fully charged as described iFTOWragraph 2-5.

For instruments that are still under warranty (less than 12 months since date of shipment), if the instrument's performance falls outside specifications at any point, contact your Racal-Dana representative or the factory immediately.

3-2. RECOMMENDED TEST EQUIPMENT. Recommended test equipment for performance verification is listed in Table 3-1. Alternate test equipment may be used. However, if the accuracy of the alternate test equipment is not at least 10 times better than the instrument specifications, additional allowance must be made in the readings obtained.

3-3. ENVIRONMENTAL CONDITIONS. All measurements should be made at an ambient temperature within the range of 18° to 28°C (65° to 82°F), and a relative humidity of less than 80%.

3-4. PERFORMANCE VERIFICATION PRODECURE. Use the following procedures to verify the basic accuracy of the Model 4002 DMM for voltage, resistance and current measurements. If the instrument is out of specifications at any point, perform a complete calibration as described in Section 4, unless the instrument is still under warranty, as noted above.

5) Repeat all cnacks with negative voltage.

TABLE 3-1.

ITEM	DESCRIPTION	SPECIFICATION	MFR.	MODEL
A	DC Calibrator	0.10, 10, 100, 1000, 10000	Fluke	343A
В	00.07 AC Calibrator 2000.1	±0.002% or 20µV 0.1V, 1V, 10V, 100V ±0.022%	200 mV 9-H 2 V	745A
C	AC Calibrator/Amplifier	1000V @ ±0.04% V 000.01	209¥H	745A/746A
D	Decade Resistor 20.00		200123	R\$725
E	Current Source	100µA, 1mA, 10mA, ±0.006%	Fluke	3330B
F	Current Source	100mA, IA, ±0.02%	Fluke	382A

Recommended Test Equipment For Performance Verification.

2) Connect the AC Calibrator (Item 3, Table 3-1) to the DMM. Set the Tobrace frequency to 1 kHz.

3) Set the DMM to the 200 mV range and apply 100 mV ac to the DMM. The read is must be within the limits specified in Table 3-3.

Performance verification should be performed by qualified personnel using accurate and reliable test equipment.

PERFORMANCE VERIFICATION

Model 4002

a. Initial Conditions. Before beginning the verification procedure the instrument must meet the following, conditions:

1) If the instrument has been subjected to extremes of temperature, allow internal temperatures to stabilize for one hour minimum at the environmental conditions specified in Paragraph 3-3.

2) Turn-on the 4002 DMM and allow it to warm up for 10 minutes. The instrument may be operated from either line power or from battery pack power, as long as the battery pack has been fully charged as described in Paragraph 2-6.

3) Zero the instrument as described in Paragraph 2-14.

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Some procedures require the use of high voltage. Take care to prevent contact with live circuits which could cause electrical shock resulting in injury or death.

b. DC Volts Checkout. solibes and ni abem al term abrewills leveltible , ordite

- 1) Select dc voltage readings with the AC/DC and V pushbuttons.
- 2) Connect the DC Calibrator (Item A, Table 3-1) to the instrument.

3) Select the 200mV range, and apply positive 100 mVdc to the DMM. The reading must be within the limits specified in Table 3-2.

4) Select each remaining range and apply the required voltage as specified in Table 3-2, verify that the reading is within specifications.

5) Repeat all checks with negative voltage.

TABLE 3-1.

Range	Applied Voltage	Allowable Readings at 18° to 28°C
200 mV	100.00 mV	99.93 to 100.07
2 V	1.0000 V	0.9995 to 1.0005
20 V	10.000 V	10.01 9.995 to 10.005 (and rotordile
200 V	100.00 V 🐋	bei
1200 V	1000.0 ⁸ V ^{6.0}	999.5 to 1000.5

Recommended fest Equipments 22 TABLE 322 The output for the section.

c. AC Volts Checkout.

Select ac voltage readings with the AC/DC and V pushbuttons.

2) Connect the AC Calibrator (Item B, Table 3-1) to the DMM. Set the calibrator frequency to 1 kHz.

3) Set the DMM to the 200 mV range and apply 100 mV ac to the DMM. The reading must be within the limits specified in Table 3-3.

4) Select the 2, 20 and 200 volt ranges and apply the required voltages as specified in Table 3-3. Verify that the readings are within specifications.

and relievle test equipment.

PERFORMANCE VERIFICATION

INSTRUCTION MANUAL

Digital MultImeter

Model 4002

5) To check the 1000 volt range, connect the AC Calibrator Amplifier (Item C, Table 3-1) to the output of the AC Calibrator per the manufacturer's instructions. Set it for an output of 1000 volts ac rms and verify that the DMM readings is within the specified limits.

Select the 200 uA range and apply a current of 100.00 UA to the DMR. The

TABLE 3-3. aldaf ni stimi ant miniw ad

AC Voltage Performance Check

Range	Applied Voltage	Allowable Readings at 18° to 28°C
200 mV	100.00 mV	99.15 to 100.85 mV and of aller vinena
2 V	1.0000 V	0.9925 to 1.0075 V
20 V	10.000 V	9.935 to 10.065 V
200 V	100.00 V	99.35 to 100.65 V
1000 V	1000.0 V	993.5 to 1006.5 V

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d. Resistance Checkout. ss. ool of 88.00

1) Select resistance readings by pressing the Ω pushbutton.

2) Set the H1/LO pushbutton to H1 and select the 20k Ω range.

3) Connect the decade resistor (Item D, Table 3-1) to the DMM.

4) Set the decade resistor to zero and measure the resistance of the test leads. Subtract this reading from the displayed reading in all of the following steps.

5) Set the decade resistor to 19.000 k Ω . Verify that the reading is within the limits specified in Table 3-4.

6) Select the next range and measure the next resistance as specified in Table 3-4. Verify that each reading is within specifications. Test each item in the table, switching the HI/LO pushbutton as indicated.

HI/LO	Range	Resistance	Allowable Reading at 18° to 28°C
HI	20 kΩ	19,000 kΩ	18.990 to 19.010 kΩ
HI	200 kΩ	190.00 kΩ	189.90 to 190.10 kΩ
' HE	2000 kΩ	1.9000 MΩ	1899.0 to 1901.0 kΩ
нι	20 MΩ	19.000 MΩ	18.980 to 19.020 MΩ
LO	2 kΩ	1.9000 kΩ	1.8957 to 1.9043 kΩ
LO	20 kn	19.000 ka	18.957 to 19.043 kΩ
LO	200 kΩ	190.00 kΩ	189.57 to 190.43 kΩ
LO	2000 kΩ	1900.0 kΩ	1895.7 to 1904.3 kΩ

TABLE 3-4. Resistance Performance Check

PERFORMANCE VERIFICATION

e. DC Current Checkout.

1) Select dc current readings with the AC/DC and A pushbuttons.

2) Connect the dc current source (Item E or F, Table 3-1) to the DMM.

3) Select the 200 μ A range and apply a current of 100.00 μ A to the DMM. The reading must be within the limits in Table 3-5.

4) Select each range and apply the required current as specified in Table 3-5. Verify that the reading is within specifications.

f. Analysis. If the instrument is out of specified limits at any point in Tables 3-2 through 3-5, calibrate the DMM as described in Section 4. If the unit is still under warranty, refer to the note in Paragraph 3-1.

Range	Applied Current	Allowable Reading at 18° to 28°C
200 uA	100.00 μA	99.88 to 100.22 µA
2 mA	1.0000 mA	0.9988 to 1.0022 mA
20 mA	10.000 mA	9.988 to 10.022 mA
200 mA	100.00 mA	99.88 to 100.22 mA
2000 mA	1000.0 mA	998.8 to 1002.2 mA

TABLE 3-5. DC Current Performance Check

ade real while 19.000 kB, Verify that the reading

inv is lenge and measure the next resistance as specified each readiments within specifications. Test each item in to L0 pushbriton as indicated

	a a finanti ka ni ka	second to the second second	
s of "81 je paipeas	Allowable		
19 to 19.010 kg	66.81		
o to 190.10 ks	e. (8)		
01 0.1001 of 0	. 9881		
5 M 630.01 03 0	86. 8 1		
7 to 1.9043 k.			
W (0 19,00 K	18.95		
7 to 190.43 ks	2.981		
28 8. 17 P. C. T	2081		

TABLE 3-4. relstance Performance Check

SECTION 4. CALIBRATION

4-1. GENERAL. Calibration should be performed yearly (every 12 months) or whenever performance verification (See Section 3) Indicates that the Model 4002 DMM is out of specifications. If any step in the calibration procedure cannot be performed properly, refer to Section 5 for Troubleshooting Information or contact your Racal-Dana representative or the factory.

4-2. RECOMMENDED TEST EQUIPMENT. Recommended test equipment for calibration is listed in Table 4-1. Alternate test equipment may be used. However, the accuracy of the alternate test equipment must be at least 10 times better than the instrument specification, or equal to Table 4-1 specifications.

ltem	Description	Specification	Mfr.	Model
A	DC Calibrator	0.1V, 1V, 10V, 100V, 1000V ±0.002% or 20µV	Fluke	343A
В	AC Calibrator	0.1V, 1V, 10V, 100V ±0.022%	H-P	745A
c	Decade Resistor	1.9KΩ, 190KΩ, ±0.01%	ESI	RS725

	0.124	TABLE 4-1.		
Recommended	Test	Equipment	For	Calibration.

4-3. ENVIRONMENTAL CONDITIONS. Calibration should be performed under laboratory conditions having an ambient temperature of 20° to 26°C (68° to 78°F), and a relative humidity of less than 80%.

4-4. CALIBRATION PROCEDURE. Perform the following adjustments to calibrate the 179 DMM and restore its operation to specified limits.

a. Calibration Shield Installation. If the Option O6 Battery Pack is installed in the instrument it must be removed and the calibration shield reinstalled before calibration.

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Disconnect the line cord before removing the case cover.

1) Turn off the power and disconnect the line cord. Remove four screws from the bottom of the case and separate the top cover from the bottom cover.

2) Push back the ground clip (shown in Figure 2-2) from the upper side of the battery pack and remove the battery pack from the spacers.

3) Calibration may be performed on battery power as long as the battery pack is sufficiently charged. Leave the battery pack plugged into the instrument, but set the battery pack behind the DMM on the bench or table.

4) Set the calibration shield in place on the spacers. The shield should read correctly when viewed from the front of the instrument.

5) Slide the ground clip over the top of the calibration shield so that it contacts the upper surface of the shield.

6) If battery power is not to be used, plug in the line cord.

b. Calibration Instructions.

WARNING

Some procedures require the use of high voltage. Take care to prevent contact with live circuits which could cause electrical shock resulting in injury or death.

1) Refer to Table 4-2 and perform the listed adjustments in the sequence indicated. Note that the step sequence is also indicated on the calibration shield by boxed numerals. The sequence must be followed exactly because the adjustments are interrelated and dependent on the preceeding steps.

2) If the indicated adjustment cannot be made to obtain the specified reading, refer to Section 2-5 for Troubleshooting Information.

	Calibration Procedure						
Step	Function	Range	Input	Adjustment Point	Desired Reading	Test Equipment*	
1	DC V	2 V	+1.9 V	R107	1,9000	DC Calibrator (A)	
2	DC V	200 mV	+190 mV	R108	190.00	DC Calibrator	
3	DC V	2 V	+1.9 V	R107	1,9000	DC Calibrator	
4	Ω LO	200 kΩ	Short	R149	Set Front Panel Zero to Mechanical Center.	None	
5	ΩL0	200 kΩ	Short	R112	00.0±10 digits	None	
6	Ω L0	200 kΩ	Short	R149	00.00 ±2 digits	None	
7	ΩHI	200 kΩ	190 kΩ	R127	190.00	<pre>Decade Resistor(C)</pre>	
8	Ω LO	2 kΩ	1.9 kΩ	R129	1.9000	Decade Resistor	
9	DC V	200 V	+190 V	R103	190.00	DC Calibrator	
10	DC V	20 V	+19 V	R126	19.000	DC Calibrator	
11	DC V	1000 V	+1000 V	R128	1000.0	DC Calibrator	
12	AC V	20 V	l V at 1 kHz	R142	1.000	AC Calibrator (B)	
13	AC V	20 V	10 V at 1 kHz	R143	10.000	AC Calibrator	
14	AC V	20 V	l V at 1 kHz	R142	1.000	AC Calibrator	
15	AC V	20 V	10 V at 1 kHz	R143	10.000	AC Calibrator	
16	AC V	200 V	100 V at 10 kHz	C106	100.00	AC Calibrator	
17	AC V	20 V	10 V at 10 kHz	C112	10.000	AC Calibrator	
18	AC V	2 V	l V at 10 kHzero	C111	1.0000	AC Calibrator	

TABLE 4-2. Calibration Procedure

* See Table 4-1.

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SECTION 5. TROUBLESHOOTING.

5-1. GENERAL. The troubleshooting instructions contained in this section are intended for qualified personnel having a basic understanding of analog and digital electronic principles and components used in a precision electronic test instrument. Instructions have been written to assist in isolating the defective circuit or subcircuit. Isolation of the specific defective component has been left to the technician.

INE/BAT switch Must de set to BAT for use with

NOTE ynsdiad

For instruments that are still under warranty (less than 12 months since date of shipment), if the instrument's performance falls outside specifications at any point, contact your Keithley representative or the factory immediately.

5-2. TROUBLESHOOTING PROCEDURE. This section contains tables listing step-by-step checks of the major DMM circuits described in Section 6, Theory of Operation. Proceed as follows:

a. In general, start troubleshooting with Table 5-1, Line Power Checks, to verify that the power supplies are providing the specified voltage to the electronic components.

b. If trouble occurs on battery power only, or if battery operating time is substantially less than 6 hours after overnight charging, test the batteries and charging circuit per Table 5-2.

c. Proper operation of the A/D converter & display should be verified before troubleshooting the signal conditionings. Check these circuits per Tables 5-4 and 5-3, respectively.

d. Problems with ac voltage ranges may involve the ac attenuator, the ac amplifier, or the ac converter. Check these circuits per Table 5-6 and 5-8.

e. Check the dc voltage attenuator per Table 5-5 if problems occur with the dc voltage ranges. Check the resistance circuit per Table 5-7 if resistance measurements are erratic.

f. If problems occur with current readings, check the current shunts and related circuits per Table 5-9.

g. All measurements are referenced to analog common (ground clip).

On main printed circuit board.

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TABLE 5-1. Line Power Checks

Step	ltem/Component	Required Condition	Remarks
1	S101 line switch	i 250V as appropriate.	
2	S102 LINE/BAT switch	Must be set to BAT for use with battery pack.	
3	F101 line fuse	Continuity.	
4	P1007 line cord	Plugged into live receptacle.	
5	2 ** i 0 1 0 * d	Turn on nower	
6	+5V pad*	+5 volts ±10%,	Output of VR104.
7	VR104, INclassia or a	+7 volts Minimum.	Output of CR101, input to VR104.
8	+15V pad*	+15 volts ±10%.	Output of VR102.
9	la be zerified re ≭fqT ar "ables 5-0 mi	od+17.5 volts minimum.co () a sol atiuchic sents kosili association	Output of CR102, input to VR102.
10		-15 volts ±10%.	Output of VR101.
11	li dina neuso speld	-17.5 volts minimum.	Output of CR102, input to VR101,
		ant readings, check the b	NOTE: Hot reg- ulator may indi- cate shorted loa

* On main printed circuit board.

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Digital Multimeter" Model 4002

TABLE 5-2.

		TABLE 5-2. Battery Power Checks	
Step	ltem/Component	Required Condition	Remarks
1		Check AC line power per Table 5-1.	
2		Turn off power.	
3	S102 LINE/BAT switch	Move to BAT.	
4	Pl007 line cord	Plugged into live receptacle.	Charge circuit checks.
5	F301	Remove fuse and connect ammeter to fuse clip. O to 500mA charging rate, varies with line voltage and battery state of charge.	
5A	BT301 Batteries	Full charge is ≃9.8 volts over 4 cells. R301 adjusts charging rate (float voltage).	If voltage is low and adjustment of R301 does not start charging, see steps 7 and following. If voltage is low and adjustment of R301 does start charging, see Table 5-10 for adjustment of bat- tery charge voltage.
6	Each battery cell voltage <u>during</u> charging.	Less than 3 volts for any cell.	High voltage or zero indicates damaged cell.
7	Q301 anode	<pre>PFull wave rectified voltage, 15 VDC nominal.</pre>	Output of CR101.
8	C304 +	+17.5 volts minimum.	Output of CR102. Triggers Q301 gate thru R306 and CR301 unless Q302 is on.
9	Q302	Should saturate only when bat- tery approaches full charge.	
10	VR301	8.2V zener.	
11		Unplug line cord & turn power on.	Discharge checks.
12	P1004 pin 8 or U301 pin 11	100 kHz 5V square wave,	Clock input. If no input, see step 12A.
12A	VR104, IN	+7 volts minimum.	Battery voltage input to VR104,
13	Q307 and Q308 base	Square wave, ±0.7 volts at 25 kHz.	Output of U301, ÷ 4.
14	Q307, Q308 collector	Must oscillate from saturation to twice battery voltage (≃19 volts) at 25 kHz.	inverter.
15	C304, C305	±17.5 volts minimum (±25 volts typical with fully charged bat.).	Inverter Output, input to VRIOI&VRIO2

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TROUBLESHOOTING

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Step	ltem/Component	Required Condition	Remarks
1		Turn on power. Any function or range except OHMS.	
2	+5V* or J1001, pin 5	+5 volts ±10%,	lf low, check per Table 5-1.
3	U202, pins 2, 6, 7, 9 and 13	Digit drive LOW = Enabled,	LED cathode.
4	U201, pins 1, 2, 6 and 7	HI = Enabled.	BCD input to segment decoder/driver.
5	U201, pin 4	Positive-going signal lasting for 200 clock pulses.	Leading digit sup- pression. Output of 'Ul07A.
6	J1002, pin 9	Polarity line (SIGN) HI = off LO =	NOTE: Polarity out- put (in at J1002, pin 9) is inverted for VDC on 20 volt and higher ranges. Polarity output is disabled on AC and Ω.
7	J1001, pins 1, 2, 3 and 4.	Appropriate DP line high (on).	

TABLE 5-3. Display

* On main printed circuit board.

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Model 4002

Step	ltem/Component	Required Condition	Remarks
1	Referen- Ulué.	Turn on Power. Select 2 volt DC range and short inputs.	On this function and range (also on 200 mV DC), input
	insegr i	clov (.0± 000.1+	HI connects thru R106, R136, and R135G to A/D, with
2	U103 1 pgrat	1.2 +0.2 velts.	out attenuation. s
2	Display	.0000 ±1 digit.	
3	TP10*	0.0000 volts.	A/D signal input.
4	TP8*offu8	+1.00 volt.	Reference output.
5	Display Calib*EqT	+100 millivolts.	Reference output.
6		pib 16.30±0.25 volts.um	Reference zener voltage.
7	U106, pin 7	+1.00 volt. board tieve	Reference input to U106.
8	CLK*	0 to +5V square wave at 100 kHz.	Clock input.
9	TP6*	+1.0 ±0.1 volt,	Stored autozero
10	U103, pin 11 gai sissa	ntegrator waveform for 1.0± 0.1+ full	voltage. Σ-node voltage to integrator in V10
11	TP7*	-1.2 ±0.2 volt,	Ul03 integrator output voltage.
12	U104, pins 2, 3, and 6	+1 volt.	Buffer voltage on U104.
13	External voltage 001 source	Apply +1.9000 volts. Display must read 1,9000 ±1 digit.	Calibration point
14	TP7* *797	Waveform per Figure 5-1.	Integrator output
15	U103, pin ¹ 2 ¹²⁸³ V000 1	Waveform per Figure 5-1 during ramping of integrator output.	Comparator output
16	(Eujdwe	Select 200 mV range and short inputs.	
17	Display	00.00 ±3 digits.	Proceed if out-of limits. Change selected value of R145 if tests 18- meet required conditions.
18	TP10*	0.0000 volts.	A/D signal input.

TABLE 5-4. A/D Converter

* On main printed circuit board.

IGURE 5-1 Integrator and Comparator Waveforms

TROUBLESHOOTING

Step	ltem/Component	Required Condition	Remarks
19	U106, pin 7	+0.100 volt.	Reference input to U106.
20	U103, pin 11	+1.000 ±0.1 volt.	Σ-node voltage to integrator in UlO3.
21	TP7*	-1.2 ±0.2 volts.	Ul03 integrator output voltage.
22	U104, pîns 2, 3	+100 millivolts.	Buffer voltage on Ul04.
23	· U104, pin 6	+1 volt. the second	Buffer voltage.
24	External voltage source	Apply +190 millivolts. Display must read 190.00 ±1 digit.	Calibration point.
* 0n	main printed circuit boar	d, Jlov Of L4	
		The source was	
	-1.2V	mSEC mSEC mSEC	
	Comparator	waveform (Logic 1 <u>during</u> ramping)	
	+5		
	+5 0 ———		

TABLE 5-4. A/D Converter, continued

FIGURE 5-1. Integrator and Comparator Waveforms.

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	- An and a second	DC Volts Attenuator	
Step	ltem/Component	Required Condition	Remarks
1	A. A MA	Turn on power. Select 20 VDC range, and short inputs.	
2	R149	Front panel adjustment must zero the display.	
3	U101, pin 2	0.000 ±0.005 volts.	
4	External voltage source	Apply +10 volts from HI to LO.	Calibrated input.
5	TP5*	-l volt	Output of U101.
6	External voltage source	(Apply +100 and +1000 volts on 200 and 1000 volt ranges.	Calibrated input.
7	TP5*	-l volt	Output of UlOl and feedback components, including relays.

TABLE 5-5. DC Volts Attenuator

* On main printed circuit board.

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TROUBLESHOOTING

TROUBLESHOOTING

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Mo	del	4002	

Step	ltem/Component	Required Condition	∿ Remarks
1		Turn on power. Select 2 VAC range,	NOTE: Full scale inputs should pro- duce ≃2 volts out- put at TP9.
2	External voltage source	Apply I volt rms at I kHz.	Calibrated input.
3	TP9* & TP5	l volt rms.	Output of U101 and feedback components.
4	External voltage source	10, 100 and 1000 volts rms on 20, 200 and 1000 volt ranges.	Calibrated input.
5	TP9* & TP5	l volt rms on all ranges except 200 millivolts.	Output of UlO2 and feedback components, including relays.
6	External voltage source	Apply 1, 10, 100V @ 20kHz on 2, 20 and 200V ranges respect- ively.	C106, C111, C112, C113, and C114.
7	External voltage source	Apply 10V @ 45Hz on 20V range.	C105, C115, C116.
8		Select 200 mV range.	
9	External voltage source	100 millivolts at 1 kHz.	Calibrated input.
10	TP5*	100 millivolts rms,	Output of UlOl and feedback components.
11	TP9*	I volt rms.	Output of U102,

TABLE 5-6. AC Volts Attenuator and X10 Amplifier

* On main printed circuit board.

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TROUBLESHOOTING

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Step	ltem/Component	Required Condition	Remarks
1	1.1.1.2.1	Turn on power. Select Ω , HI and 200k range. Short inputs.	
2	U101, pin 2	Continuity to input HI.	K105.
3	TP10*	0.0 volt.	A/D input.
4	Ω switch, pin 11	+l volt,	Reference voltage.
5		Remove short and measure open circuit voltage; must be +2 to +5 volts.	R150 & R151.
6	100k resistor	Apply to input.	Calibrated resistanc
7	TP10*	SHI Volt. 21/0/0, 2	A/D input.
8	1	Select LO range.	
9	Ω switch, pin 1]	+100 millivolts,	Reference voltage.
10	TP10*	-100 millivolts.	A/D input.
11		Test other ranges in similar manner as needed.	NOTE: Reference loading by the current setting resistor does not
	- 14P	Requires Family on	affect readout since A/D converter is ratiometric.

TABLE 5-7. Resistance Circuit.

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Model 4002

TABLE 5-8.

AL	LOUN	erter	the shares	
1.000	6. A. 1. 26	Service Bell	121985	

Step	Item/Component	Required Condition	Remarks
<u>a</u>]	Hi novits-	Turn on power. Select 20 VAC range.	
2	External voltage source	10 volts rms, 1 kHz,	Calibrated input.
3	TP9*	<pre>volt rms (approximate).</pre>	Input to converter.
4	U105, pin 6 or TP10*	+1 volt DC,	Output of U105
5	R143 07 S+	Gain adjustment must operate.	1 ar th
6	Repeat steps 4 & 5	10 volts rms, 45 Hz.	Low frequency response.
7	Repeat steps 4 & 5	10 volts rms, 20 kHz.	High frequency response.
* 0n r	main printed circuit boar		I nic due bee
* On r	main printed circuit boar	d. .ajiovillim 001+ .ajiovillim 001- TABLE 5-9. Current Shunts	r seisch, oin 1 reige
* On r Step	main printed circuit boar	-100 millivolts. Test other ranges in sid manner.e-2 addad	
	main printed circuit boar	TABLE 5-9. Current Shunts	्रत वि
Step	nain printed circuit boar	TABLE 5-9. Current Shunts Required Condition Continuity.	्रत वि
Step 1	Item/Component F102 R123, R124, R137,	TABLE 5-9. Current Shunts Required Condition Continuity. Correct shunt value for specified range. See	Remarks Measure with

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Step	Item/Component	Required Condition	Remarks
1		Instrument off,	
2	R301	Turn full CCW (maximum charge rate).	
3	BT301	Monitor battery voltage for > 9.8V.	Fully charged cells require several minutes to reach this level. Dis- charged cells require several hours. CAUTION: charg- ing to >10V for longer than 30 min. will reduce battery life.
4	R301	When cells reach 9.8V, turn DMM on and adjust to maintain 9.8V across BT301.	

TABLE 5-10. Adjustment of Battery Charge Voltage

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6-1. GENERAL. This section contains circuit descriptions for the Model 4002 DMM and for the Option 06 Battery Pack. An overall block diagram of signal flow is provided in Figure 6-1. The overall schematic diagram, drawing 28992E, is contained in the back of this manual.

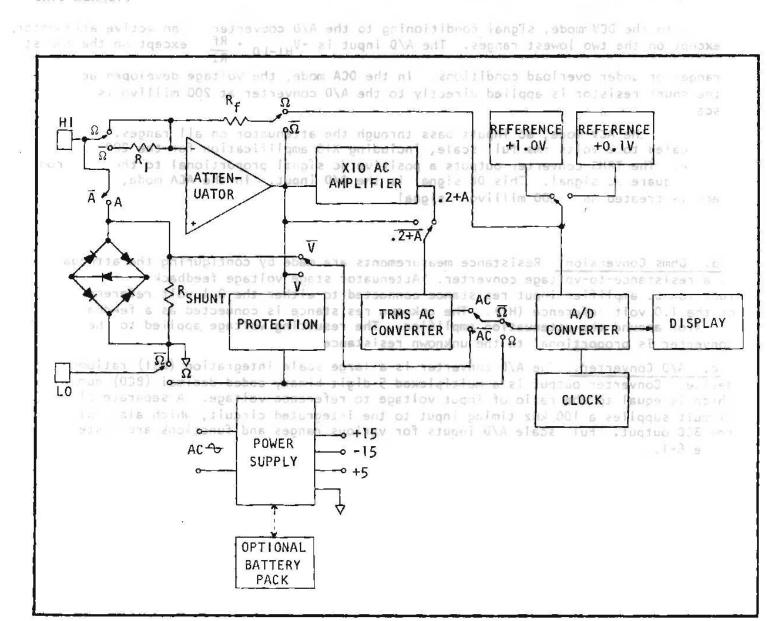


FIGURE 6-1. Simplified Signal Flow Block Diagram.

6-2. OVERALL OPERATION. The Model 4002 DMM uses a 2-volt or 200-millivolt full scale analog-to-digital (A/D) converter with a 4-1/2 digit multiplexed display. Signal conditioning permits the A/D converter to handle full scale ac and dc voltage and current measurements over 5 decades, and to measure resistance over 5 ranges.

a. Signal Conditioning. Signal conditioning includes dc attenuation (except on the 2 volt and 200 millivolt ranges), ac attenuation and X10 amplification, ac-to-dc conversion, ohms conversion and current shunts as shown in Figure 6-2.

1) In the DCV mode, signal conditioning to the A/D converter is an active attenuator, except on the two lowest ranges. The A/D input is $-V_{HI-LO} \cdot \frac{Rf}{RI}$, except on the lowest

ranges or under overload conditions. In the DCA mode, the voltage developed accross the shunt resistor is applied directly to the A/D converter at 200 millivolts full scale.

2) In the ACV mode, ac inputs pass through the attenuator on all ranges. The input is scaled to 2 volts rms full scale, including X10 amplification for the 200 millivolt range. The TRMS converter outputs a positive dc signal proportional to the true root mean square ac signal. This DC signal is the A/D input. In the ACA mode, shunt voltage is treated as a 200 millivolt signal.

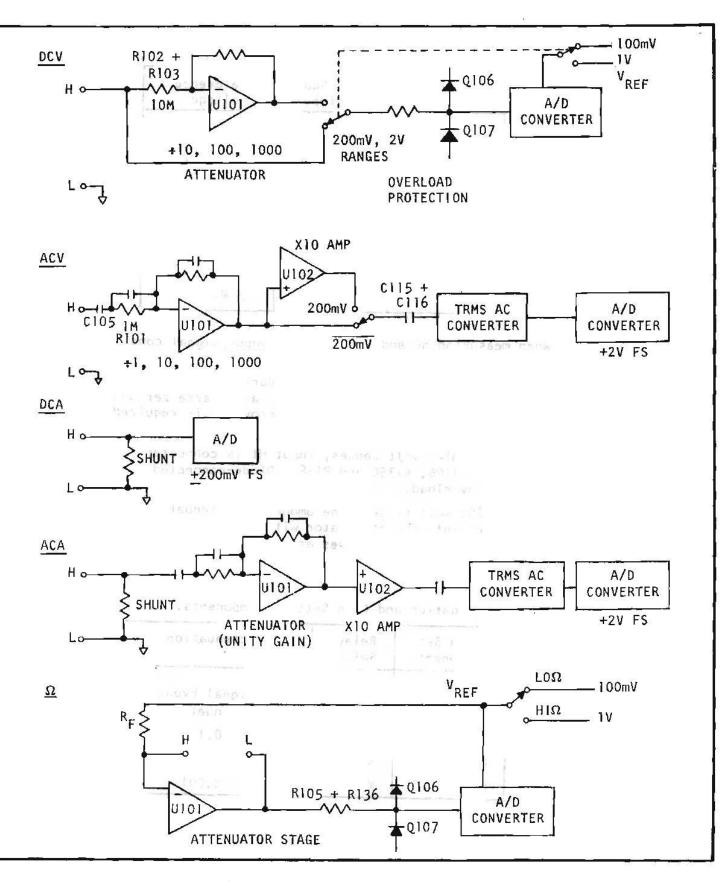
<u>b.</u> Ohms Conversion. Resistance measurements are made by configuring the attenuator as a resistance-to-voltage converter. Attenuator stage voltage feedback resistors R_f function as amplifier input resistance connected to either the 0.1 volt reference (LO) or the 1.0 volt reference (HI). The unknown resistance is connected as a feedback resistor around the attenuation amplifier. The resulting voltage applied to the A/D converter is proportional to the unknown resistance.

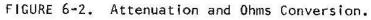
c. A/D Converter. The A/D converter is a large scale integration (LS1) ratiometric device. Converter output is a multiplexed 5 digit binary coded decimal (BCD) number which is equal to the ratio of input voltage to reference voltage. A separate clock circuit supplies a 100 kHz timing input to the integrated circuit, which also multiplexes the BCD output. Full scale A/D inputs for various ranges and functions are listed in Table 6-1.

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Function	Range	Full Scale A/D Input	Reference Voltage
DCV	200 mV	200 mV	0.1 V
DCV	2, 20, 200 1200 V	2 V	1.0 V
ACV	A11	2 V	1.0 V
DCA	A11	200 mV	0.1 V
ACA	A11	2 V	1.0 V
Ω	HL	2 V	1.0 V
	LO	200 mV	0.1 V

TABLE 6-1. Full Scale A/D Inputs.

6-3. ATTENUATION. When measuring ac and dc voltages, input signal conditioning is provided by inverting amplifier U101 and additional components as described below.

<u>a.</u> DC. Input resistance is set by R102 and R103. During calibration, R103 is adjusted to obtain a total input resistance of 10 M Ω . Both fine and coarse zero adjustments are provided since an amplifier output resolution of 10 microvolts is required for L0 resistance measurements.

1) On the 2 volt and 200 millivolt ranges, input HI is connected to the A/D converter through protection resistors R106, R135G and R136. Diode-connected FET's Q106 and Q107 clamp the A/D input during overload.

2) On the 20, 200 and 1200 volt ranges, the amount of attenuation is selected by switching feedback resistors into the attenuator with relays K101, K102 and K103. Gain setting components and attenuation values are listed in Table 6-2.

Range	Gain Set Relay/ Components Switch		Attenuation
200 mV 2 V			Signal bypasses attenuator
20 V	R118, R126	K101	0.1
200 V	R119, R127	K102	0.01
1200 V	R120, R128 🛒	к103	0.001

	TAE	BLE 6-	-2.	
DC Attenuation	and	Gain	Setting	Components.

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<u>b. AC Volts.</u> Input resistance is $M\Omega$ (R101). Shunt capacitance is typically less than 75 pF. Additional conditioning is as follows.

1) For all ranges except the 200 millivolt range, the amount of attenuation is selected by switching feedback resistors into the attenuator with relays K101 through K104. For the 200 millivoit range, non-inverting X10 amplifier UI02 boosts the signal to 2 volts full scale. Gain setting components and attenuation values are listed in Table 6-3.

TABLE 6-3.

Range Gain Set Components		Relay Energized	Attenuation	Freq. Comp. Capacitors	00 k 20 MG	
200 mV	R118, R126	K101.0	1 (X10*)	c106, c111		
2 V	R118, R126	K101		C106, CITI		
20 V	R119, R127	K102	0.1	C106, C112	l –	
200 V	R120, R128	K103	istance [0:0 uremuther	c106, C113	<u>, 1</u>	
1000 V	R121, R122, R129	s co401X te	from gr100.0 and described below	c106, C114	push at L0 1	

* Signal applied to X10 ac amplifier U102.

arrent flow in the unknown resistance is from input H1 to input to. At Function, the voltage across R_{χ} is either 2 volts (H1) or 200 millivolts (L0). Hofe

ource loading does not affect accuracy since the A/D converter is rationet in

2) On the 200 millivolt and 2 volt ranges, high frequency compensation is adjusted with capacitor Clll, as shown in Table 6-3. On the 20 volt range, adjustment is per-formed with Cll2. On the 200 and 1000 volt ranges, adjustment is performed with Cl06. Some low frequency rolloff is introduced by input blocking capacitor Cl05, and ac converter input capacitors C115 and C116. measurements, this cu

6-4. AC CONVERSION. The ac converter is a monolithic TRMS module. Output $V_{dc} = \sqrt{Avg(Vin)^2}$ Potentiometer R143 provides gain adjustment, and R142 establishes output zero. Settling time and ripple are determined by C110 and C120. Low frequency rolloff is a A/D CONVERTER. The A/D converter operates on the dual slope principle

OHMS CONVERSION. During calibration, the 10 M Ω input resistance (R102 and R103) 6-5. and all attenuator feedback resistors are adjusted for both ratio and absolute value. Therefore, these resistors can also serve as reference (current setting) for resistance measurements. In the Ω mode, the attenuation (feedback) resistors are disconnected from the output of the attenuation amplifier (U101) and are connected instead to the A/D converter reference voltage. Since two reference voltages and two A/D converter gains are available, the Model 4002 DMM provides the option of measuring resistance with the sense current reduced by a factor of 10.

a. Range Selection. Operation of the range pushbuttons selects range resistors to provide the reference current listed in Table 6-4. Operation of the HI/LO pushbutton selects the 1 volt or 0.1 volt reference, respectively. Relay K105 is always energized A/B converter exhibits high normal mode rejection for ac signals in multishom Q and ni is, particularly the 50 and 60 hertz line frequencies.

Range	Range Resistors	Relay/Switch	Nom. I _{REF} in H1 Ω	Nom. I _{REF} in LO Ω
2 kΩ	R121, R122, R129	к104	-	100µA
20 kΩ	R120, R128	K103	100µA	10µA
200 kΩ	R119, R127 00000	к102	10µA	lμA
20 00 kΩ	R118, R126	KI01 Velan	lμA	0.1µA
20 MΩ	R102, R103	1000 switch,	0.1µA	-
	eut (+0)	pins 17, and Ω 8, 9.		

TABLE 6-4, Resistance Range Setting Components

<u>b.</u> Ω Circuit. For resistance measurements, relay K105 and terminals 4, 5, and 6 of the Ω pushbutton connect the input HI terminal directly to the amplifier summing node. Input L0 is disconnected from ground and is connected to the A/D converter input through the protection components described below. The unknown resistance (R_X) then becomes the amplifier feedback resistance.

1) Current flow in the unknown resistance is from input HI to input LO. At full scale, the voltage across R_x is either 2 volts (HI) or 200 millivolts (LO). Reference source loading does not affect accuracy since the A/D converter is ratiometric.

2) The HI terminal is clamped to analog common by Q101 and Q102. The instrument protection network at the amplifier output consists of a pulldown resistance (R104 and CR103, CR104 and CR105). R104 sinks approximately 150 microamps. During in-range measurements, this current is supplied by the reference voltage through CR105 and voltage through the amplifier (U101) and CR104. Overloads with input HI positive are sustained by CR105; diodes CR103 and CR104 sustain negative overloads. Open circuit voltage is set to less than 5V by R150 and R151 through CR103 and CR105. A/D protection in Ω is the same as in V except R105 is substituted for R106.

6-6. A/D CONVERTER. The A/D converter operates on the dual slope principle. The timing is divided into three periods as described below. Operation with high and low reference voltages is described separately in subparagraph d.

<u>a. Auto-Zero.</u> The auto-zero period (A, Figure 6-3) is 100 milliseconds in length, which corresponds to 10,000 clock pulses. During this period, reference voltage V_{REF} (see subparagraph d) is stored on capacitor C124. Capacitor C117 stores V_{REF} + V_{OS1} - V_{OS2} .

<u>b. Signal-Integrate.</u> The signal-integrate period (B, Figure 6-3) is 100 milliseconds in length. The A/D input is buffered by U104 (see subparagraph d) and integrated by U103. Positive signals generate a negative-going ramp at the integrator output (pin 14), while negative signals produce a positive-going ramp. The level of the integrated signal at the end of the signal-integrate period is proportional to the average of the applied signal during this period. Since signal integration continues for 100 milliseconds, the A/D converter exhibits high normal mode rejection for ac signals in multiples of 10 hertz, particularly the 50 and 60 hertz line frequencies. c. Reference-Integrate. The reference-integrate period (C or D, Figure 6-3) is 200 milliseconds or 20,000 counts in length. During this period, the integrator is returned to baseline level by applying a reference voltage of a polarity opposite to that of the signal. A positive-going ramp is obtained by grounding the buffer input, while a negative going ramp is produced by the integration of 2 x V_{REF} (that is, V_{REF} + the voltage stored on C124). The time, or number of clock pulses required for discharge is proportional to the signal input. Digital output is from latches within U106 which store the number of clock pulses required for the integrator to return to baseline level. The maximum count during this period is 20,000 which corresponds to a discharge period of 200 milliseconds or full scale input.

<u>d.</u> Reference Voltages. Reference voltage V_{REF} may be either 1 volt or 0.1 volt. Switching through the pushbuttons turns on either Q104 (for 1 volt) or Q103 (for 0.1 volt). The voltages are provided by a divider across a temperature compensated zener diode. An operational amplifier on U103 provides the zener with a self-regulating bias. Use of the 0.1 volt reference increases converter sensitivity to 200 millivolts full scale, permitting accurate L0 ohms operation, 10 microvolt resolution on dc voltage measurements, and dc amperage measurements with a full scale burden of 200 millivolts. Increased sensitivity is accomplished by switching input buffer U104 into a gain-of-10 configuration by turning on Q105. Auto-zero charging on C124 is to a 100 millivolt reference instead of a 1 volt reference. Integrator and comparator voltage levels are unaffected by buffer gain. Buffer offset voltage is zeroed, and resistors R146 plus R144 or R145, which are selected at test, null any remaining zero offset on the 200 millivolt range.

6-7. DISPLAY. Five light-emitting diodes (LED) are driven by U201, which is a CMOS BCDto-seven segment decoder/driver with bipolar current-sourcing outputs. Segment currents are limited to approximately 20 milliamperes peak by resistor network R202. The LED readout is a multiplexed, common-cathode configuration with Darlington array U202 sequentially sinking current from each digit. Blanking of the overrange digit is accomplished by gates U107A and U107B. Emitter-follower Q108 ensures that CMOScompatible levels are maintained on U107A, pin 1, regardless of the loading of U202. The minus polarity readout is blanked on ac voltage and resistance ranges by contacts on the pushbutton switch. Proper decimal point position is determined by the combination of function and range selected.

6-8. CURRENT MEASUREMENTS. In the A mode, the signal is switched into one of five current shunts ahead of the attenuator section. For dc current measurements, the shunt voltage drop is applied directly to the A/D converter input at 200 millivolts full scale. For ac current measurements, the shunt voltage drop is treated as a 200 millivolt ac signal and passes through the ac attenuator and the X10 ac amplifier. Overload clamping occurs at three diode drops which is a level high enough to permit high crest factor current waveforms.

6-9. AC POWER SUPPLY. When the DMM is operated from ac line power, the power supply furnishes +5, +15, and -15 volts from regulators VRI04, VRI02 and VRI01, respectively. Full-wave rectified ac from bridge rectifiers CRI01 and CRI02 is filtered by reservoir capacitors Ci08, Ci04 and Cl03 and is applied to the linear voltage regulators.

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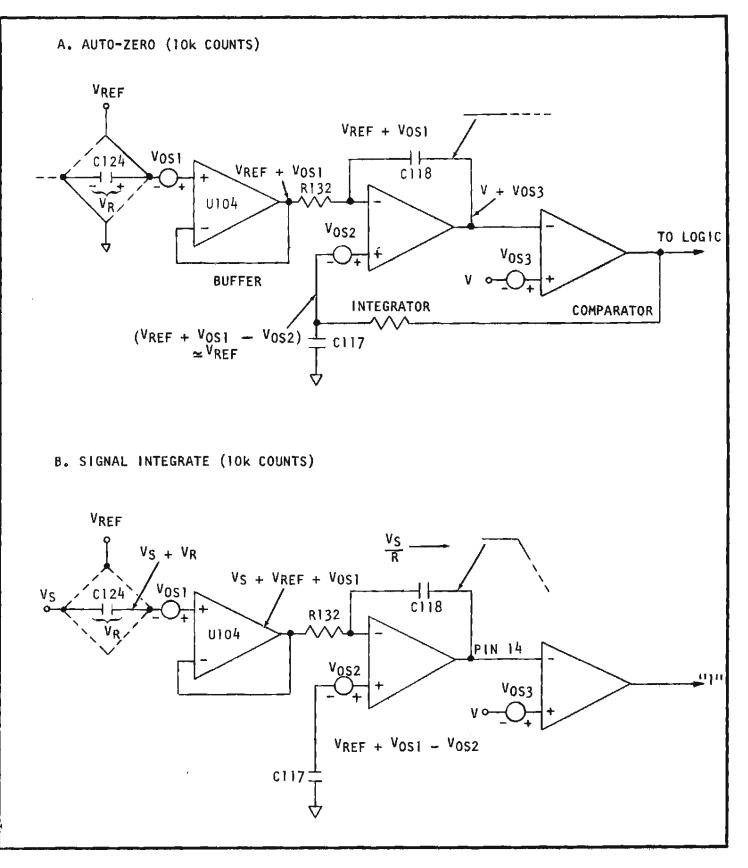


FIGURE 6-3. A/D Converter Function (Sheet 1 of 2)

THEORY OF OPERATION

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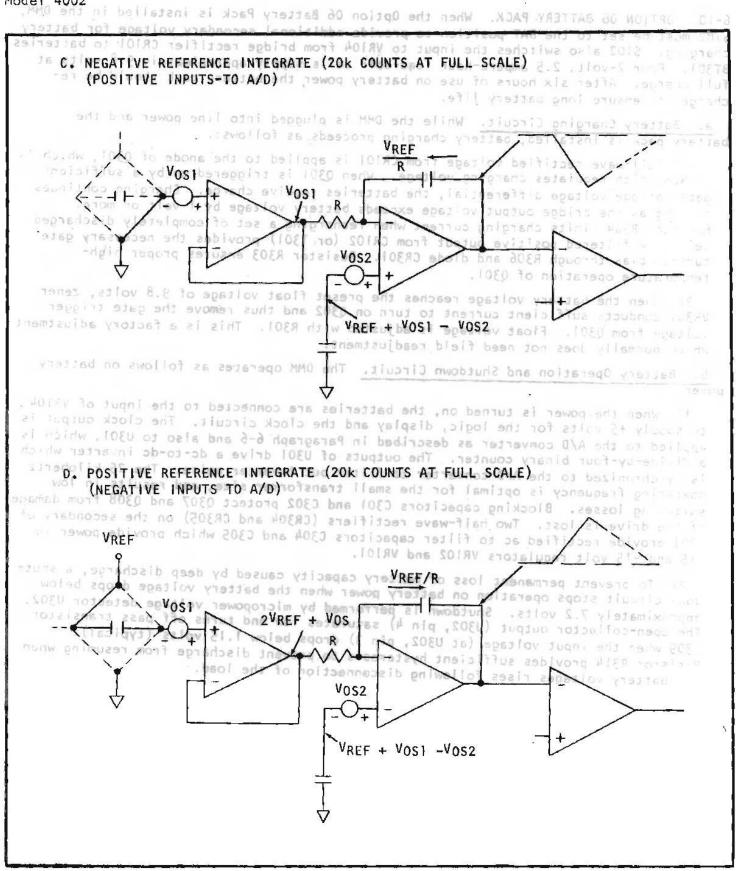


FIGURE 6-3. A/D Converter Function (Sheet 2 of 2)

6-10. OPTION 06 BATTERY PACK. When the Option 06 Battery Pack is installed in the DMM, Sl02 must be set to the BAT position to provide additional secondary voltage for battery charging. Sl02 also switches the input to VRI04 from bridge rectifier CRI01 to batteries BT301. Four 2-volt, 2.5 ampere-hour lead-acid cells supply approximately 9.8 volts at full charge. After six hours of use on battery power, the battery pack should be recharged to ensure long battery life.

a. Battery Charging Circuit. While the DMM is plugged into line power and the battery pack is installed, battery charging proceeds as follows:

1) Full-wave rectified voltage from CR101 is applied to the anode of Q301, which is an SCR which regulates charging voltage. When Q301 is triggered on by a sufficient gate-cathode voltage differential, the batteries receive charge. Charging continues as long as the bridge output voltage exceeds battery voltage by 1 volt or more. Resistor R304 limits charging current when recharging a set of completely discharged cells. A filtered positive output from CR102 (or T301) provides the necessary gate turn-on bias through R306 and diode CR301. Resistor R303 ensures proper hightemperature operation of Q301.

2) When the battery voltage reaches the preset float voltage of 9.8 volts, zener VR301 conducts sufficient current to turn on Q302 and thus remove the gate trigger voltage from Q301. Float voltage is adjusted with R301. This is a factory adjustment which normally does not need field readjustment.

b. Battery Operation and Shutdown Circuit. The DMM operates as follows on battery power:

1) When the power is turned on, the batteries are connected to the input of VR104 to supply +5 volts for the logic, display and the clock circuit. The clock output is applied to the A/D converter as described in Paragraph 6-6 and also to U301, which is a divide-by-four binary counter. The outputs of U301 drive a dc-to-dc inverter which is synchronized to the A/D converter to filter out inverter noise. The 25 kilohertz operating frequency is optimal for the small transformer size, and results in low switching losses. Blocking capacitors C301 and C302 protect Q307 and Q308 from damage if the drive is lost. Two half-wave rectifiers (CR304 and CR305) on the secondary of T301 provide rectified ac to filter capacitors C304 and C305 which provide power to +15 and -15 volt regulators VR102 and VR101.

2) To prevent permanent loss of battery capacity caused by deep discharge, a shutdown circuit stops operation on battery power when the battery voltage drops below approximately 7.2 volts. Shutdown is performed by micropower voltage detector U302. The open-collector output (U302, pin 4) saturates low and turns off pass transistor Q309 when the input voltage (at U302, pin 3) drops below 1.15 volts (typical). Resistor R314 provides sufficient hysteresis to prevent discharge from resuming when the battery voltages rises following disconnection of the load.

VREF + V051 -V052

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SECTION 7. REPLACEABLE PARTS

7-1. GENERAL. This section contains information for ordering replacement parts. The parts list is arranged in alphabetical order of their Circuit Designations.

7-2. ORDERING INFORMATION. To place an order or to obtain information concerning replacement parts, contact Racal-Dana Instruments, Irvine, California 92713, (714) 833-1234. When ordering, include the following information:

- a. Instrument Model Number, (Page 7-11) and instrument Model Number,
- c. Part Description.
- d. Circuit Designation (if applicable).
- e. Part Number.

7-3. MAINTENANCE KIT. A Maintenance Kit is available that contains a complement of spare parts that will maintain up to ten Model 4002 DMMs. Specify Part Number 29197A when ordering.

Qty.	Part Number	Schematic Callout
1	C-294-4	C124
2	DD-16	DS202, 203, 204, 205
τ	DD-17	D\$201
5	FU-13	F102
2	FU-20	F101
1	10-93	VR104
1	10-102	V10 7
2	10-165	U101, 102
1	10-168	U201
T	10-169	U202
1	10-170	VR102
1	IC-174	VRIOI
1	LSI-11	U106
1	LS1-12	U103
2	RL-59	K101, 102, 103, 104, 105
2	TG-128	0101, 102, 106, 107

TABLE 7-1. Model 4002 Maintenance Kit.

REMARKANT PART

7-4. SCHEMATIC. memebe

a. Model 4002 4-1/2 Digit TRMS Multimeter: Schematic No. 28992E (Page 7-10). This schematic also describes the Option O6 Rechargeable Battery Pack.

7-5. COMPONENT LAYOUT.

a. Model 4002 4-1/2 Digit TRMS Multimeter (Page 7-11).

b. Option 06 Rechargeable Battery Pack (Page 7-13).

is available that primains a or model 4002 mmms. Specify Part N

TABLE 7-1.	
Aug/ McIntenstera	

Schemet (fallout		nadmus (miss)
4513		
05202, 201; 204, 205		3°-04
D5201		
F 102		
1013		
40184	1	28
Ze (V		
	1	
0201		
		eði-
$+r_{c}\tau \neq q$		
		21-1
, 801 . 5		
301		
	1.1	

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Digital Multimeter

Model 4001 Model 4002

TABLE 7-1. 3184

Cross-Reference of Manufacturers

MFR. CODE	NAME AND ADDRESS	FED. SUPPLY CODE	MFR. CODE NAME AND ADDRESS	FED. SUPPLY CODE
A-B	Allen-Bradley Corp Milwaukee, Wl 53204	01121	DLE Dale Electronics Inc. Columbus, NE 68601	91637
A-D	Analog Devices Inc. Norwood, MA 02026	24355	DTN Dielettron (Consolidated) New York City, NY 10013	1.08) *64 : 819
ACI	American Components, Inc. Conshohocken, PA 19428	14298	EC1 Electro Cube Inc. San Gabriel, CA 91776	14752
AMP	Amphenol Broadview, IL 60153	02660	EDI Electronic Devices, Inc. Yonkers, NY 10710	83701
АРХ	Amperex Elkgrove Vlg, IL 60007	73445	EFJ E. F. Johnson Co. Waseca, MN 56093	74970
BEC	Beckman Inst. Inc. Fullerton, CA 92634	73138	ERI Erie Technological Prod. Erie, PA 16512	72982
BLD	Belden Mfg. Co. Chicago, IL 60644	70903	F-1 Fairchild Inst. Corp: Mountain View, CA 94043	07263
BRG	Berg Electronics Inc. New Cumberland, PA 17070	22526	FUS Bussman Mfg. (Fusetron) St. Louis, MO 63107	71400
BRN	Bourns, Inc. Riverside, CA 92507	80294	G-E General Electric Company Syracuse, NY 13201	03508
BUS	Bussman Mfg. Div. St. Louis, MO 63017	71400	G-1 General Instrument Corp. Newark, NJ 07104	72699
2-1	Components, Inc. Biddeford, ME 04005	06751	GLD Gould, Inc. St. Paul, MN 55165	52431
-W-2	Continential-Wirt Elec. Corp. Warminster, PA 18974	79727	H-P Hewlett-Packard Palo Alto, Ca 94304	50434
CAD	Caddock Riverside, CA. 92507	19647	INT Intersil Inc. Cupertino, CA 95014	32293
AN	ITT Cannon Electric Santa Ana, CA 92702	71468	IRC IRC Division Burlington, 1A 52601	07716
CLB	Centralab Division Milwaukee, WI 53201	71590	L-F Littlefuse, Inc. Des Plaines, IL 60016	75915
LR	Clarostat Mfg. Co., Inc. Dover, NH 03820	12697	MOL Molex Downers Grove, JL 60515	27264
CTS	CTS Corporation Elkhart, IN 46514	71450	MOT Motorola Semi Prod. Inc. Phoenix, AZ 85008	04713
DIC	Dickson Electronics Corp. Scottscale, AZ 85252	12954	NAT National Semi Corp. Santa Clara, CA 95051	27014

7-4

REPLACEABLE PARTS

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5**4**

Digital Multimeter

Model 4001 Model 4002

MFR. CODE	NAME AND ADDRESS	FED. SUPPLY CODE	18	NAME AND ADDRESS	FED. SUPPLY CODE
NCI	National Components, Inc. West Palm Beach, FL	846 - 34	TEP	Tepro Electric Corp. Rochester, NY 14606	02985
NEL	Northern Engr. Labs Burlington, Wi 53105	00815	TPL	Temple Tecate, CA 92080	29505
P&B	Potter & Brumfield Princeton, IN 47670	12300	TRW	TRW Capacitor Div. Ogallala, NB 69153	84411
РАК	Paktron Vienna, VA 22180		VIS	Vishay Resistor Products Malvern, PA 19355	18612
POM	Pomona Electric Pomona, CA 91766	05276	VRN	Vernitron Laconia, NH 03246	13150
QTN	Q-Tron Santa Ana, CA 92705	25525	WAB	Wabash-Magnetics Wabash, IN 46992	01101
R→D	Racal-Dana Instruments Irvine, Ca. 92713	21793			
RAY	Raytheon Company Quincy, MA	94144			
RCA	RCA Corporation Moorestown, NJ 08050	02734			
RCL	RCL Electronics, Inc. Manchester, NJ 03102	01686			
SIE	Siemens Corporation Iselin, NJ 08830	25088			
SIG .	Signetics Corp. Sunnyvale, CA 94086	18324			
SIL	Siliconix Inc. Santa Clara, CA 95054	17856			
SPG	Sprague Electric Co. Visalia, CA 93278	14659			
SOL	Solitron Devices Inc. San Diego, CA 92123	22229	E.F.		
STD	Standard Condensor Chicago, IL	97419	6		
T-1	Texas Instruments, Inc. Dallas, TX 75231	01295			
TEL	Tel Labs Manchester, NH 03102	94322	0.000		

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REPLACEABLE PARTS LIST

	-118 150 G	Ocsig.	shed				(A	DeLoript o	10.11
Circuit Desig.	Denne Bieten	P 2 89 1	1-7	Sch.	PC-Board	Mfr.	Mir.		
vesig.	Description	A 1 PM 1		Location	Location	Code	Desig.	The second se	001
	54 18	30.37	:03	CAPACIT	ORS (C)		750A. 75V	Roctific .	
	11 16	OPA2			h. 28992E-Po	7-10)	1. (Figs., 54, 5		11.0
		Lieur	100	(PC-Board	448-Pg. 7-11	1	3 50V.		\$1.1
				(i o aoaro		1	- V25 "A=2"	1.14	21.1
C101	4.7µF, 20V, ETT	1.	01-1	-D-750-1	D-4	NCL	KNS475A020K	C-179-4.7	
C102	4.7µF, 20V, ETT	1 (A (A (A (A))))		D-7	D-4	NCI	KNS475A020K	C-179-4.7	
0103	470µF, 35V, EAL			C-8	D-4	NIC	35ELA470	C-289-470	
c104	470µF, 35V, EAL			C-8	E-4	NIC	35ELA470	C-289-470	
C105	0.10F, 1000V. MP	F • • • (@)(•		C-3		STD	M2W-F-0.1uF	C-285-,)	1.961
C106	.25-1.5pF, 2000W	, Teflon Trin	mer	D-2	F-4	EFJ	273-101	C184	
C107	1000pF, 500V, ±	5%, Polystyre	ene 📲 .	D-3	F-4 6-3	CLB	CPR-1000	C-138-1000	
8013	2200µF, 15V, EAL	4 (g)()		C-7	D-3 8-3	NAC	16FLA2200	C-290-2200	1994
6109	3.3pF, ±0.5pF,	50VDC, CerD		E-2	E-3	NAC	DT200-3R3	C-291-3.3P	
C110	1µF, 100V, ±102				E-3	POT	4309C-105K	C-294-1	
GETI	.25-1.5pF, 2000V				erEz3ero	EFJ	273-1-1	C-184	
C112	1.9-15.8pF, 250V	, Trimmer -		D-200055	. Er3 antina	EFJ	187-0109-005	C-284	
C113	1100pF, 500VDC,	±1%, Silver	Mica .	0-2	10 E=3	G-1	RDH19F0112F03	C-278-110P	
C114	1100pF, 500VDC,				F-3	G-1	RDM19FD111F03	C-278-1100F	>
C115	33µF, 15V, ETT.	· · · · · · · · · · · · · · · · · · ·	C. A. 101 -	F-2so8-34	E-2	NCI	KNS336D015K	C-228-33	
CI 16	33µF, 15V, ETT.				ET2 1000	NC1	KNS3360015K	C-228-33	
C117	1µF, 100V, ±10%,				D-2	POT	4309C-105K	C-294-1	10125
C118	.22µF, 200VDC,			J-2	0-2 (+M	POT	22-200-10-X363UW	C-26922	
0119	NOT USED	FN0560	1-7	D-2	H-7		1.1.1.1.1.0.071	Lingungal -1	5052
C120	10F, 100V, ±10%				D-3 (-M	POT	4309C-105K	C-294-1	
C121	4.7uF, 20V, ETT				D-2 5-6	NCI	KNS475A020K	C-179-4.7	POSE
C122	4.7µF, 20V, ETT				D-2 (-0	NCI	KNS475AD20K	C-179-4.7	2005
6123	. LuF, 200V, 20%,				E-2	ECI	625B1C104	C-2211	12.163.0
C124	4µF, 100V, 20%,				E-1	POT	0109-5432	C-294-4	
C125	100pF, 1000V, Ce				F-2	CL8	00-101	C-64-100P	
			(01-5-1	289926-P	Series (Sch	1001			
		530		Series (S	ch. 28992E-F 449-Pg. 7-1	g. 7-10)		
C201	4.7µF, 20V, ETT	<u>pien0</u>	#002	R-7 1000 J	Faiteau	NCI	KNS475A020K		gien:
	01-30 £1-63	MDL 312002		(PC-Board	ch. 28992E-F 451-Pg. 7-1	131	+810, 1/84, 250v. (250v, 346	Fuse sto Fuse, 24,	101
301	4.70F, 35V, EAL		01-5 -10	. 28992L-P	Serles Serles	NCI	KNS475A020K	C-179-4.7	
5302	4.74F, 35V, EAL			F-7	2-3	NCI	KNS475A020K	C-179-4.7	
C303	1.0µF, 250V, MPY		с на на на	E-7	E-3 3-1	AMP	C280AE/AIN		10F
0304	100µF, 35VC	an an an an annaichte An an an an annaichte		F-8	F-4	NIC	35-ULA-100	C-295-100	
C305	100µF, 35VC		1-1.05	F-Balls	F-3	NIC	35-ULA-100	C-295-100	
		. 174	~ hit	DIODE	S (CR)	120.00	×.		
	P#r5_100	Desig.	"100"	Series (S	ch. 28992E-P	9. /-10	,		olewd

				(PC-Board	448-Pg. 7-	11)		00125	DARCE	P1202
Circuit Desig.	Description	22-15+2 22-15+2	MOL	Sch. Location	PC-Board Location	Mfr. Cod <u>e</u>	Mfr. De <u>sig</u> .	algnA sagia	Part No.	10011
GRIDI	Bridge Rectifier, 1	100V. 2A .	104	· C-6 2-0	D-5 8-0	EDI	PDIO		8F-36	20011.
CRIO2	Bridge Rectifier,				0-5 2-6	EDI	PF40	dealer days	RF-46	40011
CR103	Silicon Rectifier,	1A, 1000V.	15019	· E-3	F-5 (-A	1-1	114007	had line!	RF-50	50011
CRIO4	Silicon Rectifier,				F-5	T-1	114007	14000	RF-50	1001
CR105	Silicon Rectifier,	1A, 1000V.	6 1	· E-3	F-5	1-1	114007	0.64	RF-50	
CRIOG	Rectifier, 75mA, 75	SV	13 - 61	· D-8	0-3	T-4	IN914		RF-28	
CR107	Rectifier, 75mA, 75	5V		· J-2 00 2	C-1	1-T	11914		RF-28	
CR108	Rectifier, 75mA, 75	54		. J-2	C-2	307-1	IN914		RF-28	10
			- (1)	1-5 .07-801 3	[FL-BANK		-			

Martin Contractor	hi ni Cade Da	- 11- 1 - 1 - 1	Sch. Locatian	Osaci prico	1100710
- 40097 81-46 - 40097 81-56 - 40102 81-59 - 40103 81-59 - 40102 81-59	10 F07 10 T05 10 T05	4-3 1-3	8-1 8-8 8-4 8-4 8-4	57, Reed Type 57, Reed Type 57, Reed Type 57, Reed Type 57, Reed Type	#10 #102 #103 #104 #104

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DIODES (CR) (CONIT)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
R109	Rectifier, 75mA, 75V,	. к-з	E-2	T-1	18914	RF-28
CRIIO	Rectifier, 75mA, 75V		E-2	T-1	18914	RF-28
CR111	Bridge Rectifier, 5A, 50V	. A-3 0 2	G-2	103	PEOS	RF-48
R112	Rectifier, 3A, 50V	A-4 shield	6-2	SOL	3A50	RF-34
CR113	Rectifler, 75mA, 75V- · · · ·	. H-I	C-2	1-1	11914	RF-28
	500	Series (Sch (PC-Board	451-Pg. 7-13			
10301	Rectifier 75ma 75V.	E-6	0-4	T-1	INDIA	8F-28
CR301	Rectifier, 75mA, 75V		D-4	T-1 T-1	1N914 1N914	RF-28 RF-28
R302	Rectifler, 75mA, 75V	. 8-7	E-3	T-1	18914	RF-28
(R301 (R302 (R303 (R304		. 8-7 . 8- <u>7</u>				

DISPLAYS (DS) "200" Series (5ch. 28992E-Pg, 7-10) (PC-Board 449-Pg, 7-12)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Nfr. Code	Mfr. Desig.	Part No.
D\$201	±1 LEO Digit	H-7	C-2	F-1	FN0561	0D-17
DS202	7-Segment LED Digit	. H-7	D-2	5-1	FN0560	DD-16
DS203	7-Segment LED Digit	. н-7	D-2	F-1	FND560	DD-16
DS204	7-Segment LED Digit	. J-7	D-2	F-1	FN0560	00-16
DS205	7-Segment LED Digit	, J-7	E-2	F-I	FND560	DD-16

FUSES (F) "100" Serles (Sch. 28992E-Pg.7-10) (PC-Board 448-Pg.7-11)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
F101 F102	Fuse, Slo-Blo, 1/8A, 250V, 3AG Fuse, 2A, 250V, 3AG		0-4 F-3	BUS LIT	MDL 312002	FU-20 FU-13
			n. 28992E-P			
F301	2A, 250V, 3AG, Quick	F-6	C-3	L-F	312002	FU-13
	**00ft*		DRS (J) ch. 28992E-1 3 449-Pg.7-)	
Circuit Desig, -	01000 Description	Serles (So	ch. 28992E-1) Mfr. Desig.	_ Part N <u>o.</u>
Desig, .	Description	Series (So (PC-Board Sch. Location	ch. 28992E-1 3 449-Pg.7- PC-Board Location	12) Hfr. Code	Mfr. Desig.	ne com a
		Series (So (PC-Board Sch. Location H-7	ch. 28992E-1 3 449-Pg.7- PC-Board	12) Hfr.	ßfr.	Part No. CS-348-2 CS-348-1
Desig J1001	Description	Series (So (PC-Board Sch. Location H-7 H-6	ch. 28992E- 3 449-Pg.7- PC-Board Location B-3	12) Hfr. <u>Code</u> MOL	Hfr. Desig. 22-15-2111	CS-348-2 CS-348-1 CS-287-3
Desig J1001 J1002 J1003 J1004	Description Il-Pin Right Angle	Series (So (PC-Board Sch. Location H-7 H-6 B-8	ch. 28992E- 3 449-Pg. 7- PC-Board Location B-3 F-3	MFr. Code MOL HOL	Mfr. Desig. 22-15-2111 22-15-2061	C5-348-2 C5-348-1
Desig J1001 J1002 J1003	Description li-Pin Right Angle	Series (So (PC-Board Sch. Location H-7 H-6 B-8 D-8 A-5	ch. 28992E-1 3 449-Pg.7- PC-Board Location B-3 F-3	HFr. Code MQL MQL MOL MOL	Mfr. Desig. 22-15-2111 22-15-2061 2139-3	CS-348-2 CS-348-1 CS-287-3

RELAYS (K) "100" Series (Sch. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit Desig.	_ Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
K101	5V, Reed Type	. В- 8	E-4	сот	UF-40097	RL-56
K102	5V, Read Type		Ē-4	COT	UF-40097	RL-56
K103	5V, Reed Type		E-4	COT	UF-40102	RL-59
K104	5V. Reed Type	, A-8	F-4	COT	UF-40102	AL-59
K-05	5V, Reed Type		F-4	COT	UF-40102	RL-59

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CONNECTORS (P) "1000" Series (\$mh. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circult Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig	Part No.
P1001	6-Pin	H-7	G-5	MOL	22-03-2061	CS-247-1
P1002	11-Pin	H-6	G-2	MOL	22-03-2061	CS-347-2
P1003	MALE	в-8	C-5	MOL	A-2391-3A	CS-288-3
P1004	MALE	0-5	D-3	MOL	A-2391-8A	CS-288-8
P1005	NOT USED					
P1006	NOT USED					
P1007	Line Cord	в-8		R-D		CO-9

TRANSISTORS (0) "100" Series (Sch. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig	Part No
0101	N-Chan, JFET	. 0-3	E~4	INT	ITE4392	rg-77
Q102	N-Chan, JFET		E-4	INT	I TE4392	TG-77
0103	N-Chan, JFET.	. H-2	C-2	ENT	ITE4392	TG-77
0104	N-Chan, JFET	. H-2	C-2	INT	ITE4392	TG-77
0105	N-Chan, JFET.	- J-3	D-2	INT	1TE4392	TG-77
Q106	N-Chan, JFET		E-2	8-D		TG-128
0107	N-Chan, JFET	. G-4	E-2	R-D		TG-128
8019	NPN, Switch		G-2	MOT	2×3904	TG-47

"300" Series (Sch. 28992E-Pg. 7-10) (PC-Board 451-Pg. 7-13)

0301	Thyristor, SCR	C-4	MOT	106F1	TG-132
0302	NPN, Switch	C-3	MOT	2N3904	TG-47
0303	NPN, Switch	D-4	MOT	2N3904	T6-47
0304	NPN, Switch	E - 4	MOT	2N3904	7G-47
Q305	NPN, Switch	E-4	HOT	2N3904	TG-47
Q306	PNP, Silicon, TO-92 Case · · · · E-7	E-4	R-D		TG-53
Q307	NPN, Switch	E-3	MOT	2N3725	TG-131
0308	NPN, Switch	E-3	MOT	2N3725	TG-131
Q309	PNP, Silicon E-6	D-3	MOT	MPS-WAS	TG-133
Q310	PNP, Silicon, TO-92 Case E-6	0-3	R-D		TG-53

RESISTORS (R) "100" Series (Sch. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit Desig.		Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
R101	1MS, ±0.5%, 2W, MtF)-3	E-5	ACI	PME80T9	8-267-1M
R102	9.88M, ±0.5%, .5W, 1200V, MtF		Ē-4	TRW	AR90T10	R-265-9.88M
R103	200KΩ, 10%, Cermet Trimmer		8-5	BEC	89P	RP-89-200K
R104	100K9, 10%, 2W, Comp		F-5	A-B	НВ	R-2-100K
R105	47KΩ, 10%, 2W, Comp		F-5	A-8	нв	R-3-47K
R106	47KΩ, 10%, 2W, Comp		G-5	A-B	НВ	R-3-47K
R107	100Ω, 10%, Cermet Trimmer	1-1	C-3	BRN	30-69-P	RP-64-100
R108	2000, 10%, Cermet Trimmer J		C-3	BRN	30-69-P	R-64-200
R109	MATCHED SET WITH VR105	1-1	C-3	TRW	MAR-5	R-263-99.8K (28798A)
RIIO	MATCHED SET WITH VRI05	1-1	C-3	TRW	MAR-5	R-263-4.59K (28798A)
R111	931Ω, 1%, 1/8W, MtF J	1-1	C-3	IRC	CEA-TO-931	8-88-931
R112	50KΩ, 10%, Cermet Trimmer E	- 3	0-3	BEC	72PMR	RP-97-50K
R113	200KΩ, 1%, 1/8W, MtF D)-3	D-3	180	CEA-T0-200K	R-88-200K
R114	1.8M, 10%, 1/4W, Comp D	2-3	D-3	MEP	CR25. 5%	R-76-1.8M
R115	1000, 1%, 1/8W, MtF	1-3	D-3	IRC	CEA-T0-100	R-88-100

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RESISTORS (R) (CON'T) "100" Series (Sch. 28992E-Pg. 7-10) (PC-Board 448-Pg. 7-11)

Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Kfr. Desig.	Part No.
R116	4.99KΩ1%. 1/LOW. HtF	. E-2	0-3	TRW	MAR-5, T13	R-263-4.99K
R117	44.9KR, .12, 1/10W, MEF		D-3	TRW	MAR-5, T13	R-263-44.9K
R118	998Kg, 1%, 1/4W, MtF		E-3	TRW	MAR-7, T13	R-264-998K
R119	99.8kΩ, .1%, 1/4W, MtF.		E-3	TRW	MAR-5, 713	R-263-99.8K
R120 R121	9.98κΩ, .1%, 1/10W, MtF · · · · · · · · · · · · · · · · · · ·		E-3 E-3	TR₩ TR₩	MAR-5, T13 MAR-5, T13	R-263-9.98K R-263-1.002K
R121	270KB, 10%, 1/4W, Comp.		F-3	MEP	CR25, 5%	R-76-270K
R123	.898Ω, .1%, 5W, WW		F-3	TEP	T\$5898	R-232898
R124	.12, .12, 7.5W, WW, 5-Terminal		F-2	TEL	SPECTAL	R-2621000
R125	120Ω, 10%, 1/4W, Comp		6-3	MEP	CR25, 5%	R-76-120
R126 R127	5KG, 10%, Cermet Trimmer		E-3 E-3	BEC Bec	72PMR 72PMR	RP-97-5K RP-97-500
R128	509, 10%, Cermet Trimmer		E-3	BEC	72PMR	RP-97-50
R129	50Kn, 10%, Cermet Trimmer		E-3	BEC	72PMR	RP-97-50K
R130	143K2, .1%, 1/10W, MtF.		C-2	TRW	MAR-5, T13	R-263-143
R131	856KΩ, .1%, 1/10₩, MtF		C-2	TRW	MAR-5, T13	R-263-856
R132	100KΩ, 1%, 1/10W, MtF		0-2 0-2	IRC IRC	CEA-TO-100K CEA-TO-26.7K	R-88-100K R-88-26.7K
R133 R134	3.019, 1%, 1/8W, MtF.		D-2	TRC	CEA-TO-3.01K	R-88-3.01K
	Thick Film Network.		E-2	B-D		TF-65
R136	47KR, 10%, WW, Comp		F-2	A-B	НВ	R-3-47K
R137	9R, 0.5W, 0.1%, WW		F-2	TEL	SA3	R-252-9
R138 R139	900Ω, 0.1%, 1/2W, MtF		F-2 F-2	DLE	MFF-1/2-T0-900 NFF-1/2-TQ-90	R-169-900 R-169-90
R140	11KΩ, 1%, 1/8W, MtF		C-1	IRC	CEA-TO-11K	R-88-11K
R141	19.6KR, 12, 1/8W, MEF		C-1	IRC	CEA-TO-19.6K	R-88-19.6K
R142	50K, 10%, Cermet Trimmer	· G-3	D-2	BEC	72PMR	8P-97-50K
R143	5002, 10%, Cermet Trimmer		0-2	BEC	72PMR	RP-97-500
R144 R145	Optional, Factory Selected Optional, Factory Selected		E-2 E-2	MEP	CR25, 5% CR25, 5%	R-76=☆ (SEL) R-76-☆ (SEL)
R146	19, 5%, 1/4W, Comp.		E-2	MEP	CR25, 5%	R-76-1
R147	47KΩ, 10%, 1/4W, Comp		F-2	NEP	CR25, 5%	R-76-47K
R148	22M, 10%, 1/4W, Comp	• F-5	F-2	MEP	CR25, 5%	R-76-22M
R149	200K, 10%, Cermet Trimmer		G-1	BEC	89P	RP-89-200K
R150 R151	3.3K9, 5%, 1/4W, Comp		F~5	MEP	CR25, 5%	R-76-3.3K
R152	12KΩ, 5%, 1/4W, Comp		F-5 C-3	HEP R-D	CR25, 5%	R-76+12K R-88-*
R153	(Part of 28798A)		C-3	R-D		R-88-*
	"200"	Series (Sc) (PC-Board	n. 28992E-P 448-Pg, 7-1		l.	
R201	1200, 1/4W, Comp		C-2	MEP	CR25, 5%	R-76-120
R202	479, Thick Film Network		E-2	BEC	899-3-R47	RF-64
	1300 ⁽¹	Series (Sch (PC-Board	n. 28992E-P 451-Pg, 7-1		ĺ	
R301	20KR. 0.5W. POT	. E•7	C-3	8EC	72PMR-20K	RP-97-20K
R302	3300, 10%, 1/4W, Comp	E-7	č-3	MEP	CR25, 5%	R-76-330
R303	1KΩ, 10%, 1/4W, Comp	- E-6	D-4	MEP	CR25, 5%	R-76-1K
R304	3.9K9, 202, 3W, W		0-4	TEP	TS3	R-268-3.9
R305 R306	4.7KΩ, 10%, 1/4W, Comp		0-3 C-3	MEP A-B	CR25, 5% CB-332-10%	R-76-4.7K R-76-3.3K
R307	82KΩ, 10%, 1/4W, Comp		E-3	MEP	CR25, 5%	R-76-82
R308	82KΩ, 10%, 1/4₩, Comp	• F-7	E-3	MEP	CR25, 5%	R-76-82
R309	10Ω, 10%, 1/4W, Comp	· D-8	F-4	A-B	CB-100-10%	R-76-10
R310 R311	100, 10%, 1/4W, Comp	· D-8	F-4 E-3	A-B MEP	CB-100-10%	R-76-10 R-76-100K
R312	100KΩ, \$2, 1/4₩, Comp		E-3 E-3	MEP	CR25, 5% CR25, 5%	R-76-100K
R313	6.8MΩ, 5%, 1/4W, Comp	· F-6	Ē-3	MEP	CR25, 5%	R-76-6.8M
R314	6.8MΩ, 5%, 1/4W, Comp	• F-6	E-3	MEP	CR25, 5%	R-76-6.8M
R315 R316	576KΩ, 1%, 1/8W, Comp 100KΩ, 1%, 1/8W, Comp	• F-6 • F-6	E-3 E-3	I RC I RC	CEA-TO-576K CEA-TO-100K	r-88-576k r-88-100k
	"100"	SWITCH Serles (Scl (PC-Board				
Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.

C-4 D-4 G-4 C-W R-D R-D GG350PC0PDT

SW-318 SW-397 SW-402 (27696A)

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Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
r101	Transformer, Power	· C-6	0~5 D-5	R-D		TR-168
101	Transformer, Power (100/200 V) "300"	'Series (Sc	h. 28992E-P			TR-169
	the true count of the second o	(PC-Boar	d 451-Pg. 7•	-13)		1119 (C.1931).
1301 500	Transformer, Power	. F-7	F-3	₽-D		TR-170
			NTS (TP) E-Pg, 7-10)			
Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desiq.	Part No.
TP1	Test Point.			R-D		
TP2 TP3	Test Point	. c-8		Ĩ.		
TP4	Test Point	. J-1				
TP5 TP6	Test Point,					
TP7	Test Point,	. J-2	<u>12 12 1</u> 3			
TP8 TP9	Test Point			T		
TPIO	Test Point			R-D		
	0100)" Series (S	CIRCUITS (ch. 28992E- d 448-Pg, 7-	Pg. 7-10))	
Circult Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
U 101 U 102	Operational AMP 8-Pin, TO-5 Operational AMP 8-Pin, DIP		E-3 D-3	NAT NAT	LH0022CH LM301AN	1C-165 +C-167
U103	4-1/2 Digit Analog-Processor	. K-2	D-2	INT	8052A	LS1-12
U104 U105	Deerational AMP 8-Pin, T0-5 TRMS Converter		E-2 D-2	NAT A-D	LH0042CH AD536J	10-175
u106	4-1/2 Digit Logic Processor 4011 CMOS Unbuffered	. J-4	E-2 F-2	INT MOT	7103A MC14011CP	LS1-11 IC-102
	"200)' Series (S (PC-Boar	ch. 28992E-1 d 449-Pg.7-)	
U201	Segment Drive		F-2	MOT	4511	10-168
U202 @	Digit Driver		F-2	T-1	75492	10-169
	''300	" Series (S (PC+Boa	ch. 28992E-4 rd 451-Pg. 7))	
U301	Sual D-Type Flip-Flop, 14-Pin DIP		D-3	RCA	CD4013AE	10-103
U302	1.IV Micro-Power Detector ,	0	E-3	INT	ICL8211CPA	10-177
		VOLTAGE REG Series (Sc (PC-8oard		g, 7-10)		
Circuit Desig.	Description	Sch. Location	PC-Board Location	Mfr. Code	Mfr. Desig.	Part No.
VR101 VR102	-15V, 3-Term . +15V, 3-Term, LO-Power	· C-8 - C-8	E-5 E-5	мот мот	MC7915CT MC78L15CP	10-174 10-170
	NOT USED +5V, 3-Term, TO-220		0-4 C-2	мот R-D	мс7805ст	IC-93 (28798A)
VR103 VR104	Reference Zener	• UT2				(20/308)
VR103 VR104	Reference Zener	Series (Sch		23		
VR103 VR104 VR105 VR301		(PC-Board	451-Pg. 7-1 C-3	3) MOT	1N765A	DZ-61
VR103 VR104 VR105	"300" 8.2 Volt, Zener	(PC-Board . E-6 CRYS " Series (So	451-Pg. 7-1 C-3 TAL (Y) th. 28992E-P	МОТ 9g. 7-10	187654	0Z-61
VR103 VR104 VR105	"300" 8.2 Volt, Zener	(PC-Board . E-6 CRYS " Series (So	451-Pg. 7-1 C-3 TAL (Y)	МОТ 9g. 7-10		DZ-61

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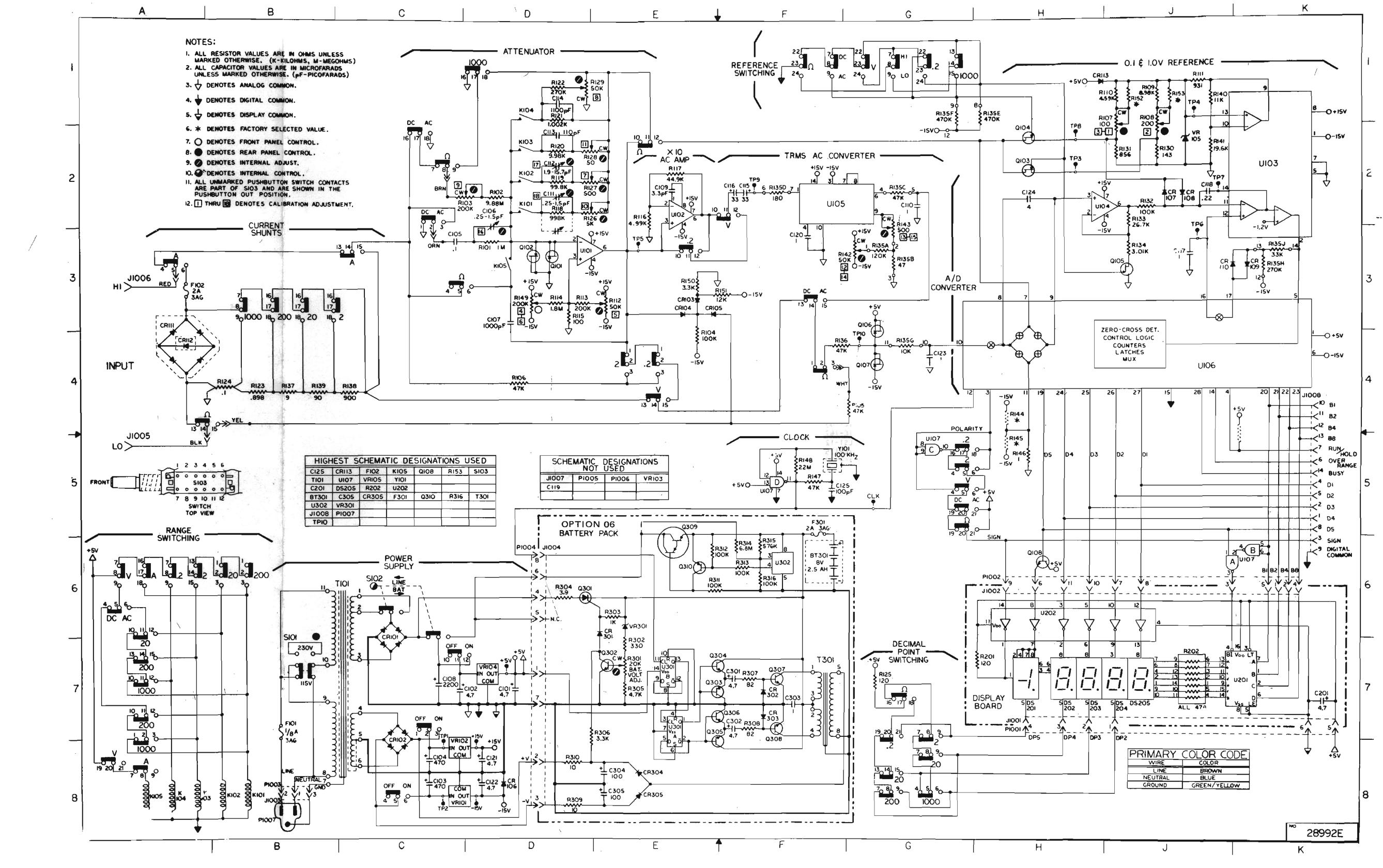
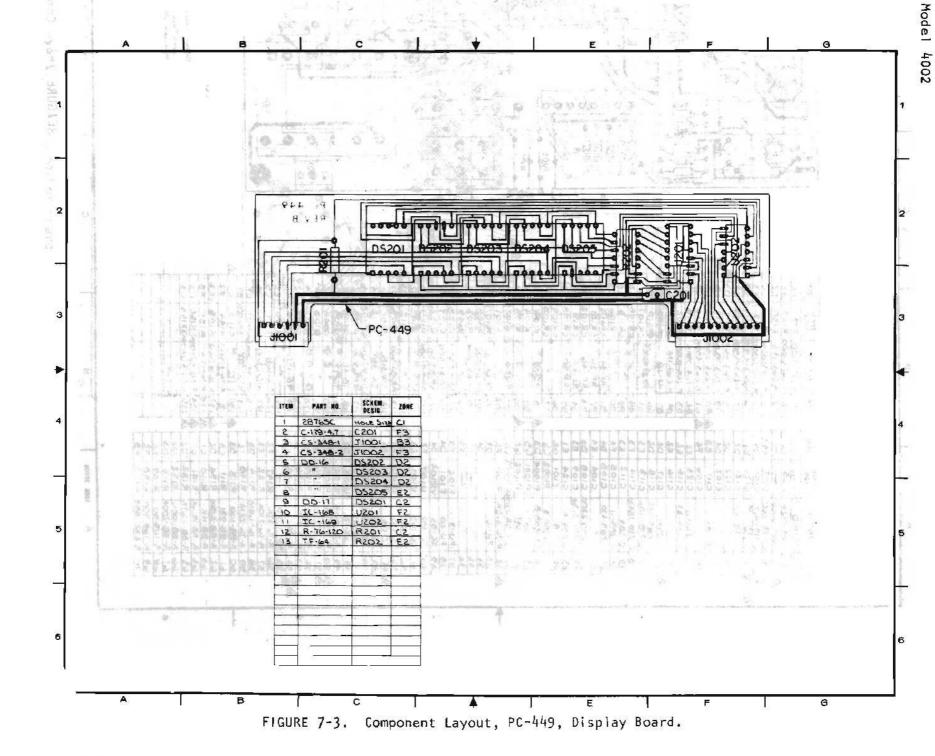
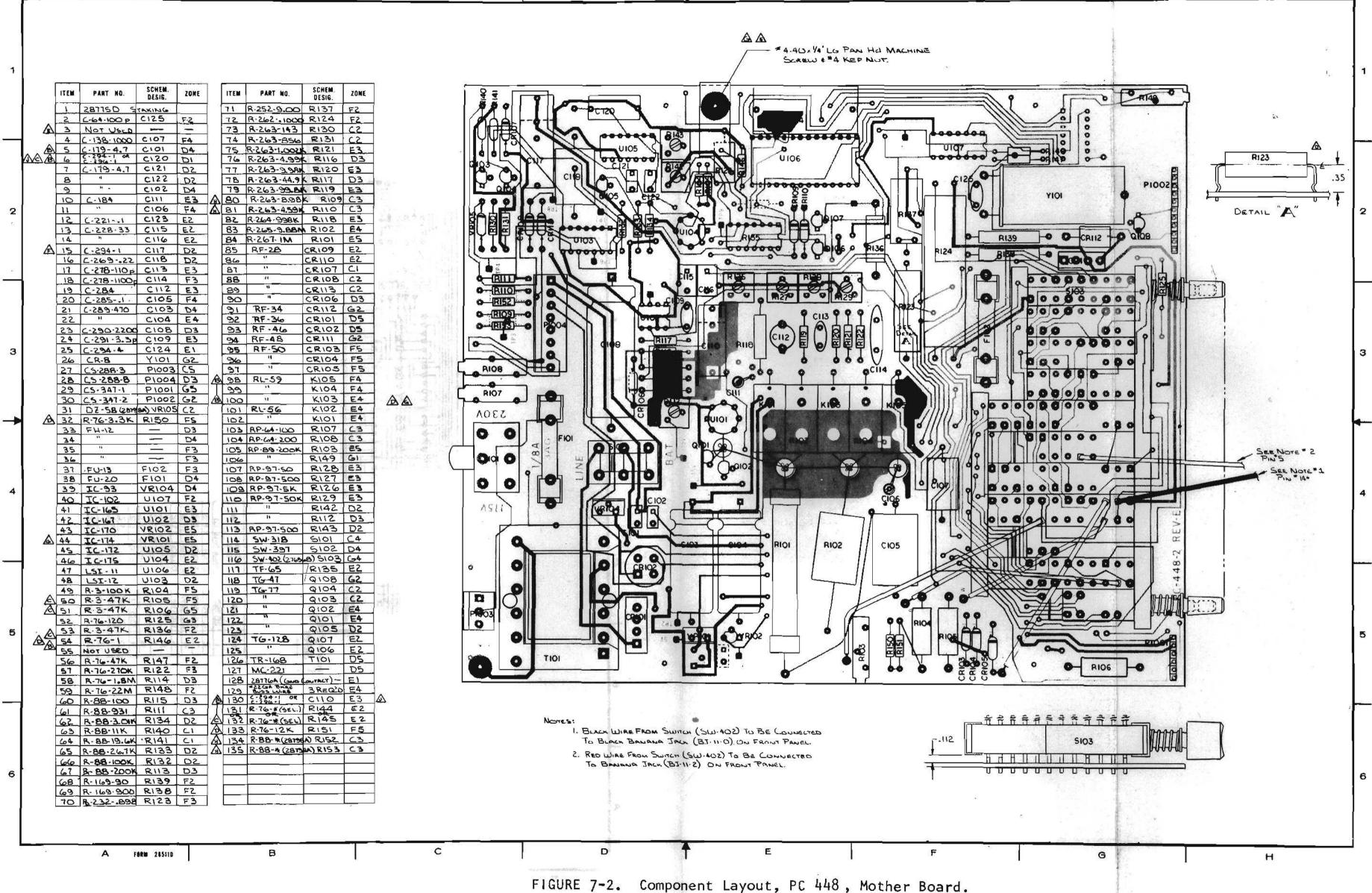


FIGURE 7-1 Schematic Diagram - Model 4002 TRMS Multimeter.



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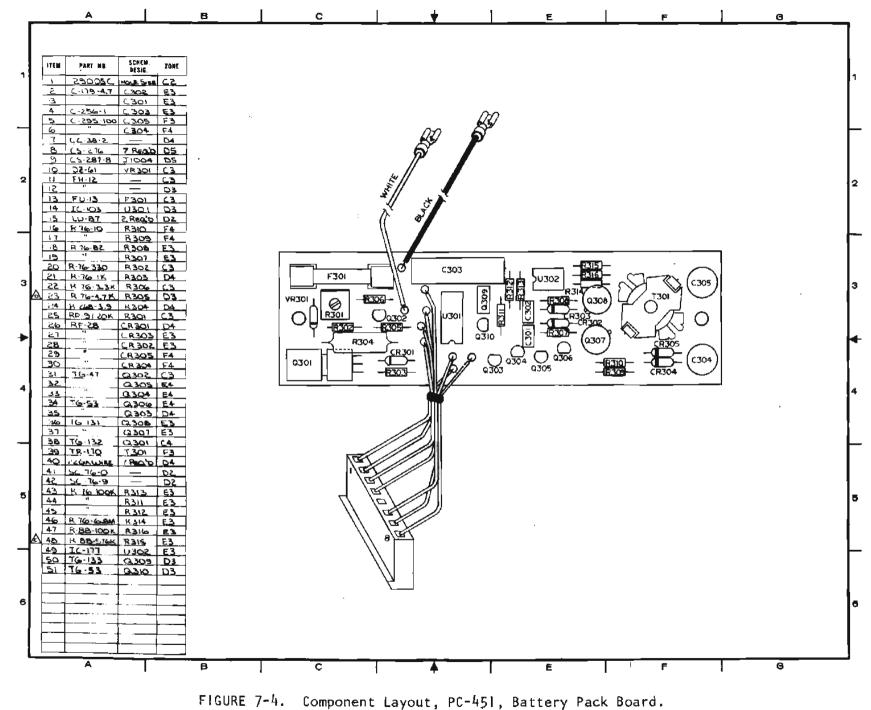
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