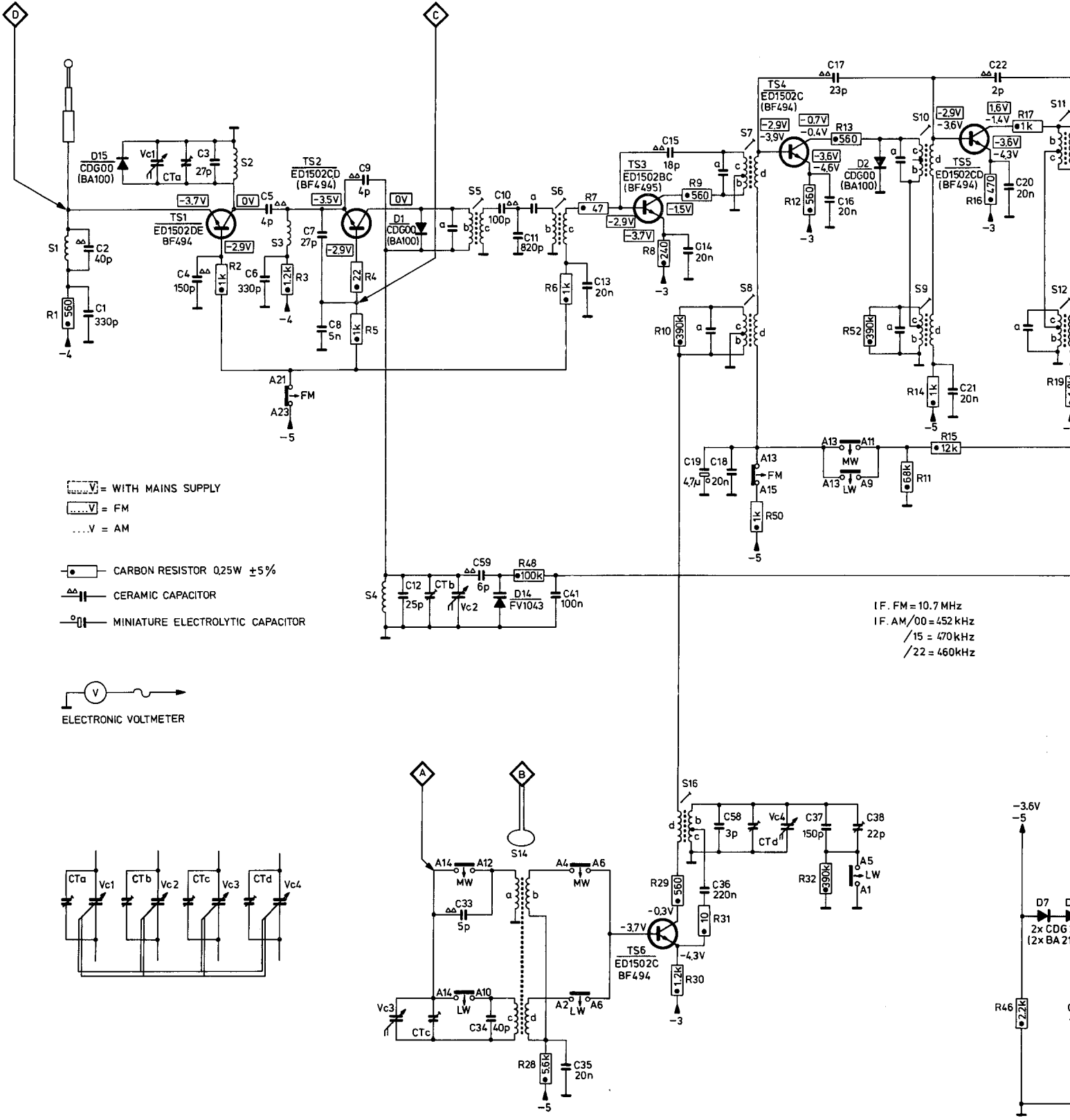


MISC	S1	D15	TS1	S2	S3	TS2	D1	S5	S6	TS3	S7	S8	TS4	D2	S9	S10	TS5	S11	S
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