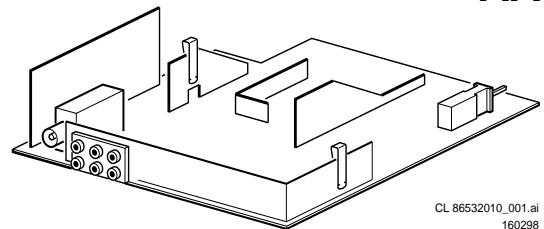


Service

Service

Service



Service Manual

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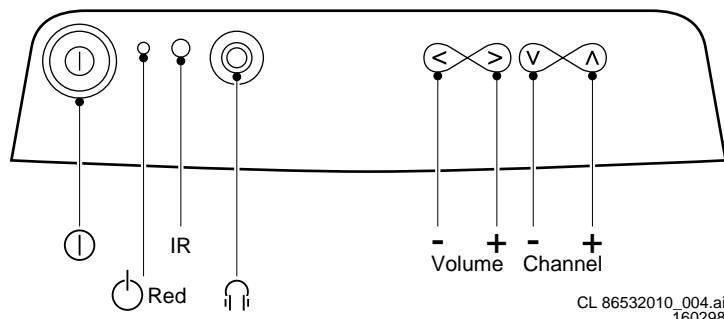
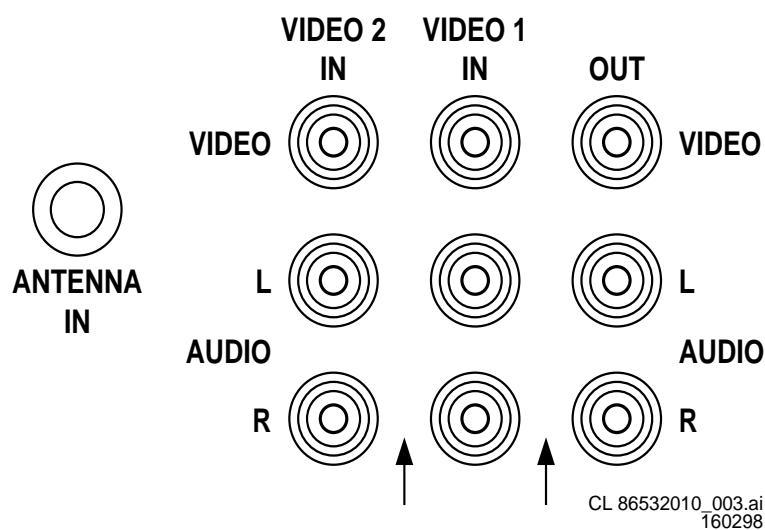
1 Technical specifications

Mains Voltage:	: 150 - 276 Volt
	: 90 - 276 Volt
Mains frequency	: 50 Hz
	: 60 Hz
Power Consumption in stand-by	: < 10 Watt
Power Consumption normal mode	: 25" : 75Watt +/- 10% : 29" : 90 Watt +/- 10%
Chassis mains isolated	: Yes
Aerial input impedance TV	: 75 Ohm - coax
Picture tube range	: 25" and 29"
EHT voltage for 25"	: 27 KVolt
EHT voltage for 29"	: 30 KVolt
Max EHT power	: Limited at 33 Watt
Sound output power	: 2 X 3 Watt : 2 X 5 Watt
Picture S/N at 70uVolts	: > 46 dB unweighted : > 53 dB weighted

2 Connection facilities

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2.1 Cinch

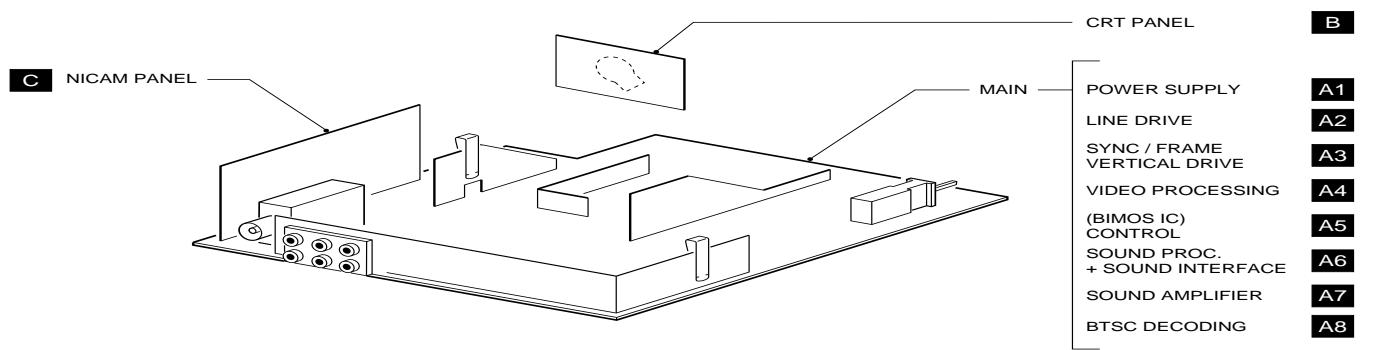
- Video 1Vpp/75Ω
- Audio L(0.5Vrms ≥10kΩ)
- Audio R(0.5Vrms ≥10kΩ)

2.2 Head phone

- (8 - 600Ω / 4mW)

3.5mm $\frac{1}{4}$ Ω

2.3 Location of panels



3 Safety instructions, Maintenance instruction,

3.1 Safety instructions for repairs



Figure 3-1

1. Safety regulations require that during a repair:
 - the set should be connected to the mains via an isolating transformer;
 - safety components, indicated by the symbol (see fig. 3.1), should be replaced by components identical to the original ones;
 - when replacing the CRT, safety goggles must be worn.

2. Safety regulations require that after a repair the set must be returned in its original condition. In particular attention should be paid to the following points.
 - As a strict precaution, we advise you to resolder the solder joints through which the horizontal deflection current is flowing, in particular:
 - all pins of the line output transformer (LOT);
 - fly-back capacitor(s);
 - S-correction capacitor(s);
 - line output transistor;
 - pins of the connector with wires to the deflection coil;
 - other components through which the deflection current flows.

Note: This resoldering is advised to prevent bad connections due to metal fatigue in solder joints and is therefore only necessary for television sets older than 2 years. The wire trees and EHT cable should be routed correctly and fixed with the mounted cable clamps.

- The insulation of the mains lead should be checked for external damage.
- The mains lead strain relief should be checked for its function in order to avoid touching the CRT, hot components or heat sinks.
- The electrical DC resistance between the mains plug and the secondary side should be checked (only for sets which have a mains isolated power supply). This check can be done as follows:
 - unplug the mains cord and connect a wire between the two pins of the mains plug;
 - set the mains switch to the on position (keep the mains cord unplugged!);
 - measure the resistance value between the pins of the mains plug and the metal shielding of the tuner or the aerial connection on the set. The reading should be between 4.5 MW and 12 MW;
 - switch off the TV and remove the wire between the two pins of the mains plug.
- The cabinet should be checked for defects to avoid touching of any inner parts by the customer.

3.2 Maintenance instruction

It is recommended to have a maintenance inspection carried out by a qualified service employee. The interval depends on the usage conditions:

- When the set is used under normal circumstances, for example in a living room, the recommended interval is 3 to 5 years.

- When the set is used in circumstances with higher dust, grease or moisture levels, for example in a kitchen, the recommended interval is 1 year.
- The maintenance inspection contains the following actions:
 - Execute the above mentioned 'general repair instruction'.
 - Clean the power supply and deflection circuitry on the chassis.
 - Clean the picture tube panel and the neck of the picture tube.

3.3 Warnings



1. ESD

All ICs and many other semiconductors are susceptible to electrostatic discharges (ESD). Careless handling during repair can reduce life drastically. When repairing, make sure that you are connected with the same potential as the mass of the set by a wristband with resistance. Keep components and tools also at this same potential.

- Available ESD protection equipment:
 - anti-static table mat (large 1200x650x1.25mm) 4822 466 10953
 - anti-static table mat (small 600x650x1.25mm) 4822 466 10958
 - anti-static wristband 4822 395 10223
 - connection box (3 press stud connections, 1 M ohm) 4822 320 11307
 - extension cable (2 m, 2 M ohm; to connect wristband to connection box) 4822 320 11305
 - connecting cable (3 m, 2 M ohm; to connect table mat to connection box) 4822 320 11306
 - earth cable (1 M ohm; to connect any product to mat or connection box) 4822 320 11308
 - complete kit ESD3 (combining all 6 prior products - small table mat) 4822 310 10671
 - wristband tester 4822 344 13999

2. In order to prevent damage to ICs and transistors, all high-voltage flashovers must be avoided. In order to prevent damage to the picture tube, the method shown in Fig. 3.2 should be used to discharge the picture tube. Use a high-voltage probe and a multimeter (position DC-V). Discharge until the meter reading is 0V (after approx. 30s).
3. Together with the deflection unit and any multipole unit, the flat square picture tubes used from an integrated unit. The deflection and the multipole units are set optimally at the factory. Adjustment of this unit during repair is therefore not recommended.
4. Be careful during measurements in the high-voltage section and on the picture tube.
5. Never replace modules or other components while the unit is switched on.
6. When making settings, use plastic rather than metal tools. This will prevent any short circuits and the danger of a circuit becoming unstable.
7. Wear safety goggles during replacement of the picture tube

3.4 Notes

1. The direct voltages and oscilloscopes should be measured with regard to the tuner earth, or hot earth as this is called (see fig. 3.3)
2. The direct voltages and oscilloscopes shown in the diagrams are indicative and should be measured in the Service Default Mode (see chapter 8) with a colour bar signal and stereo sound (L:3 kHz, R:1 kHz unless stated otherwise) and picture carrier at 475.25 MHz.

3 Safety instructions, Maintenance instruction,

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3. Where necessary, the oscillograms and direct voltages are measured with and without aerial signal. Voltages in the power supply section are measured both for normal operation and in standby. These values are indicated by means of the appropriate symbols (see fig. 3.3).
4. The picture tube PWB has printed spark gaps. Each spark gap is connected between an electrode of the picture tube and the Aquadag coating.
5. The semiconductors indicated in the circuit diagram and in the parts lists are completely interchangeable per position with the semiconductors in the unit, irrespective of the type indication on these semiconductors.

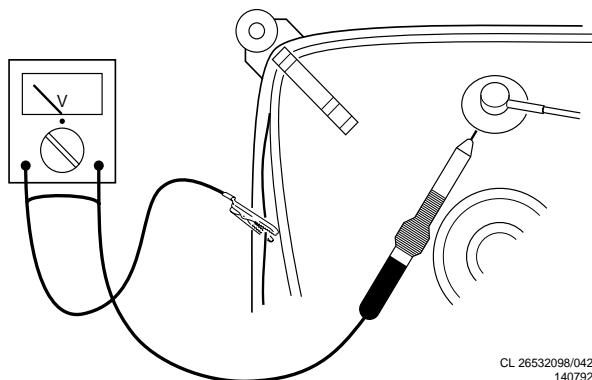


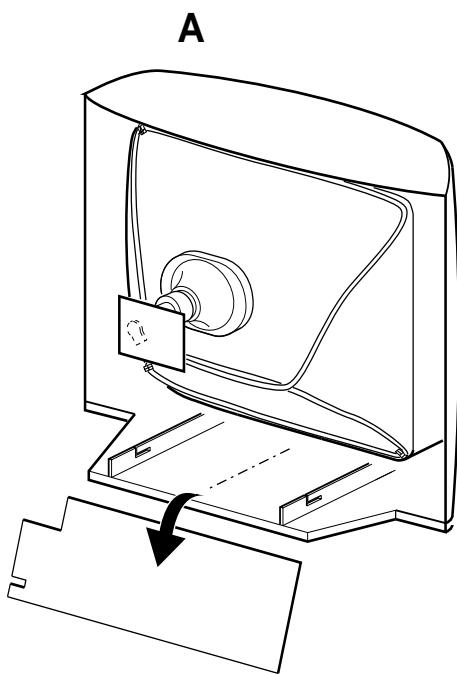
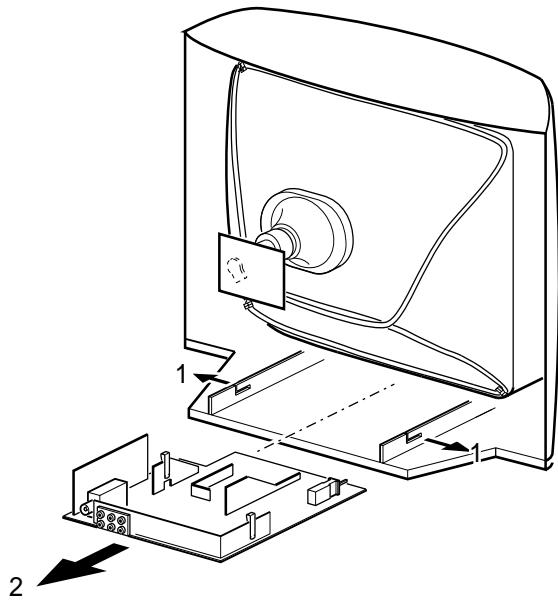
Figure 3-2

	tuner earth tuner aarde la masse du tuner Tuner-Erde massa del tuner tierra del sintonizador		hot earth hete aarde la terre directe heilzen Erde massa calda tierra caliente
	with aerial signal met antenne signaal avec signal d'antenne mit Antennensignal con segnale d'antenna con la señal de antena		without aerial signal zonder antenne signaal sans signal d'antenne .ohne Antennensignal senza segnale d'antenna sin la señal de antena
	normal condition normaal bedrijf fonctionnement normal normaler Betrieb funcionamiento normal funcionamiento normal		stand by stand by position de veille in Bereitschaft modo di attesa posición de espera

Figure 3-3

4 Mechanical instructions

For the service position of the main carrier see figure 4.1:
The main carrier can be removed by releasing the 2 carrier
blocking lips (1) and pulling the carrier panel backwards.



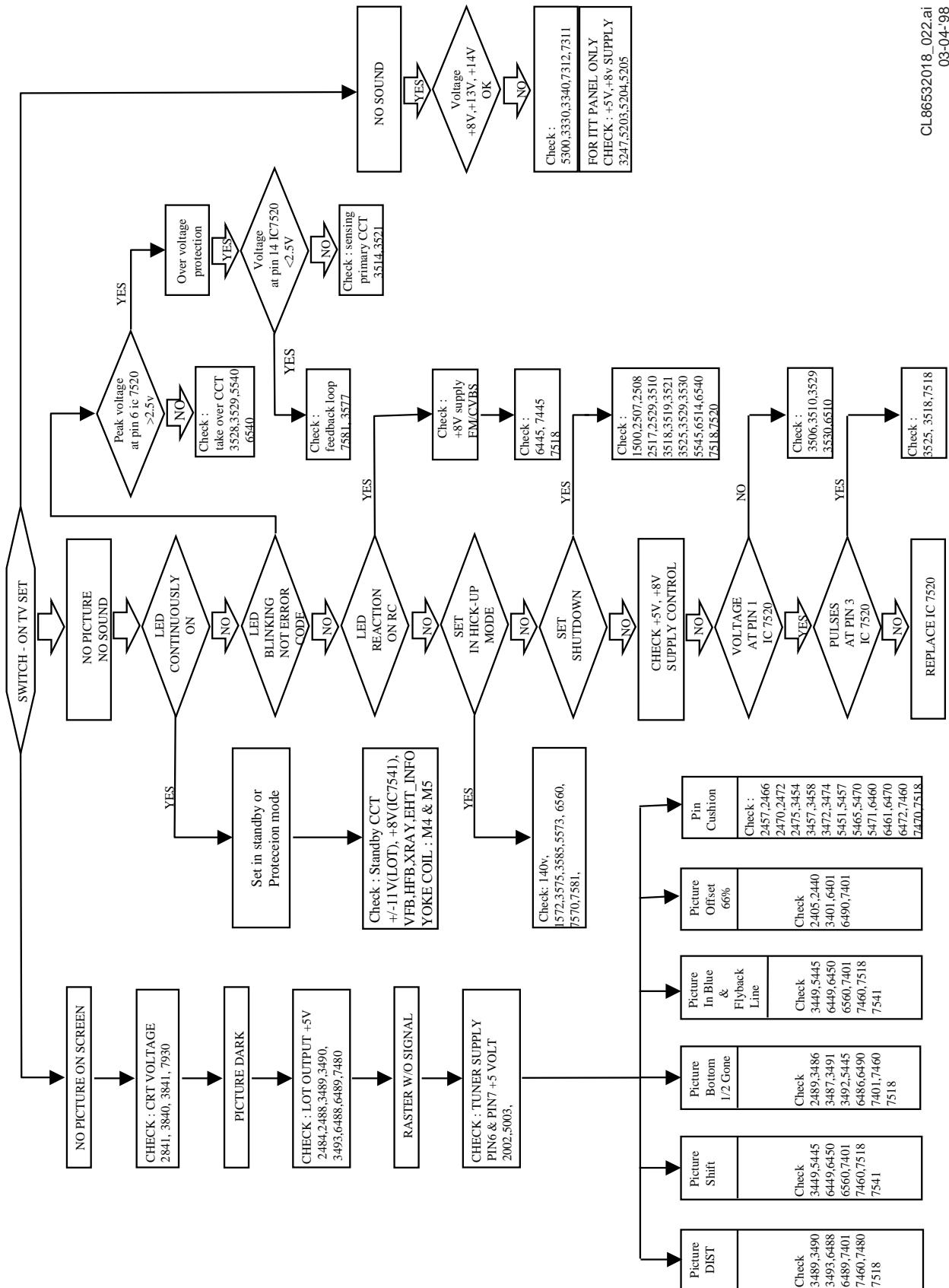
CL 86532010_002.ai
160298

Figure 4-1

5 Faultfinding tree & Repair facilities

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5 Faultfinding tree & Repair facilities

5.1 Repair facilities

5.2 Functional blocks

The PCB has service printing on both sides , so on the copper and the component side. Functional blocks are indicated by lines and text.

Test points are referring to the functional blocks as mentioned above :

- Test pointElectrical function
- P1, P2, etc. :Power supply
- L1, L2, etc. :Line drive and line output
- F1, F2, etc. :Frame drive and frame output
- S1, S2, etc. :Synchronization
- V1, V2, etc.: Video
- A1, A2, etc.: Audio
- C1, C2, etc.:Control
- B1, B2, etc : BTSC stereo decoding
- N1, N2, etc.:NICAM stereo decoding

The numbering is done in for a diagnostics logical sequence.

Example: Checking the power supply, start with test point P1, P2 etc.

5.3 Service mode

The service mode is a pre-defined mode which can be used for faultfinding and adjustments (especially when the TV gives no picture at all you can use the service default mode). All oscillograms and DC voltages in this service manual are measured in the service default mode.

The service mode is split into two parts :

- Service Default Mode (SDM).
- Service Alignment Mode (SAM).

SDM. In the service default mode you are able to carry out (hardware) measurements under pre-defined conditions. To entry the (SDM) Service Default Mode there are two possibilities:

- Via the "DEFAULT" button on the Dealer Service Tool, service codenumber : 4822 218 21232.
- Via Short-circuiting the service pins M24 and M25 on PCB and switching on the set via the mains switch. For M25 and M24 see Diagram A4 and the PWB drawing of the main panel

In the SDM mode a "S" (in green) and the menu (in red) is displayed (see Fig. 1).

SAM. In the SAM mode you are able to align/adjust settings via menu selection. To entry the (SAM) Service Alignment Mode there are two possibilities :

- Via the "ALIGN" button on the Dealer Service Tool
- Via Short-circuiting service pins M28 and M29 on PCB and switching on the set via the mains switch. For M25 and M24 see Diagram A4 and the PWB drawing of the main panel

In the SAM mode a "S" (in green) and the menu (in red) is displayed (see Fig. 2).

REMARKS: After the set is in the service mode the short circuit can be removed.

To leave the Service mode press the stand-by button on the remote control.

REMARKS : After switching off and on the mains switch, the set remains in the service mode (SDM- or SAM mode).

Features are :

- Service settings after entry
- Service (sub) menu selection
- Error buffer display
- Software version and identification display
- Life timer (run timer) display
- Option bytes

5.4 Initial states

The initial state after switching on in service mode for System and Tuning are as follows :

- For Multi-System sets PAL BG/SECAM/DK/NTSC
- For Bi-Norma and Tri-Norma sets NTSC/PAL-M/PAL-N
- For sets with VST tuning : Programme number 1 is selected and the system will be tuned at the tuning data (for programme 1) read from EEPROM
- For sets with PLL tuning : Set is tuned to a frequency of 475.25 MHz.

Further settings :

- The automatic switch off (no IDENT) timer and the sleep timer will be ignored
- The child lock will be disabled.
- If the TV sets was in hotel mode, this mode will be disabled as long as the TV is in service mode.
- Brightness, saturation, sharpness, contrast and balance are initialised to 50% level.
- The volume is set to 25% level.
- The TV is normally controllable.
- All displayed text strings in service mode, so SDM and SAM mode will be displayed in the English language.

Other features

RAM test of the uP (=IC7600) and the NV RAM (=IC7620). At every start-up of the TV, a read after write test for the complete RAM of IC7600 and via I2C the NVRAM (=IC7620) will be performed. If this check fails, the appropriate error number will be written in the error buffer. The patterns will be chosen in such a way that every bit of all bytes, will be written high and low. If the NVRAM is empty default values will be loaded.

5.5 SDM (Service Default Mode)

This menu is being displayed whenever SDM is entered. In this menu the error buffer can be inspected, and the option byte(s) can be (re)programmed. The overview of the menu is shown below :

The MENU UP/DOWN command can be used to select the next/ previous option ; the MENU LEFT/RIGHT command can be used to change the option value.

L73AP21 1.0 (Software version)	S (Service mode) 9999 (Life timer)
-----------------------------------	---------------------------------------

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AS (options description)	VALUE	8888 7777 66 (options bytes)
ERR (error buffer)	3 4 3 0 0 (error code)	

Explanation :

L73AP21 1.0: The software identification, version and cluster.
AS: A two character short name for the option to be selected.
(see table)

VALUE: The value of the selected option.

ERR: Error buffer display

3 4 3 0 0

S: The character " S " indicates that the TV set is in the service mode.

9999: The hexadecimal value of the life timer.

8888 7777 66: 10 digits of value representation of the option byte contents

5.5.1 Software identification

If there is a change of the software this number will be changed.

5.5.2 Life timer (run timer)

This life timer only counts the normal operations hours and not the stand-by hours. Also at every switch- on the life timer is incremented by 1.

5.5.3 Option code

The possible options are listed in the following table 1:

Error buffer

The last five errors, stored in the NVRAM=EEPROM (IC7620), are shown in the service main menu; this is called the errorbuffer. An error will be added to the buffer if this error differs from the last error in the buffer. The last found error is displayed on the left-side.

Example: Suppose the display shows: 3 4 0 0 0. This means the last found error is error number 3, the last found error but one is error number 4, and so on.

- 40000
- 34000

REMARKS: If the (IC7620=) EEPROM is replaced by a new one the set has to be installed according to the option code. The ERROR BUFFER will be erased when the set is switched from service mode in stand by, or via DST code 99.

Error codes

The following error codes have been defined :

- 0 - No error
- 1 - Internal RAM
- 2 - General I2C error
- 3 - EEPROM Configuration error (Checksum error)
- 4 - I2C error (MSP3410 / TDA9850/TDA9860)
- 5 - I2C error (TDA8374)
- 6 - EEPROM error (ST24C04)
- 7 - I2C error (PLL tuner)

5.5.4 Error code indication via blinking stand by LED

The ERROR code are also indication via blinking of the standby LED. This is important if no OSD function or picture is available. The method is to show that the LED blinks as many as the error code indicates..

Example: Error code 4 will result in four blinks of the LED (0.25 seconds ON and 0.25 seconds OFF). After this sequence the LED will be OFF for 3 seconds and the blinking of the LED is repeated again.

5.5.5 Option abbreviation and Option status.

To select another option abbreviation use the MENU UP/DOWN buttons and to change the status use the MENU LEFT/RIGHT buttons.

Elucidation: With above items the option statuses stored in the EEPROM can be changed. This is necessary if the EEPROM is replaced by a fresh EEPROM, because a fresh EEPROM is initial loaded with default options by the uC. The options stored in the factory can differ per type and stroke number. Therefore it is necessary to load the EEPROM with the correct options (configuration). These options are indicated on a sticker glued inside on the rear cover of the set.

Remark: For sets with BTSC sound, also the options TUN.FOA, TUN.FOB, EXT.FOA and EXT.FOB have to be set. See for these items Paragraph 6

Loading a fresh EEPROM

- Switch on the TV via the power switch.
- Audio mute the TV (to get no big noise).
- Change the option codes as indicated on the sticker on the rear cover.
- Put TV in stand by via the remote control.
- Switch on the TV again via the remote control.
- Switch OFF the TV via the power switch
- Switch on the TV again via the power switch.

In the table all the possible option abbreviation with full option name and possible status are listed.

Table 1 : Options description for L7.3 versions.

Full option name	Option name abbreviation	Status Possibilities
Auto cable detect	AC	ON/OFF
AFC Status	AF	ON/OFF
Auto scan	AS	ON/OFF
AVL	AV	ON/OFF
Balance	BL	ON/OFF
Bilingual Mono	BM	ON/OFF
Bass Treble	BT	ON/OFF
Child Lock	CH	ON/OFF
Clock	CK	ON/OFF
Contrast plus	CP	ON/OFF
EW control	EW	ON/OFF

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4 : 3 Expend	EX	ON/OFF
Games	GM	ON/OFF
Hospital mode	HS	ON/OFF
Monitor No Ident	NI	ON/OFF
NTSC Play Back	NP	ON/OFF
Spatial	SA	ON/OFF
Sound Board	SB	MA = Mono All
		MM=Multi Mono
		ND = ITT Nicam MSP3410D
		NB = ITT NICAM MSP3410B
		IT = ITT 2CS MSP3400
		50 = TDA 9850
		60 = TDA 9860
		52 = BTSC TDA9852
		55 = BTSC TDA9855
Smart Picture Term Control	SC	ON/OFF
Smart Sound	SS	ON/OFF
Bimos Standby	ST	ON/OFF
Surf	SU	ON/OFF
Multi System	SY	AF = AP FULL MULTI
		SS = SINGLE SYSTEM
		AD = AP DUAL
		AM = AP MULTI
		2N = Binorma
		3N= Trinorma
		4N = Fournorma
Tuner	TN	ON/OFF
Volume Limiter	VL	ON/OFF
Video Mute	VM	ON/OFF
Remote Control	W1	ON/OFF
External 1	XT	ON/OFF
External 2	2X	ON/OFF
TDA884X Family	88	ON/OFF

OPTION code

OPT is the abbreviation of OPTION, this abbreviation refers to the following 12 digit hexadecimal option codes (BFDD FBEEF 2C). The option code can not be selected. It only give an quick

indication in hexadecimal form of the options settings of the relevant set.

5.6 SAM (Service Alignment Mode)

This menu is being displayed whenever SAM is entered.

In SAM you are able to make tuning adjustments, Alignment of white tone, adjustment of picture geometry, Sound alignment ,VSD and AKB enable/disable (black current loop).

The overview of the menu is shown below :

General: The following items of the sub menus can be selected with the UP(+)/DOWN(-) arrow keys on the user remote control.

Entry into the sub menus is executed with the VOL(+) / VOL(-) arrow keys.

SAM MAINS MENU

S	
AKB	0
VSD	0
TUNER	>
WHITE TONE	>
GEOMETRY	>
AUDIO	>

5.6.1 AKB

With the option AKB the "black current loop" can be enabled or disabled. ON =enabled, OFF = disabled.

5.6.2 TUNER

With the items TUN.FAO and TUN.FOB the speed (time constant) for internal signals is set. The speed can be set to normal, slow or fast. The tuner sub menu contains the following information :

For monitoring during alignment of AGC,YD,CL, IF-PLL, AFW.

All entered data will be send directly to the video one chip; values are stored in EEPROM if this sub menu is left.

S	
TUNER	
AGC	37
YD	1
CL	0
IF-PLL	61
AFW	80

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AFA	1
AFB	1

5.6.3 White tone

The white tone menu contains the following information :

MAIN WHITE TONE MENU

S	
NORMAL RED	37
DELTA COOL RED	0
DELTA WARM RED	0

NORMAL TEMPERATURE SUB MENU

S	
RED	37
GREEN	37
BLUE	39

REMARK :

Only one of the 3 items (RED, GREEN or BLUE) will be displayed on the screen. Via "scrolling" with the UP/DOWN keys the items can be changed.

- NORMAL: Via this menu the NORMAL colour temperature alignment can be set.
- DELTA COOL : Via this menu the cool colour temperature alignment can be set
- DELTA WARM : Via this menu the normal warm temperature alignment can be set

Changing any of the items red, green or blue can be changed by first pressing the control left/right keys to highlight the desired setting. With the desired setting highlighted, the user can either increment or decrement the setting by using control up/down key respectively. All changed data will be stored into the EEPROM after returning to the service main menu via the OK key.

The initial default value for all setting is 37.

The factory settings of the colour temperatures are :

NORMAL	10500K	R = 37	G = 37	B = 39
DELTA WARM	8200K	R = 0	G = 254	B = 249
DELTA COOL	14000K	R = 0	G = 1	B = 3

DELTA COOL TEMPERATURE SUB MENU

- DELTA COOL RED 0
- DELTA COOL GREEN 1
- DELTA COOL BLUE 3

DELTA WARM TEMPERATURE SUB MENU

- DELTA WARM RED 0
- DELTA WARM GREEN 254
- DELTA WARM BLUE 249

5.6.4 GEOMETRY

The geometry menu contains the following information :

HSH	Horizontal shift
VSL	Vertical slope
VAM	Vertical amplitude
VSH	Vertical shift
VX	Vertical Zoom
VSC	Vertical Scroll
EW	E-W amplitude
PW	E-W parabola/width
CP	E-W cornerparabola
TC	E-W trapezium
SC	S-Correction
SBL	Service blanking
H60	Delta HSH for 60 Hz
V60	Delta VAM for 60 Hz

S	
HSH	32
VSL	32
VAM	32
VSH	34
VX	13
VSC	39
EW	54
PW	46
CP	39
TC	41
SC	52
SBL	0
H60	10
V60	10

Into the picture geometry menu, the first item will be highlighted and displayed. The value can be incremented or decremented by pressing the control right or left key. The rest of the parameters can be scrolled through by using the control up/down keys.

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All changed data will be stored into the EEPROM after returning to the service main menu via the OK key

5.6.5 AUDIO

The sound adjustments sub menu contains the following information :

	S
AUDIO	
AUTO	>
ST-NT	7
SAP-NT	7
LA	7
WIDEB	31
SPECT	31

The TDA9852 sub menu is only available if the BTSC sound IC is present. The settings can be changed by first highlighted the respective item using the control up/down keys and then increment or decrement the value by using the control right or left keys. The default value shown in the diagram are the normal values. For service purposes the normal values can be used

NOTE: The value should not change unless it is absolutely necessary !!!!

5.7 Dealer remote used as a Dealer Service Tool (DST)

With the DST, under mentioned extra service features can be executed.

The main features are :

- Direct entering SDM via the "DEFAULT" button on the DST.
- Direct entering SAM via the "ALIGN" button on the DST.
- Read the error buffer even if the OSD is not working at all. This is done via the blinking LED procedure

REMARKS:

- Entry of the SDM and SAM via the DST is possible in all states, except from stand-by.
- All software is suspended till the dealer remote mode is left.
- The service mode status is left if : the stand-by command is received.

5.8 Downloading of tuning data with the DST

Downloading of tuning data (programme number, frequency and system) via the DST is made possible. This downloading is ONLY possible in the version containing PLL tuning for Europe.

5.9 Blinking LED procedure

Via the DIAGNOSE 1 (for errorbuffer position 1) through the DIAGNOSE 5 (for error 5) commands of the DST, the error buffer can be made visible via the blinking LED. This is useful

if the screen is not working properly. The method is to use the LED pulses with as many pulses as the error number, followed by a time period of 3 seconds in which the LED is off. E.g. error code 4 will result in four times the sequence LED on for 0.25 seconds / LED will be off for 0.25 seconds. After this sequence the LED will be off for 3 seconds.

5.10 Hotel-mode

The L7.3 chassis has one special mode, called the hotel mode. In the hotel mode the following occurs:

- Installation menu cannot be entered.
- When entering the hotel mode the maximum volume will be the current value.
- The set will always switch to a selectable channel when set is switched on.

Entering the hotel-mode :

- Select channel 38
- Push the menu button on the local keyboard (vol. + and vol. -) and the OSD-button of the RC simultaneously for 3 seconds; then you are in the hotel mode.

Leaving the hotel mode :

- Same as entering the hotel mode.

The OSD will display the hotel mode "on" or "off".

HOTEL MODE SUB MENU " ON "

38	HOTEL ON
HOTEL CHANNEL	38
BLANK FROM	6
TO	6

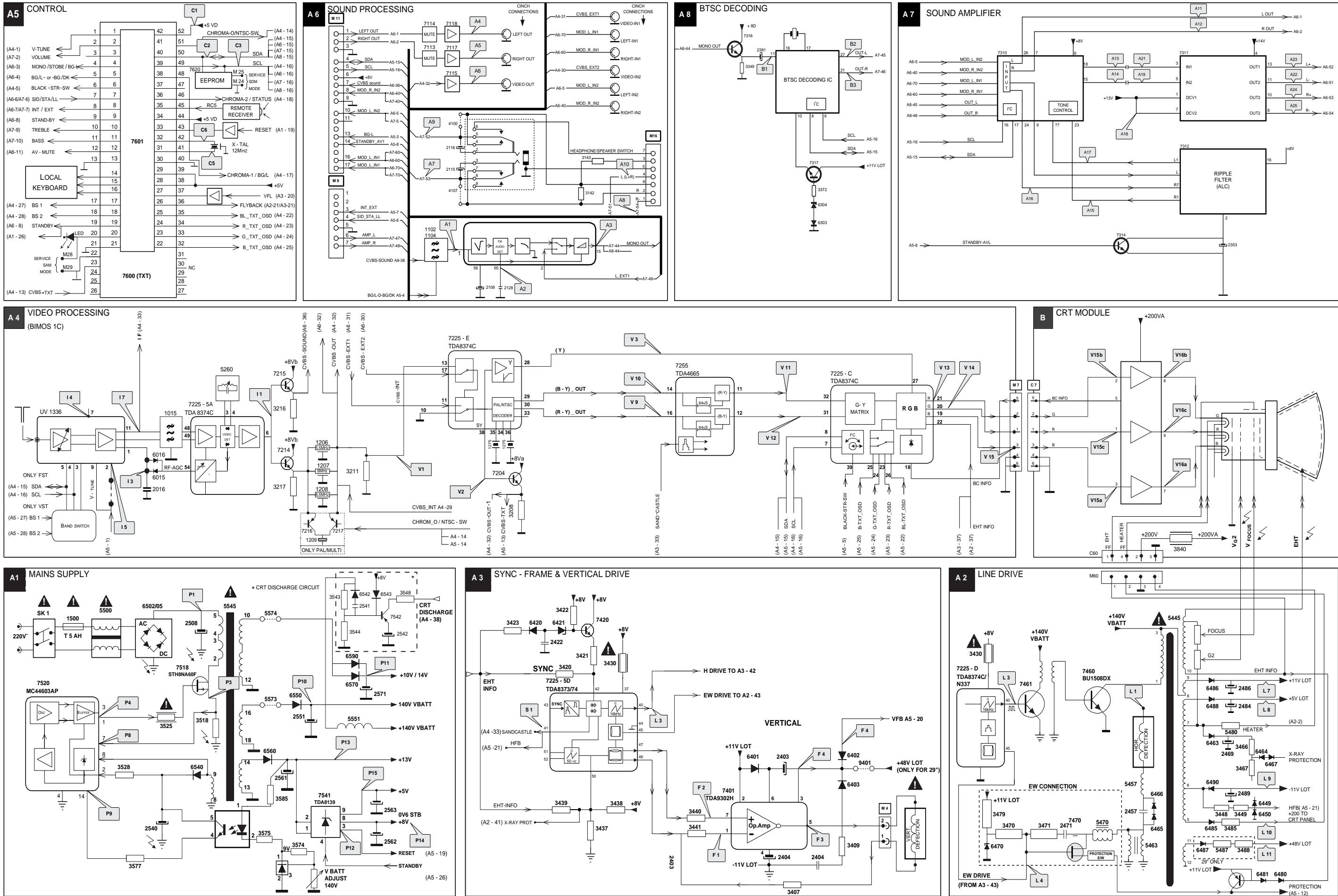
HOTEL MODE SUB MENU " OFF "

38	HOTEL OFF

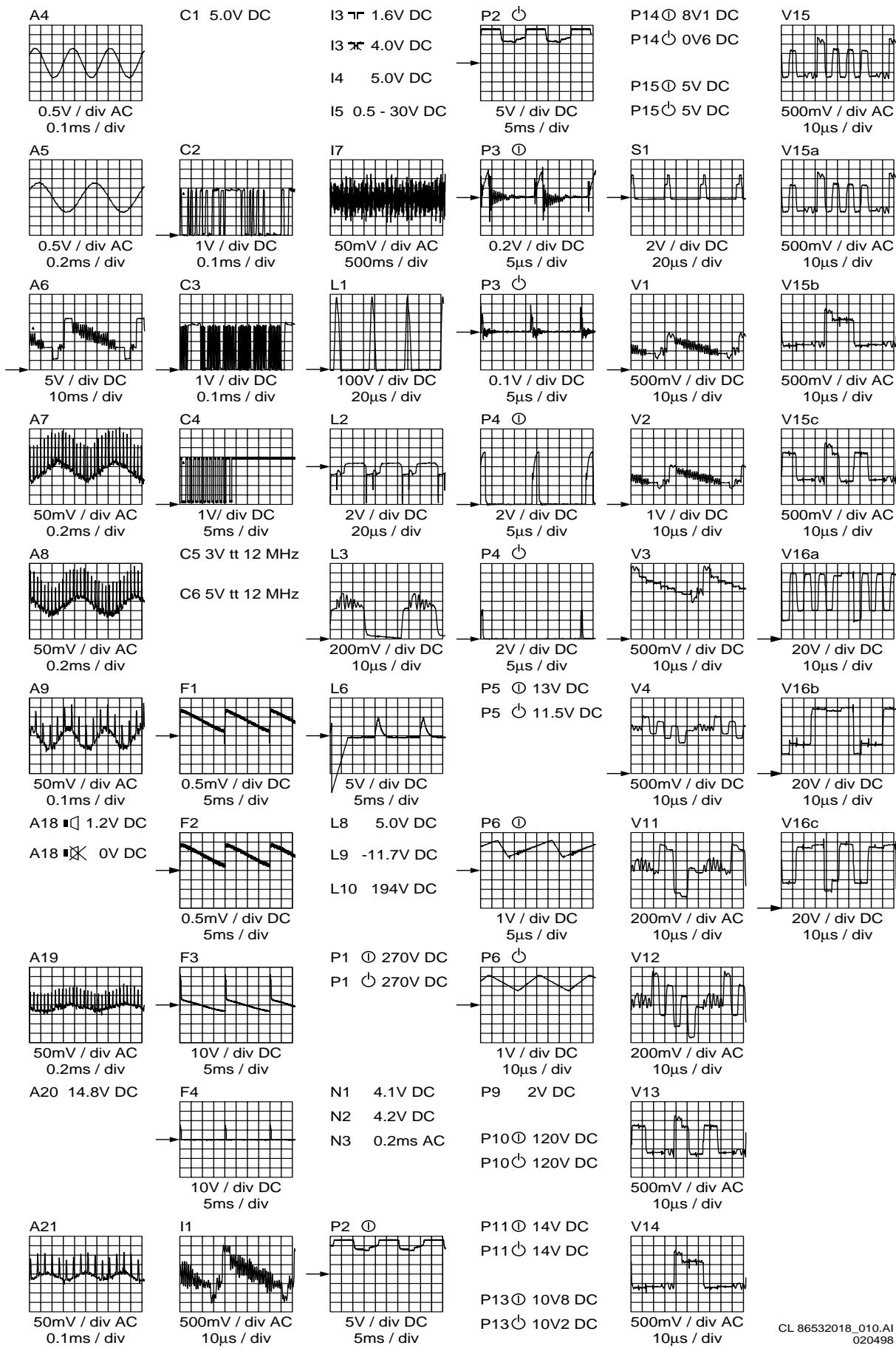
6 Block diagram

L7.3A

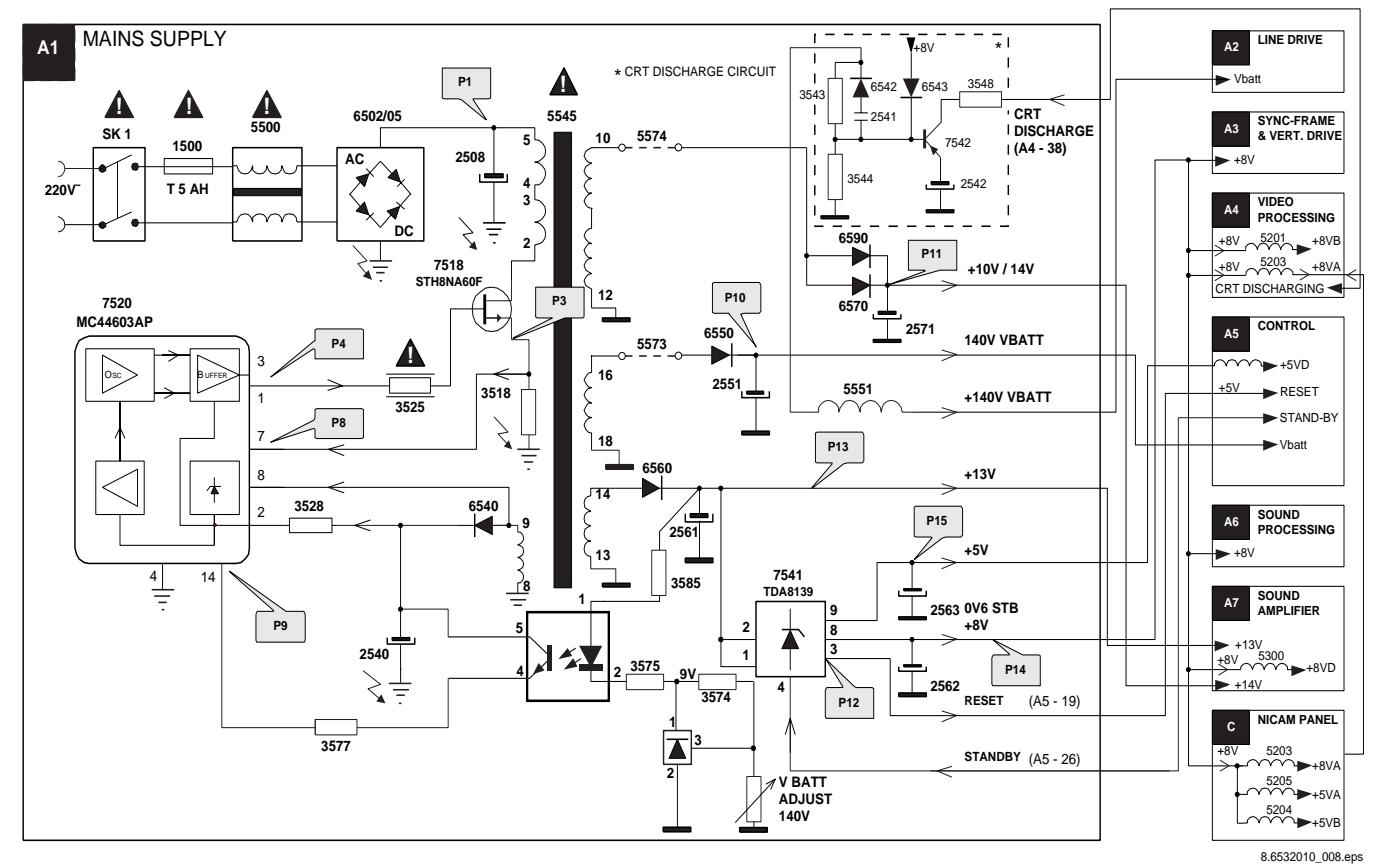
13



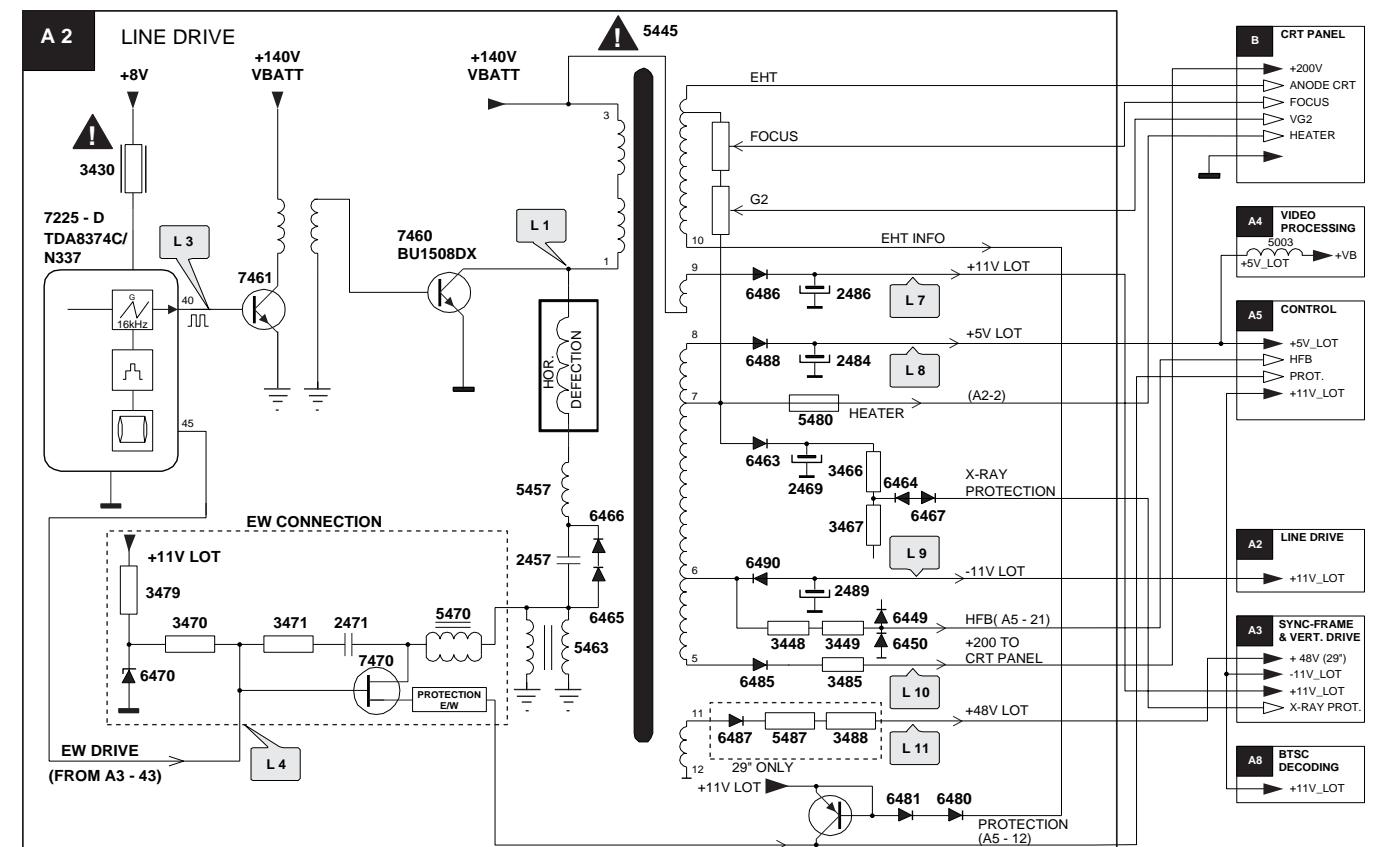
6 Block diagram



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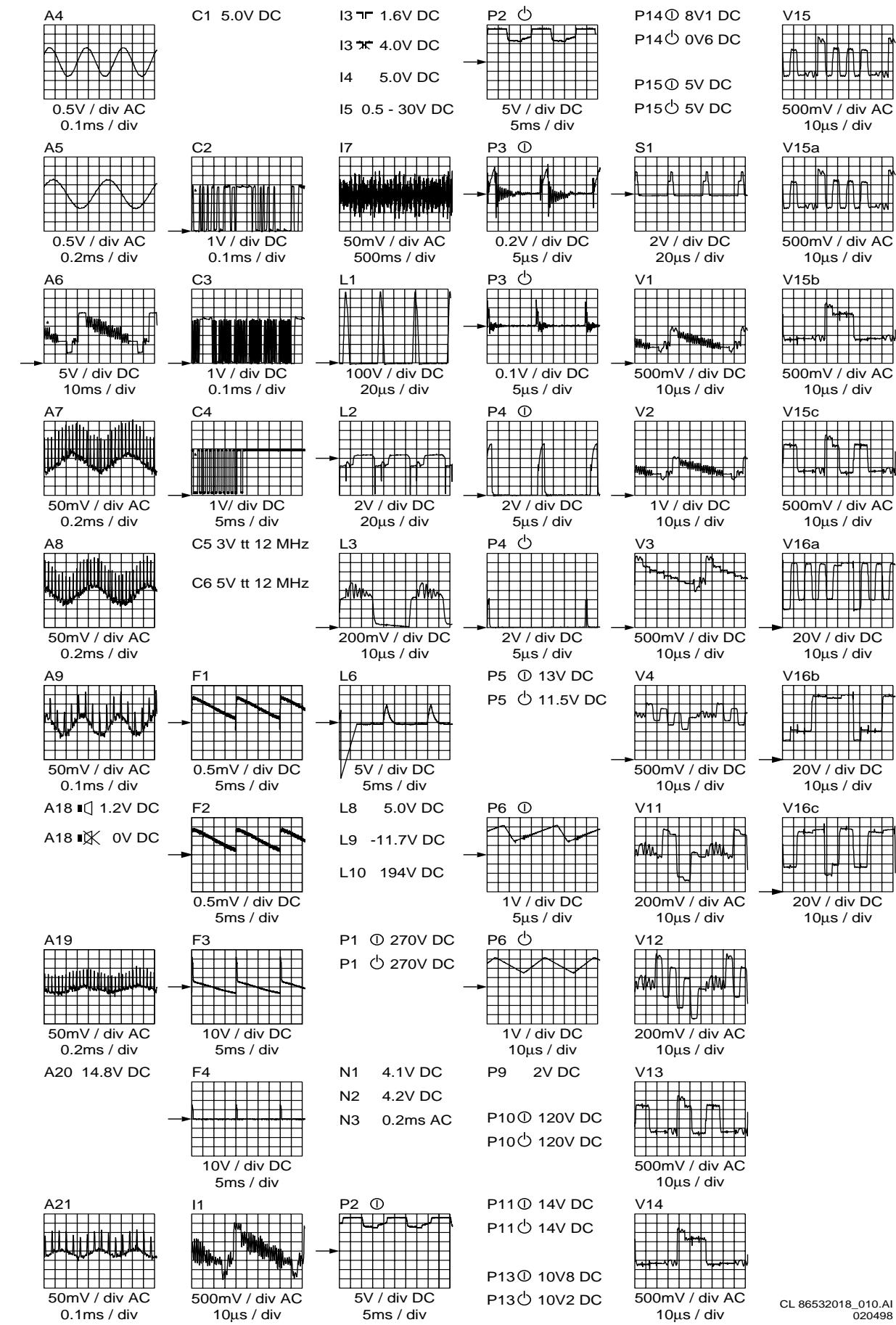
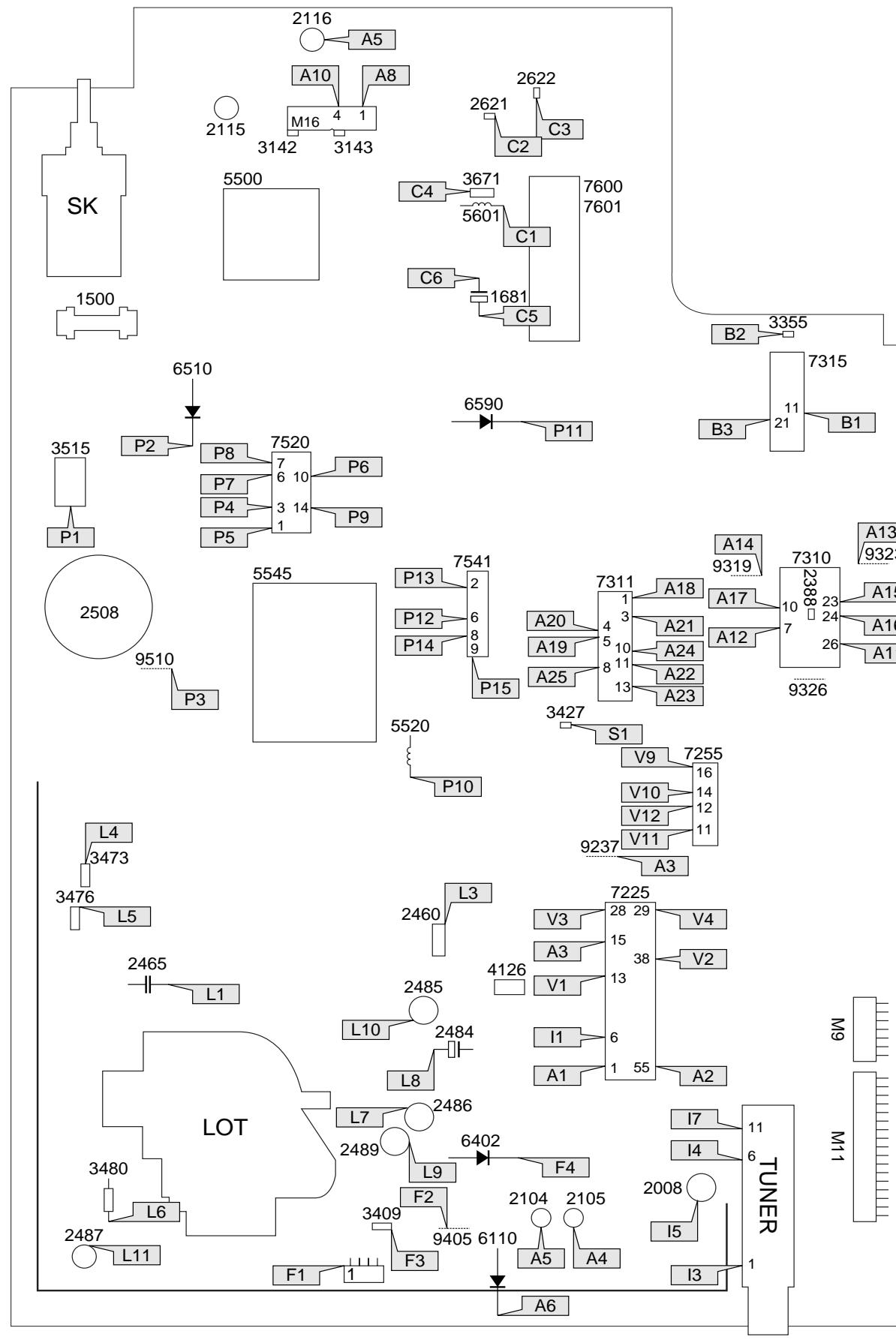


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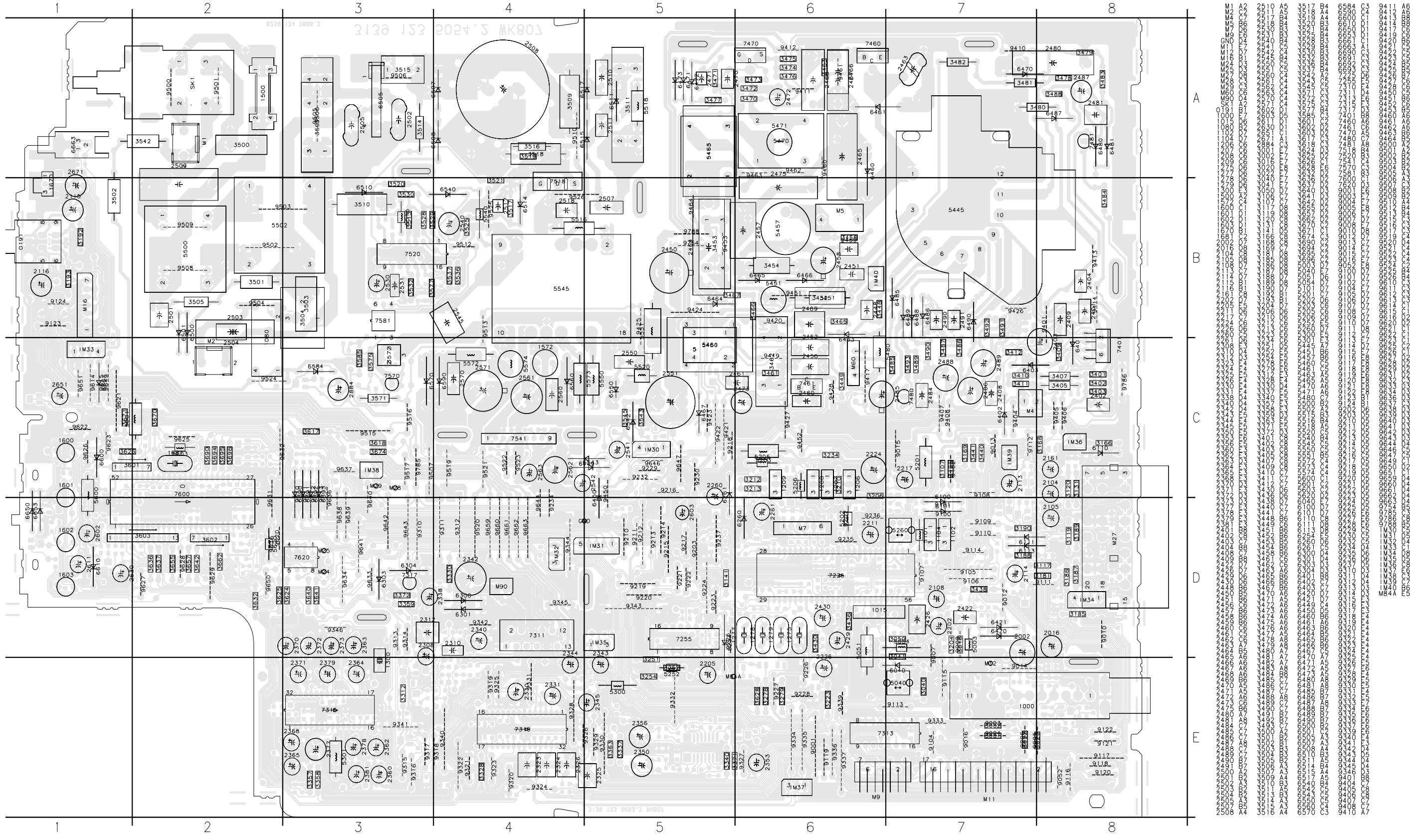


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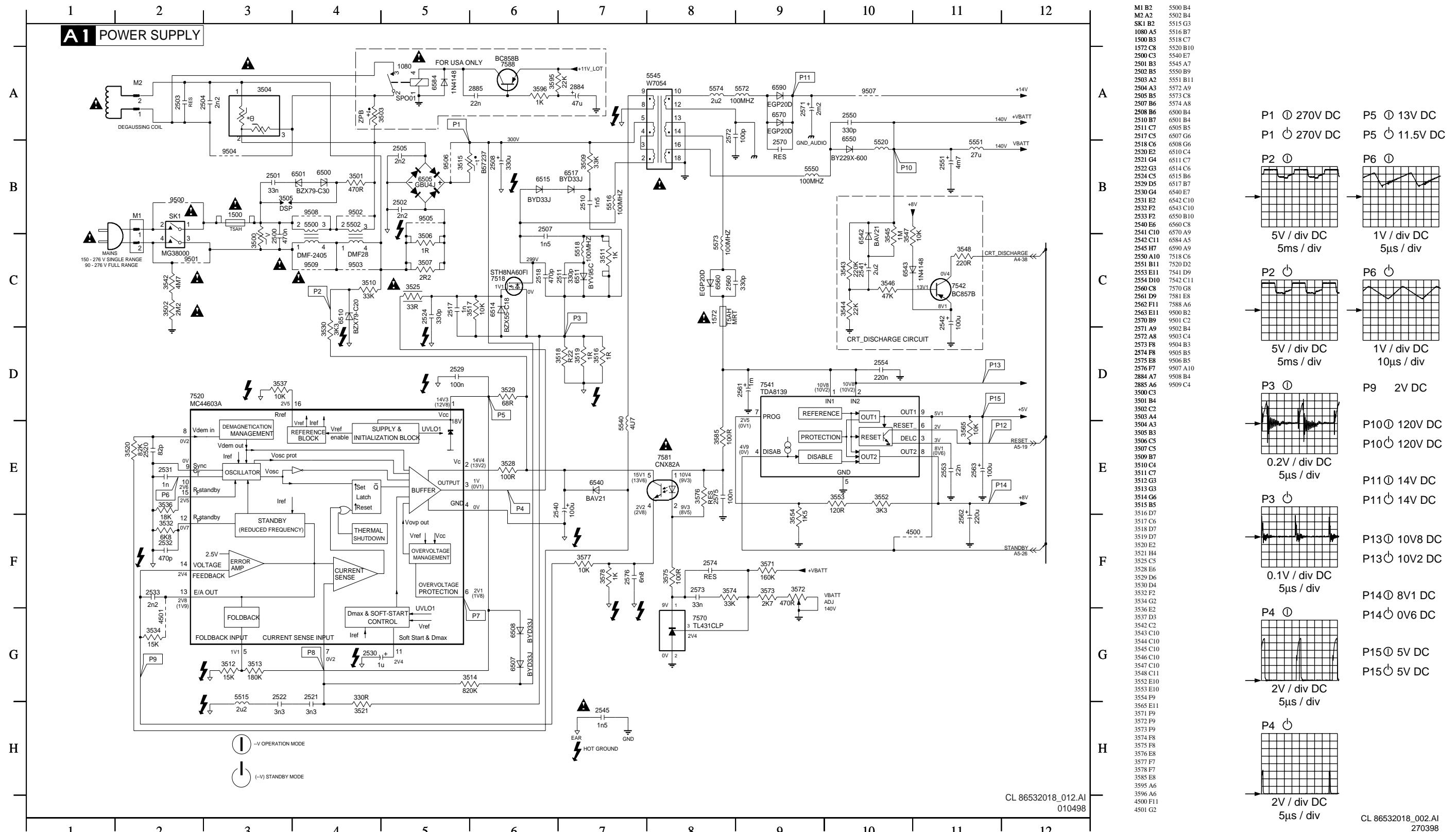
6 Block diagram



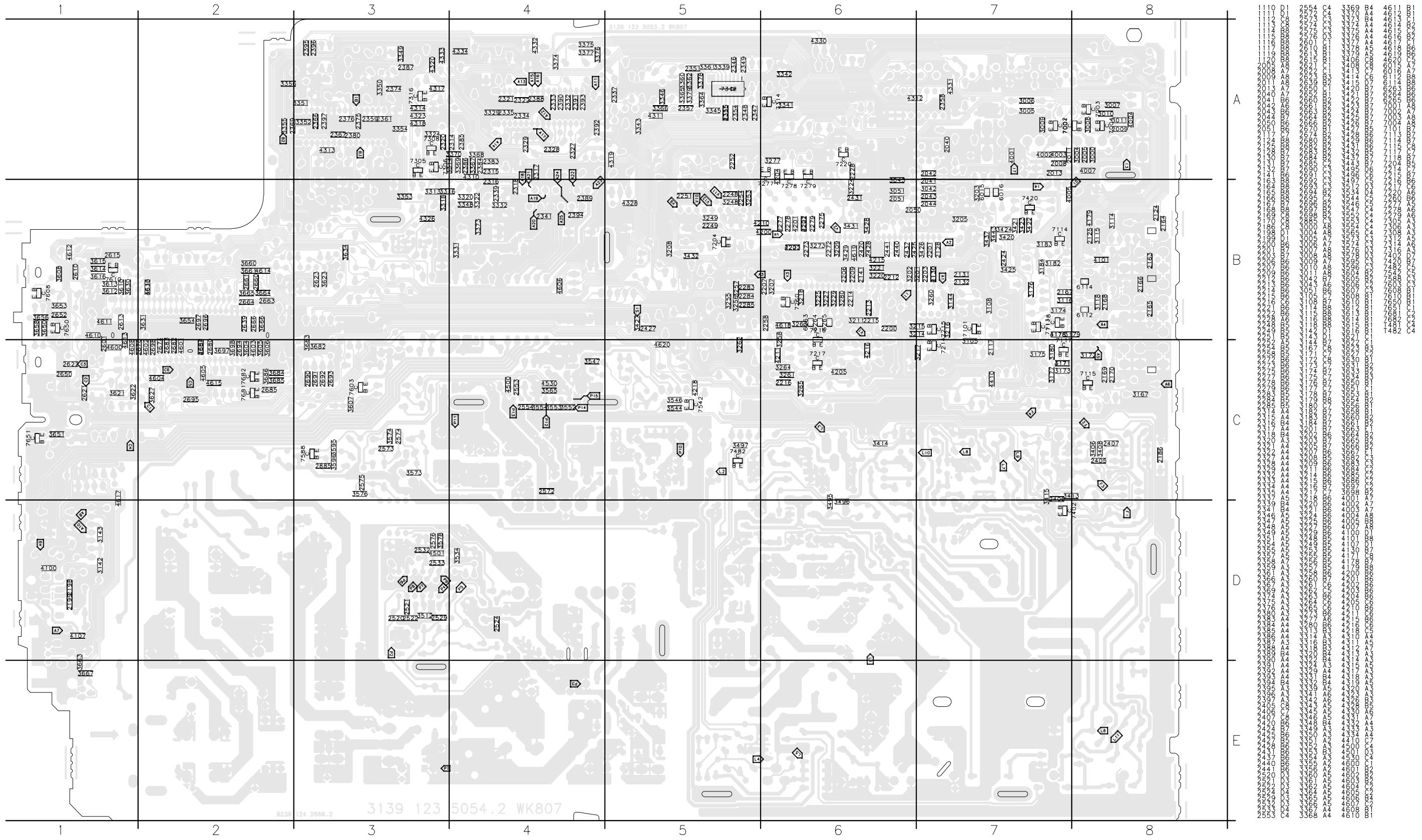
7 Diagrams and print lay-outs



7 Diagrams and print lay-outs



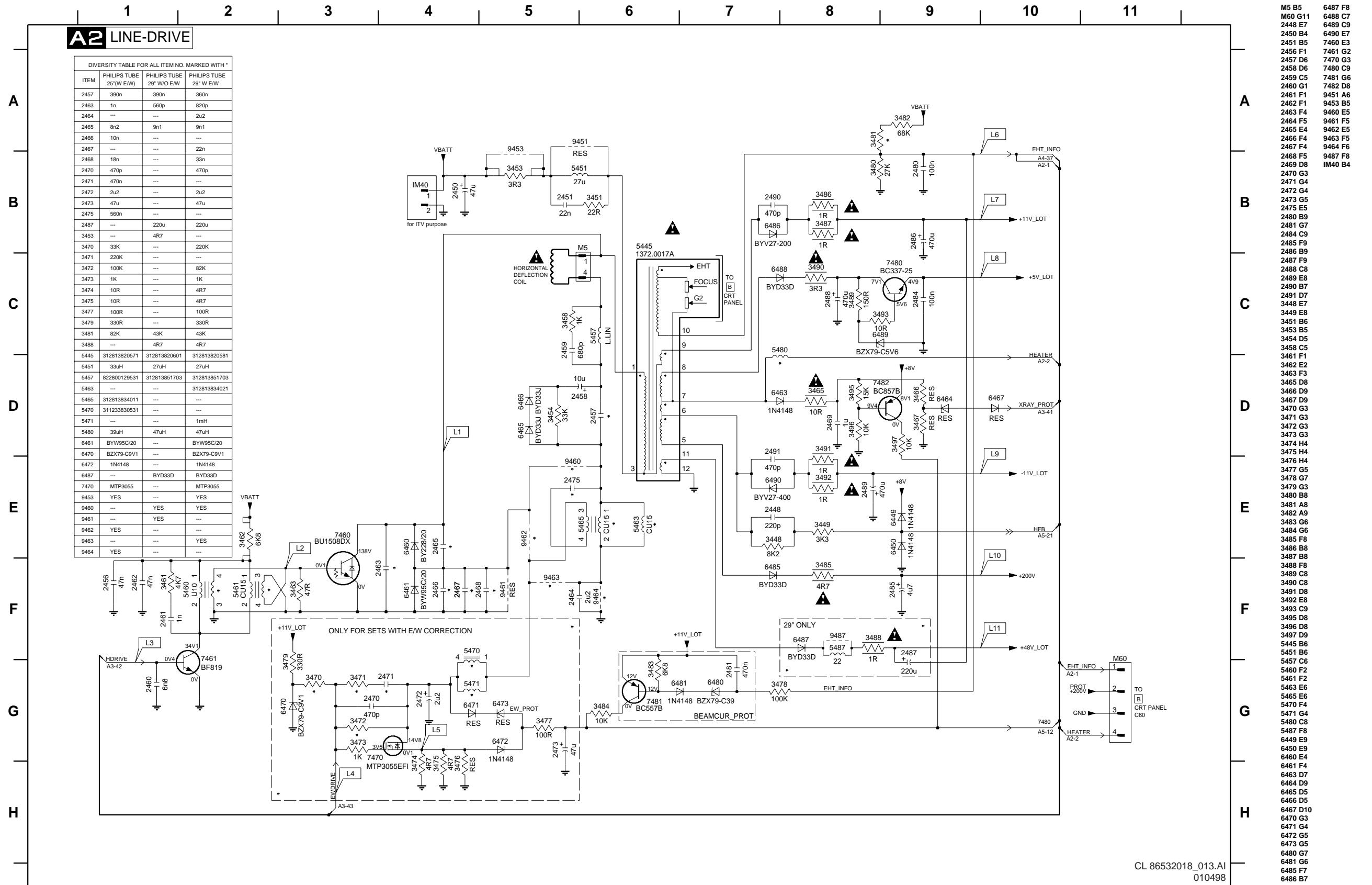
7 Diagrams and print lay-outs



7 Diagrams and print lay-outs

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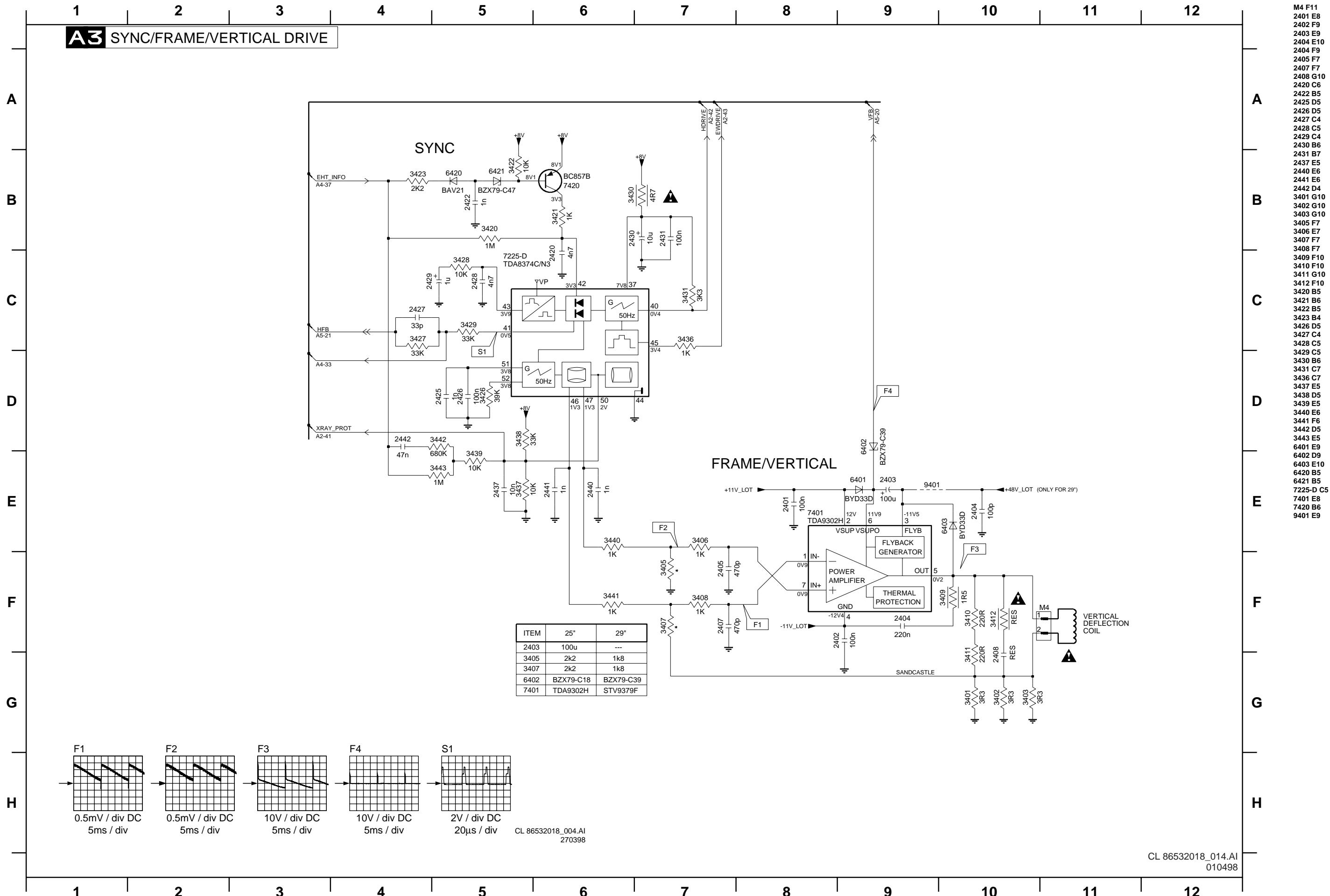


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M60 G11	6488 C7
2448 E7	6489 C9
2450 B4	6490 E7
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2457 D6	7470 G3
2458 D6	7480 C9
2459 C5	7481 G6
2460 G1	7482 D8
2461 F1	9451 A6
2462 F1	9453 B5
2463 F4	9460 E5
2464 F5	9461 F5
2465 E4	9462 E5
2466 F4	9463 F5
2467 F4	9464 F6
2468 F5	9487 F8
2469 D8	IM40 B4
2470 G3	
2471 G4	
2472 G4	
2473 G5	
2475 E5	
2480 B9	
2481 G7	
2484 C9	
2485 F9	
2486 B9	
2487 F9	
2488 C8	
2489 E8	
2490 B7	
2491 D7	
3448 E7	
3449 E8	
3451 B6	
3453 B5	
3454 D5	
3458 C5	
3461 F1	
3462 E2	
3463 F3	
3465 D8	
3466 D9	
3467 D9	
3470 G3	
3471 G3	
3472 G3	
3473 G3	
3474 H4	
3475 H4	
3476 H4	
3477 G5	
3478 G7	
3479 G3	
3480 B8	
3481 A8	
3482 A9	
3483 G6	
3484 G6	
3485 F8	
3486 B8	
3487 B6	
3488 F8	
3489 C8	
3490 C8	
3491 D8	
3492 E8	
3493 C9	
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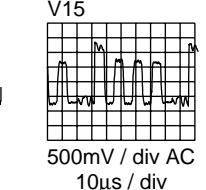
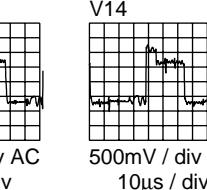
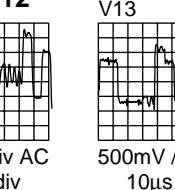
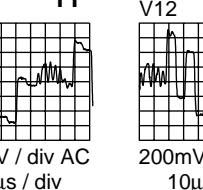
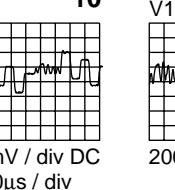
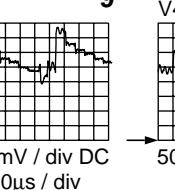
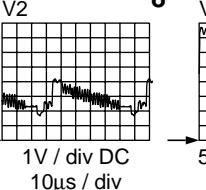
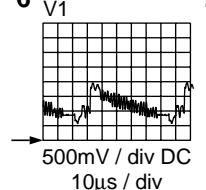
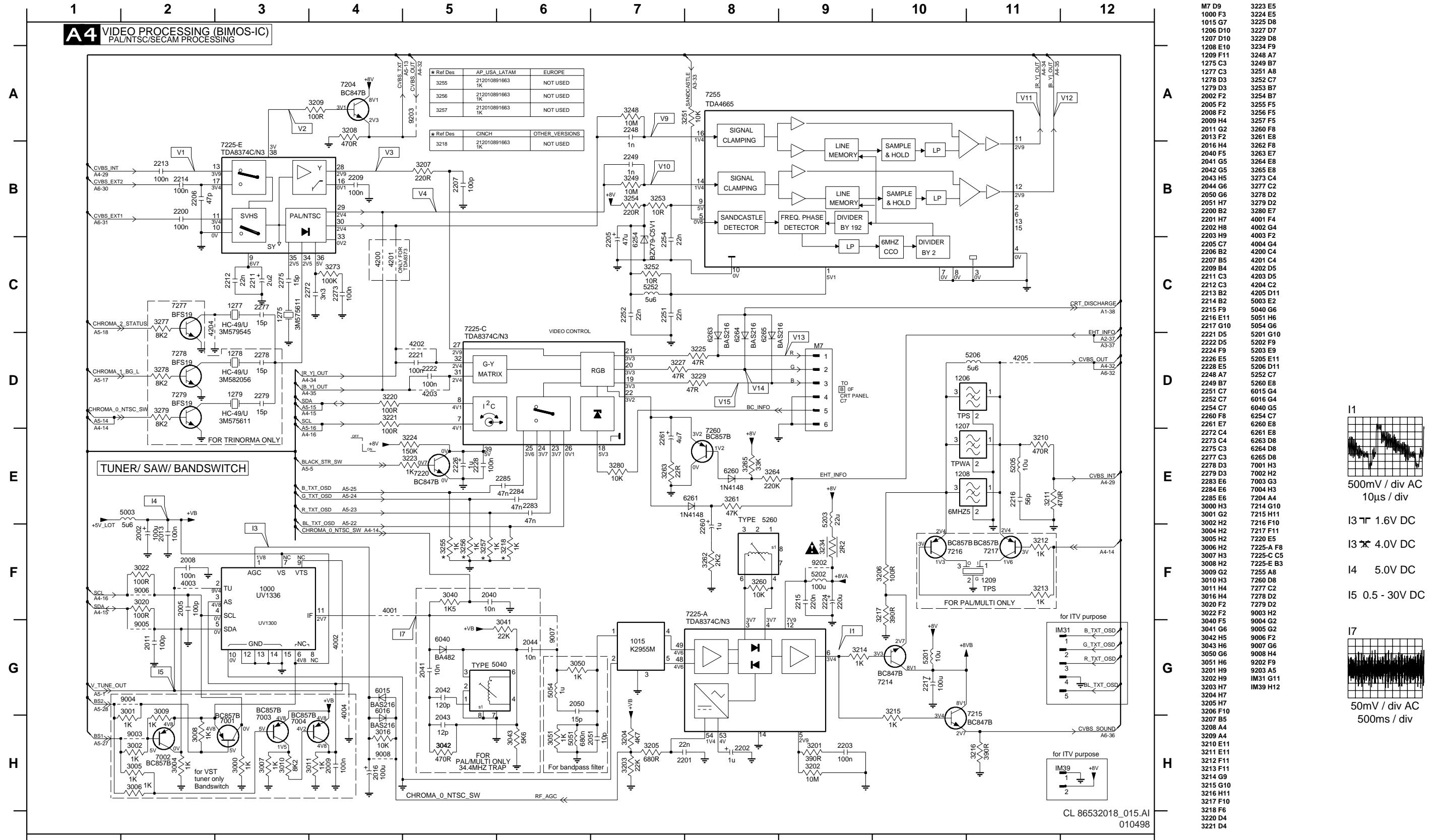
7 Diagrams and print lay-outs

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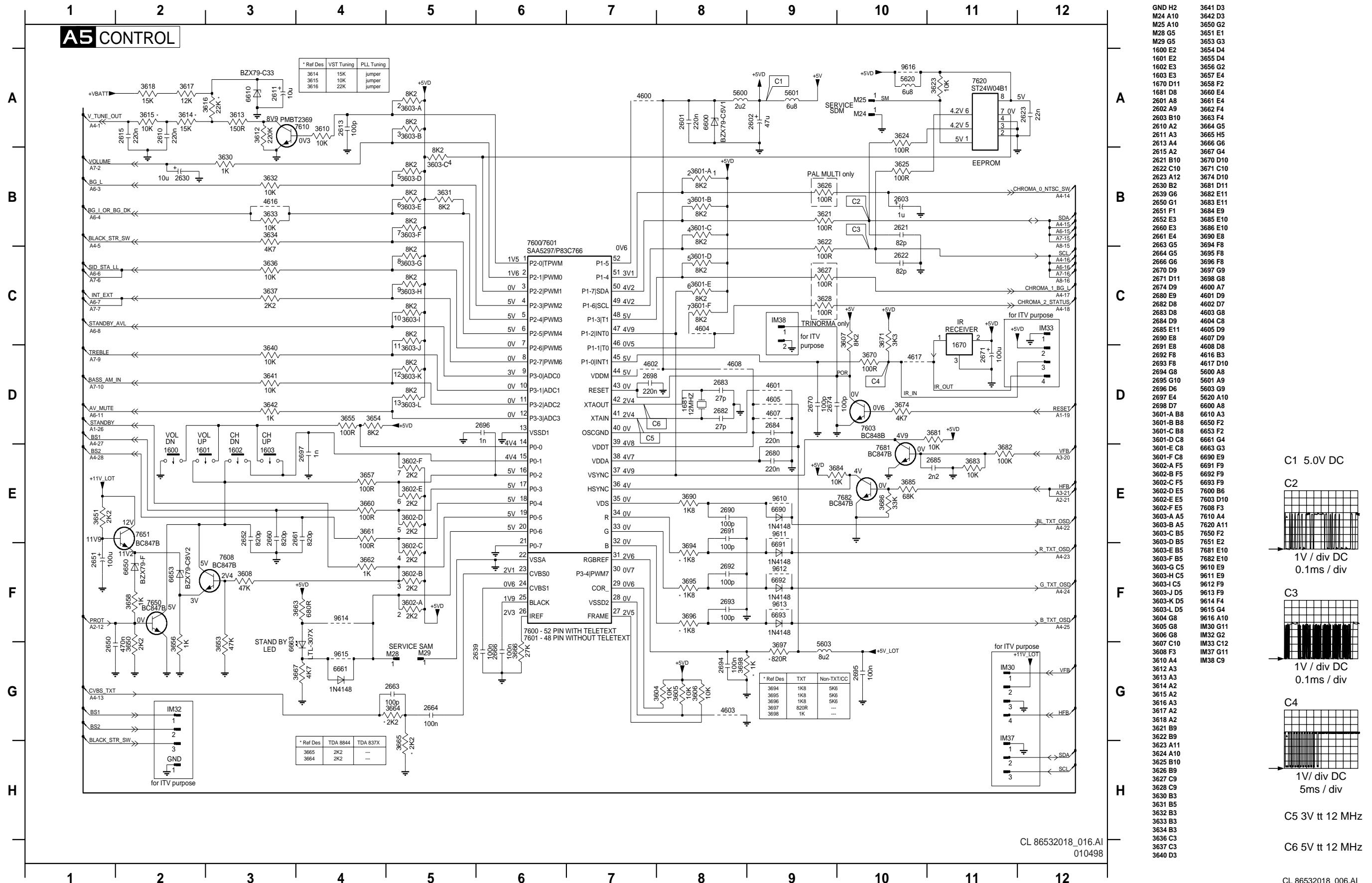
7 Diagrams and print lay-outs



7 Diagrams and print lay-outs

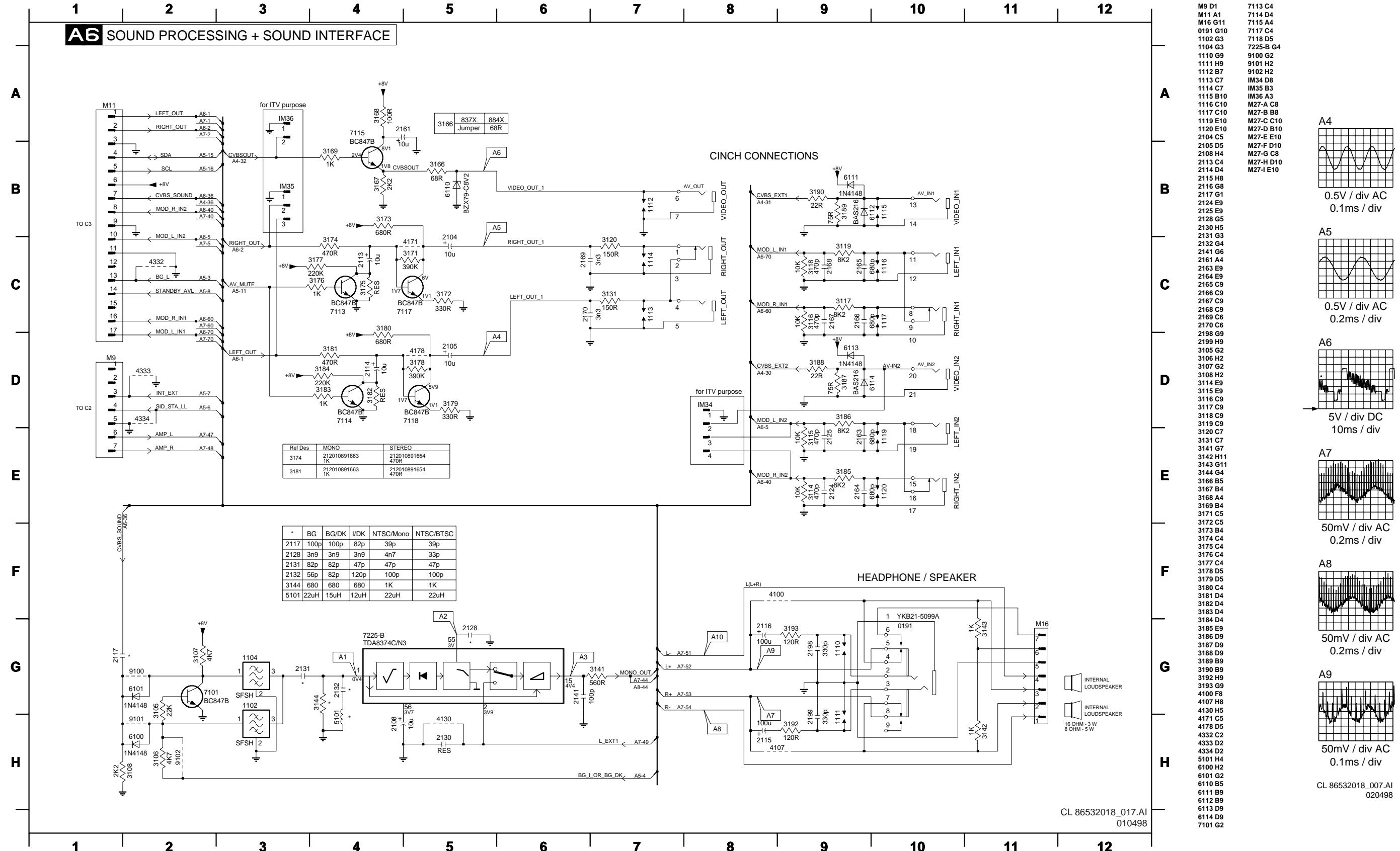
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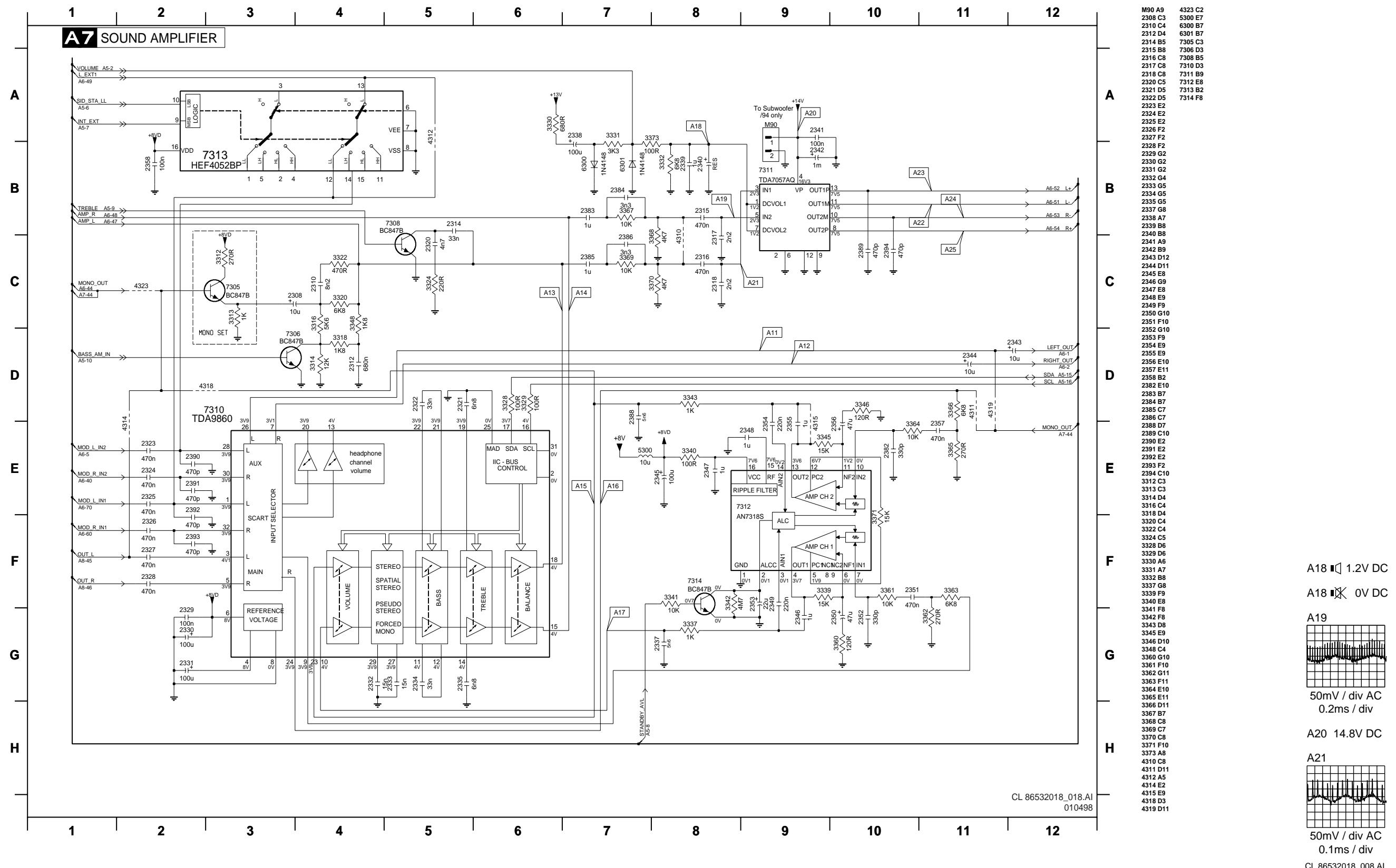
7 Diagrams and print lay-outs



7 Diagrams and print lay-outs

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A18 ■ 1.2V DC

A18 ■ 0V DC

A19
50mV / div AC
0.2ms / div

A20 14.8V DC

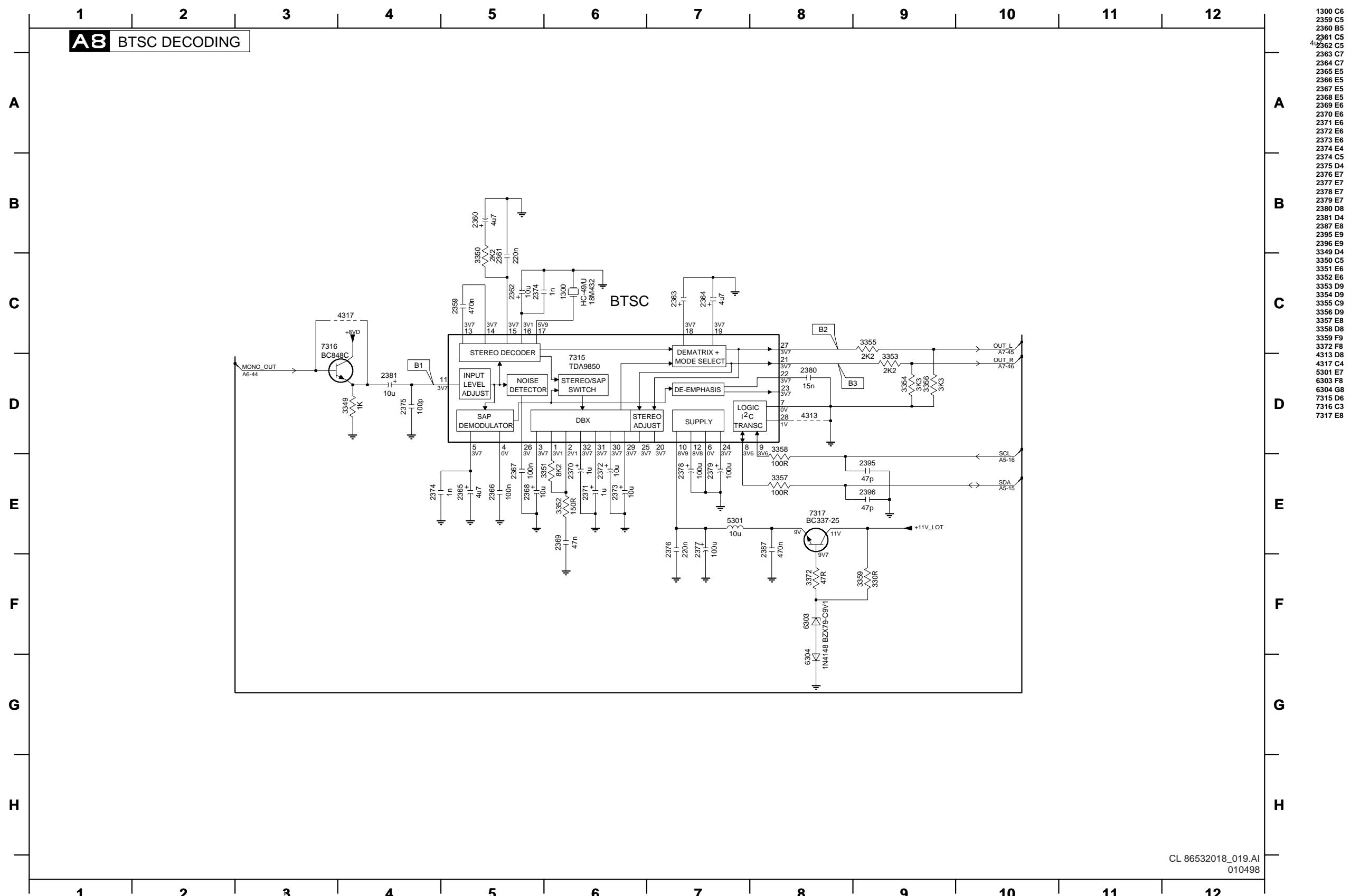
A21
50mV / div AC
0.1ms / div

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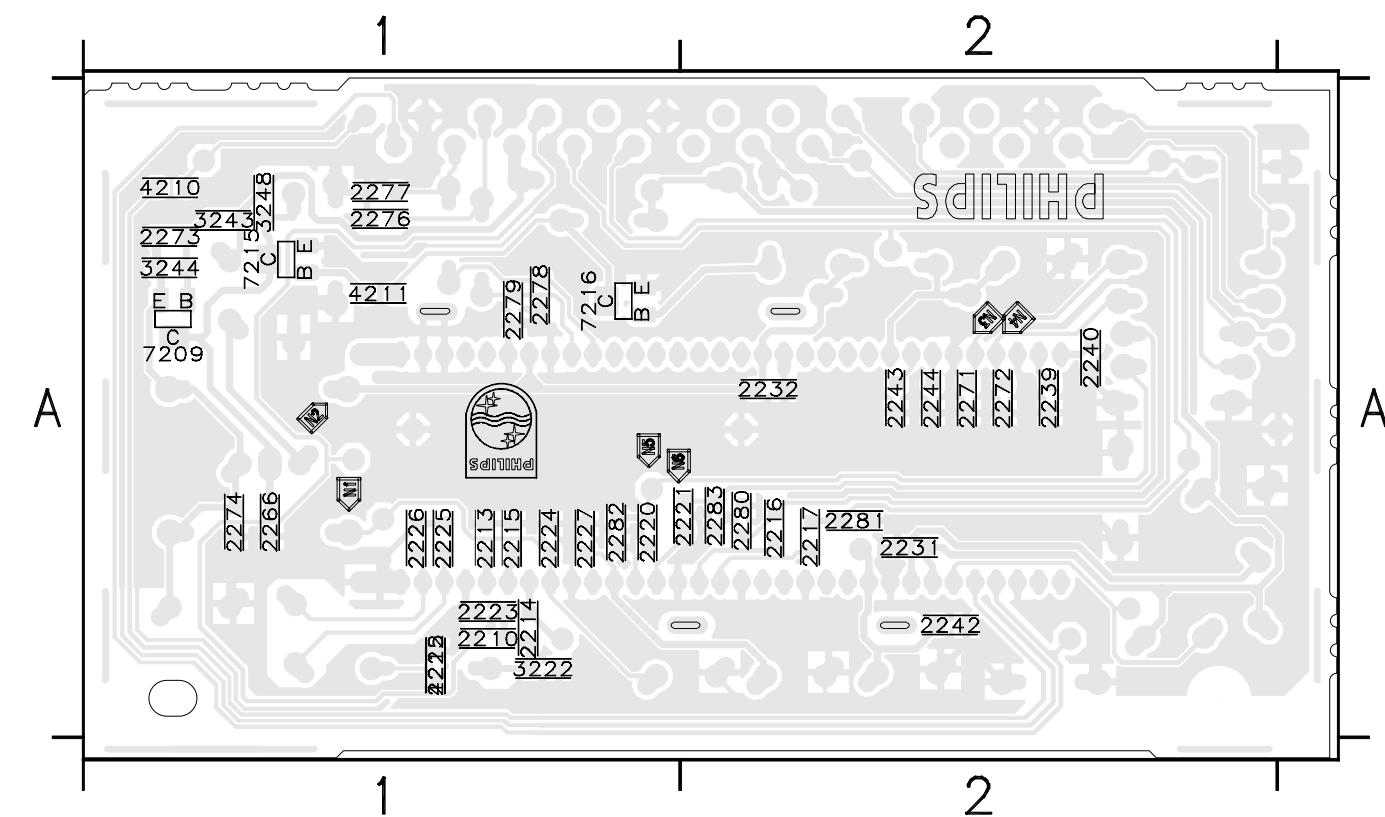
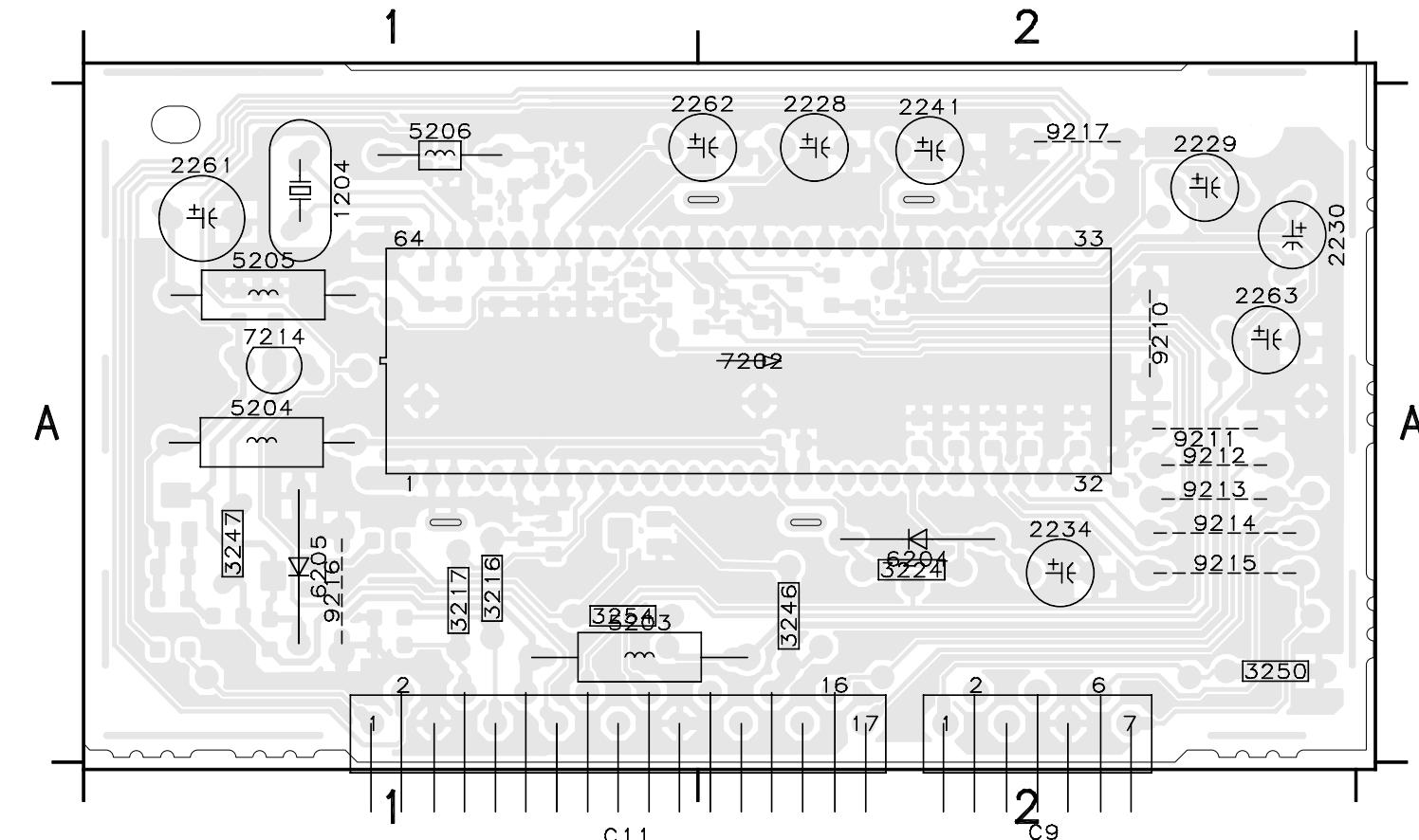
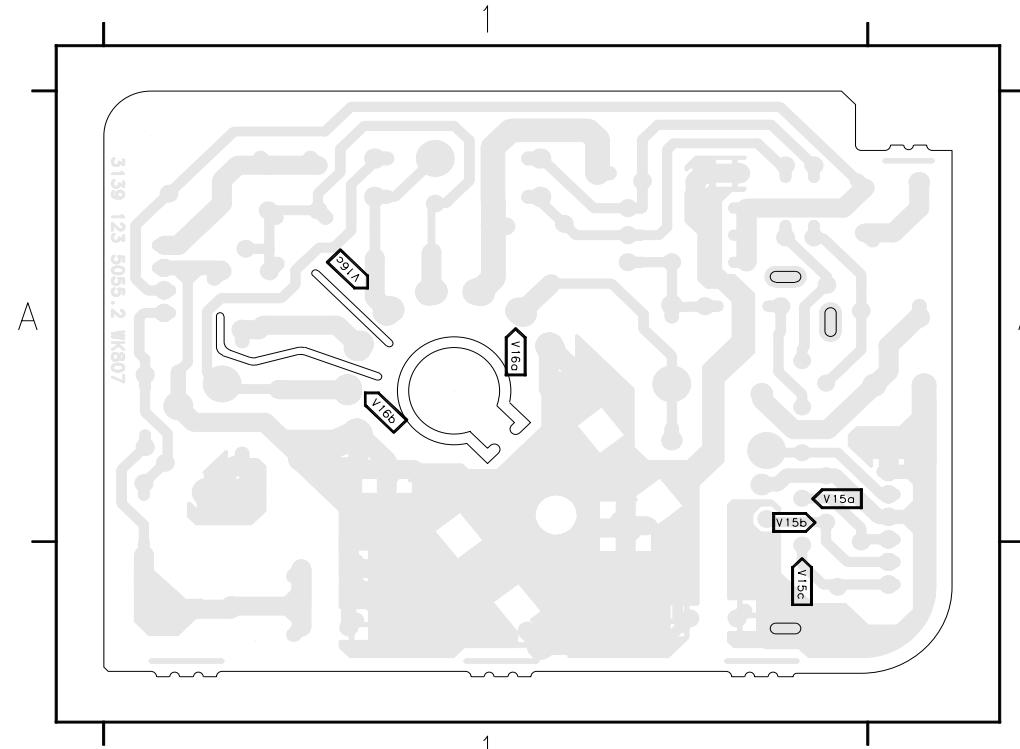
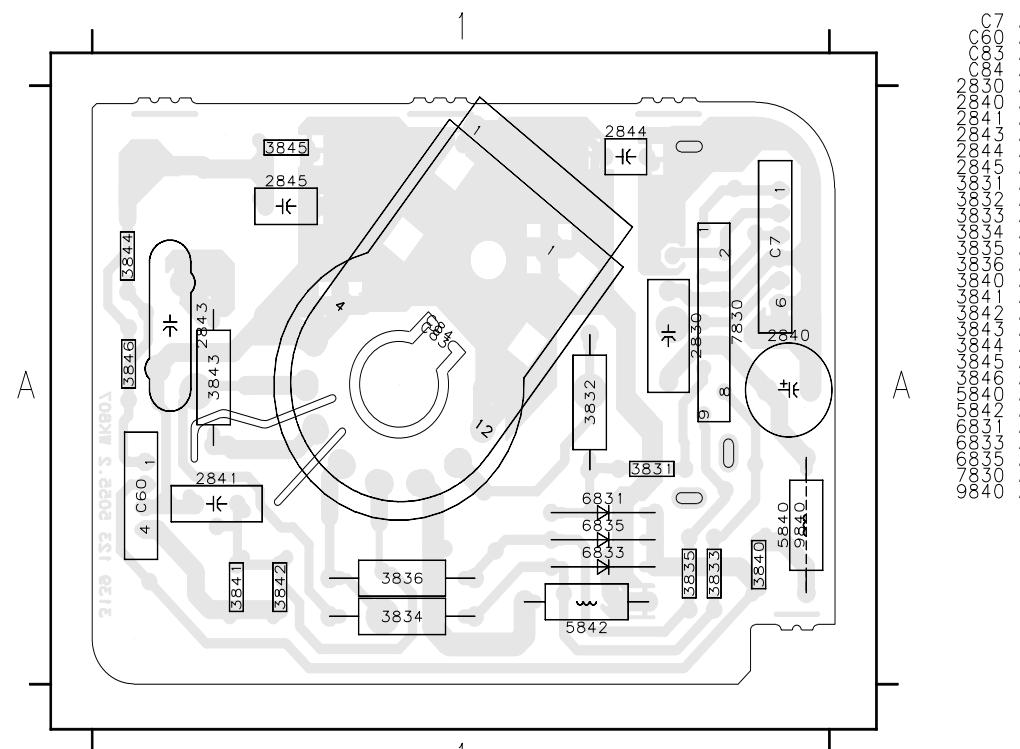
L7.3A

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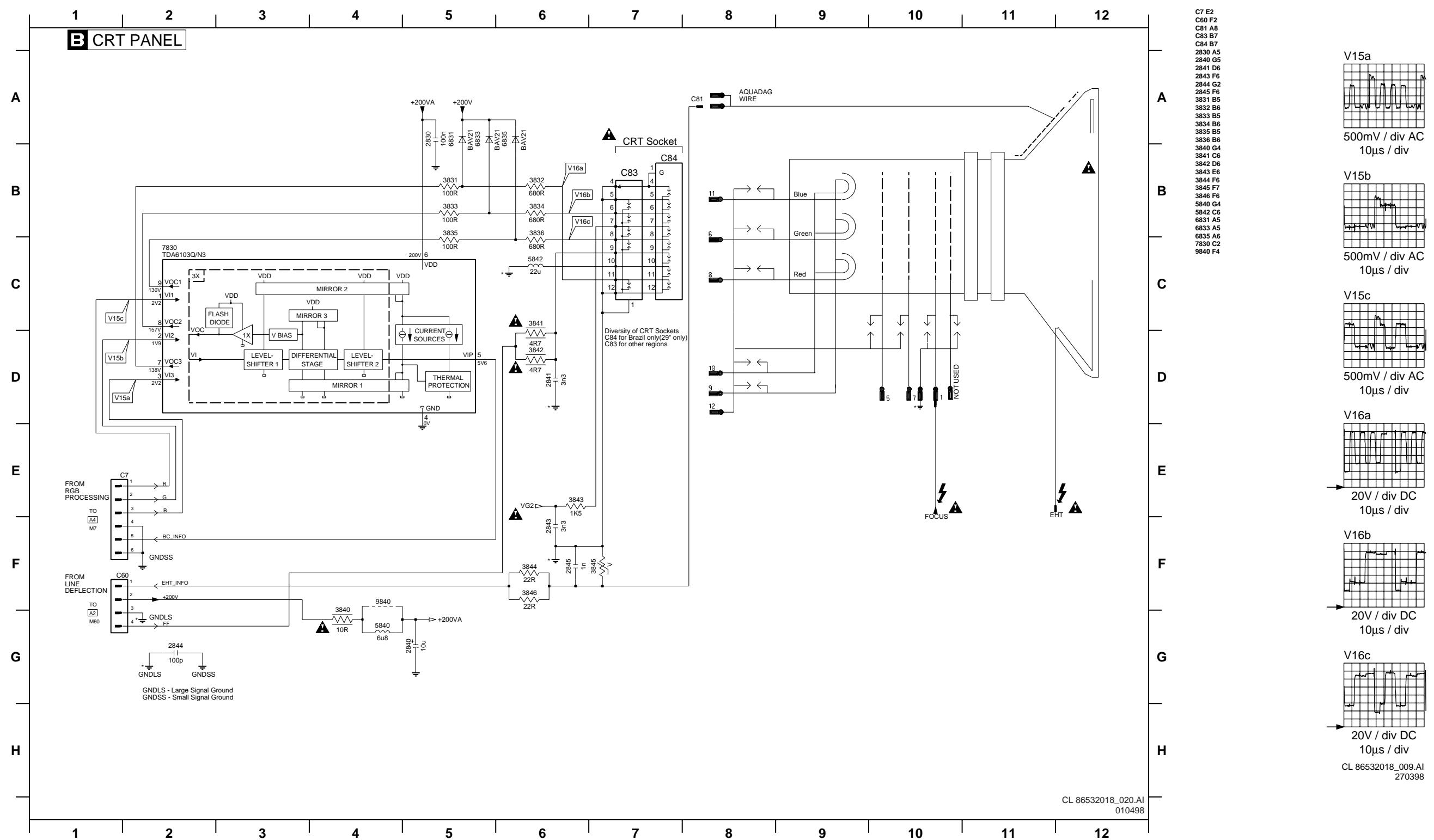
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 2361 C5
 2362 C5
 2363 C7
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 2365 E5
 2366 E5
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 2369 E6
 2370 E6
 2371 E6
 2372 E6
 2373 E6
 2374 E4
 2375 C5
 2376 E7
 2377 E7
 2378 E7
 2379 E7
 2380 D8
 2381 D4
 2387 E8
 2395 E9
 2396 E9
 3349 D4
 3350 C5
 3351 E6
 3352 E6
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 3354 D9
 3355 C9
 3356 D9
 3357 E8
 3358 D8
 3359 F9
 3372 F8
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 7316 C3
 7317 E8

7 Diagrams and print lay-outs

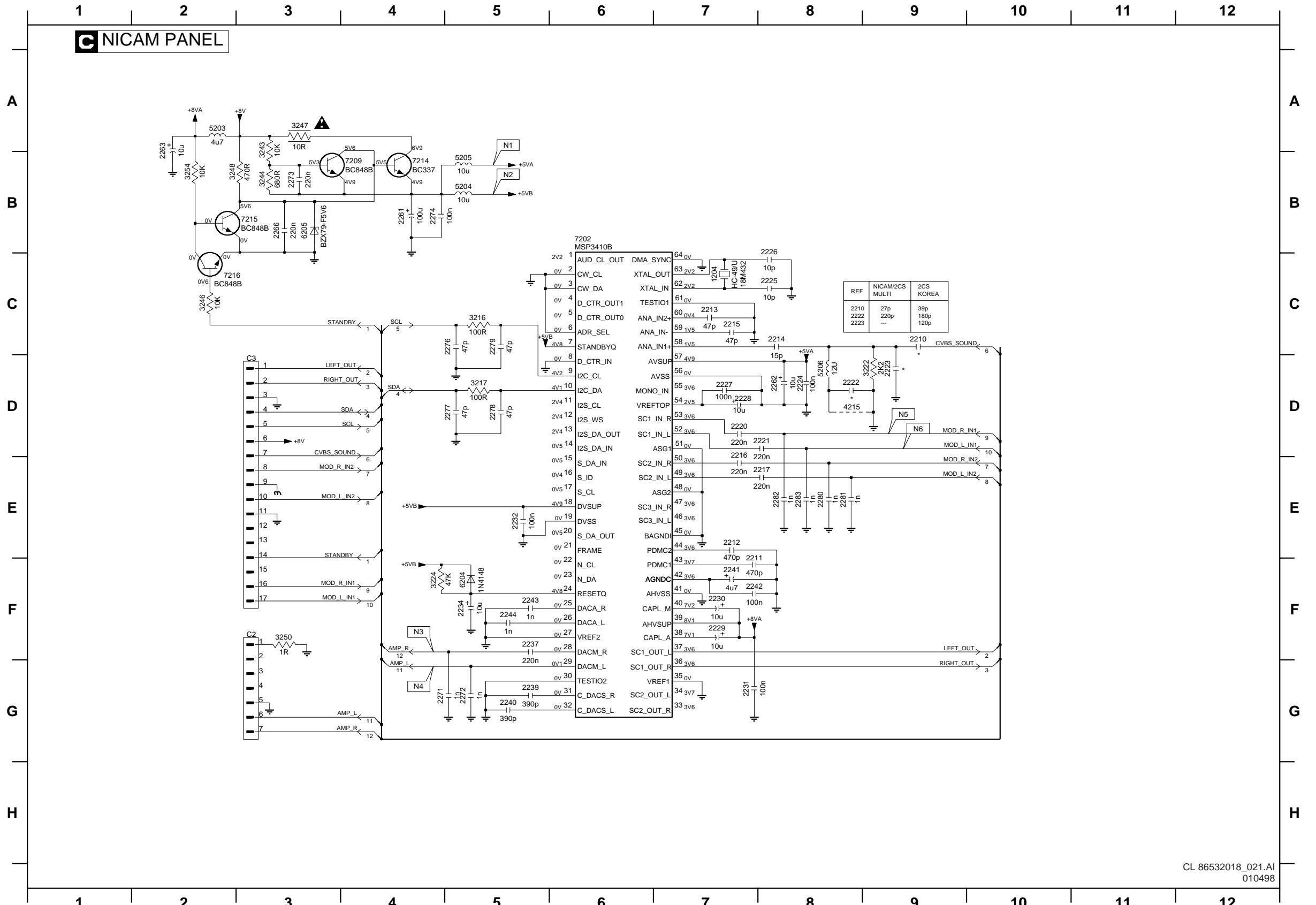


C9 A2
C11 A1
1204 A1
2228 A2
2229 A2
2230 A2
2234 A2
2241 A2
2261 A1
2262 A2
2263 A2
3218 A1
3217 A1
3224 A2
3248 A2
3247 A1
3250 A2
3254 A1
5203 A2
5204 A1
5205 A1
5206 A1
6204 A2
6205 A1
7202 A1
7214 A1
9210 A2
9211 A2
9212 A2
9213 A2
9214 A2
9215 A2
9216 A1
9217 A2

2210	A1	3222	A1
2213	A1	3243	A1
2214	A1	3244	A1
2215	A1	3248	A1
2216	A2	4210	A1
2217	A2	4211	A1
2220	A1	4215	A1
2221	A2	7209	A1
2222	A1	7215	A1
2223	A1	7216	A1
2224	A1		
2225	A1		
2226	A1		
2227	A1		
2231	A2		
2232	A2		
2239	A2		
2240	A2		
2242	A2		
2243	A2		
2244	A2		
2266	A1		
2271	A2		
2272	A2		
2273	A1		
2274	A1		
2276	A1		
2277	A1		
2278	A1		
2279	A1		
2280	A2		
2281	A2		
2282	A1		
2283	A2		



7 Diagrams and print lay-outs



N1 4.1V DC
N2 4.2V DC
N3 0.2ms AC

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8 Electrical adjustments

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Notes :

Unless stated otherwise, the supply voltage used is :

- 220v to 240v +/- 10%, 50 - 60 Hz +/- 5%.
- Voltage and oscillograms are measured in relation to earth.
- Never use a heatsink as earth.

For the adjustments a colour pattern generator PM5418 has been used. Pattern generator type PM5193 (optional) .

8.1 Settings on the carrier panel

8.1.1 + 140 V supply voltage for 25" and 29" picture tubes.

Connect a multimeter (DC) across C2551. Set brightness at mid position and contrast at maximum. Apply a pattern generator with a colour bar. Adjust potentiometer R3572 to 140 Volts +/- 1 V DC.

8.1.2 Geometry adjustments (software adjustment)

Set pattern generator (e.g. PM5418) with colour bar pattern and connect to aerial input with RF signal amplitude - 1mv. Enter SAM mode byusing the Dealer Service Tool or short-circuit service pins M28 and M29 on PCB and switch power "ON". The menu is displayed whenever SAM mode is entered.

Enter into GEOMETRY menu.

- Vertical Amplitude and position
 - Select item VSH and set vertical shift to 31 (Nominal value !!).
 - Select item VAM and align the top of the screen with vertical amplitude.
 - Select item VSL and align the bottom of the screen with vertical slope.
- Horizontal Amplitude and phase
 - Select item HSH and align horizontal shift until picture to center of the screen.
 - Select item EW and align the picture width (only those sets with East-West drive circuit).
 - Select item PW and align East-West parabola for straight vertical lines.
 - Select item TC and align east-west trapezium for a rectangle.
 - Repeat if necessary.

General :

The value can be increment or decrement by pressing the user remote control right or left key. The results of the picture will change according to the selected item.

8.1.3 Focusing

Set pattern generator (e.g. PM5418) with colour bar pattern and connect to aerial input with RF signal amplitude of 1mv. Set brightness and contrast at maximum. Adjusted with focusing potentiometer (upper hole of LOT 5445) for maximum sharpness of the picture.

8.1.4 IF-PLL / RF AGC adjustment (software adjustment)

Set generator (e.g. PM5418) with colour bar pattern and set frequency for PAL to 189.25MHz and for NTSC 55.25MHz with RF signal amplitudeof 3mv (70dBuv) and connect this signal to the aerial input.

Enter SAM mode by Dealer Service Tool or short-circuit service pins M28 and M29 on PCB and switch power ON. The menu is displayed whenever SAM mode is entered. Enter into TUNER menu, select items IF-PLL for AFT alignment

Method Of Alignment

- Set AFW to 80 kHz
- Adjust IF-PLL until AFA & AFB just switching from "0" to "1". The correct value for IF-PLL is 61.

Enter into TUNER menu, select items AGC for RF AGC alignment.

Method Of Alignment

- Connect a multimeter (DC) at pin 1 of tuner.
- Adjust the AGC until the voltage at pin 1 of the tuner is 4Volts +/- 0.4V DC. The correct value for AGC should be 37.

The value can be increment or decrement by pressing the user remote control right or left key.

8.1.5 Picture demodulator adjustment

Connect a pattern generator (e.g. PM5418) with a cross hatch. Connect an oscilloscope (1ms/div) to pin 6 of IC 7225-5A and adjust L5260 so that the overshoot response is minimum . Select a colour bar signal and verify if the picture is OK.

8.1.6 34.4MHz Trap Alignment

NOTE: For Pal multi set only.

GENERAL SET-UP

Connect Frequency Synthesizer PM5193 with sine wave and signal strength of 100mv rms. (100dBuv) via a 50 ohm cable terminated with a RC network(50ohm,10nF) and connect to tuner C-CUT (C-CUT OPEN) . Output detection points at saw filter coil 5040 ,pin 3of 1050. The capacitor is in series with the signal, the resistor is connected to ground. Adjust L5040 for minimum amplitude.

8.2 VG2 and White-D settings

8.2.1 Vg2 cut off alignment

Connect a pattern generator (e.g. PM5418) with a black picture , set picture controls of the TV set as following :

Brightness: 25

Colour : 30

Contrast: 50

Switch on the TV set and measure with a 100 : 1 scope probe at the cathodes of the picture tube socket pin 6,8 and 11 of CRT sockets C83/84 ; alternately pin 6 for Green, pin 8 for Red and pin 11 for Blue .

Measure the level of the pluses and adjust the level of the highest pluses by means of the Vg2 potentiometer (lower knob of LOT 5445). The DC level must be 140 Volts +/- 2V DC for 25" tubes and 160Volts +/-2V DC for 29" picture tubes.

8 Electrical adjustments

8.2.2 White-D adjustment (software adjustment)

Connect a pattern generator (e.g. PM5418) and set it to white raster pattern.

Enter SAM by using the Dealer Service Tool or short-circuit service pins M28 and M29 on PCB and switch power "ON"

The menu will be displayed whenever SAM mode is entered. Enter into WHITE TONE menu and select item NORMAL, DELTAWARM, or DELTACOOL; only one of the three items (R, G or B) will be displayed on the screen. The initial default value setting for NORMAL is 37. The factory settings of the colour temperatures are:

NORMAL	10500K	R = 37	G = 37	B = 37
DELTA COOL	14000K	R = 0	G = 254	B = 249
DELTA WARM	8200K	R = 0	G = 1	B = 3

8.3 Audio adjustments

8.3.1 BTSC Sound adjustment (software adjustment)

Set generator (e.g. PM5418) BLACK(without video) pattern, set audio to stereo mode, carrier "ON" and test data to 1, RF frequency NTSC 187.25MHz with signal amplitude of 1mv and connect this signal to the aerial input.

Enter SAM mode by Dealer Service Tool or short-circuit service pins M28 and M29 on PCB and switch power "ON"

The menu is present whenever SAM mode is entered. Enter into the AUDIO menu and select item according to factory setting. Check the sound quality by the generator setting listed below.

The factory settings for BTSC are :

AUDIO	
AUTO	>
ST-NT	7
SAP-NT	7
LA	7
WIDEB	19
SPECT	19

Abbreviations:

ST-NT -> Stereo Nicam adjustment

SAP-NT -> Second audio program adjustment

LA -> L L' adjustment

WIDEB -> Wideband adjustment

SPECT -> Spectral adjustment

NOTE: ALL VALUES ARE RECOMMENDED NOMINAL VALUES.

8.3.2 2CS/NICAM Sound adjustment (software adjustment)

Set generator (e.g. PM5418) BLACK(without video) pattern, set audio to stereo mode, carrier "ON", set RF frequency PAL 189.25MHz, NTSC 55.25MHz with signal amplitude - 1mv and connect to aerial input. Enter the SAM mode by using the Dealer Service Tool or short-circuit service pins M28 and M29 on PCB and switch power "ON". The menu is displayed whenever SAM mode is entered.

Enter into AUDIO menu and select item according to factory setting. Check the sound quality by the above generator setting. The factory settings for 2CS/NICAM are :

AUDIO	
A-FM	80
AT	8
STEREO	143
DUAL	113

NOTE: ALL VALUES ARE RECOMMENDED NOMINAL VALUES.

9 Circuit description new circuits

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Power supply (diagram A1)

9.1 Introduction

9.1.1 General

The switched mode power supply (SMPS) is mains isolated. The control IC7520 (MC44603AP) gives pulses for driving FET 7518 with duty cycle control at fixed frequency of nominal 40 kHz in normal operation. In stand-by, slow-start and overload situation the SMPS runs at other frequencies than these 40 kHz.

Basic characteristics of SMPS :

- Mains Isolated flyback Converter type
- Input range : 90 - 276 Volts AC (multi voltage) or 150 - 276 Volts AC (single voltage).
- Secondary Sensing by Opto Coupler
- IC7520 is Featured with a Slow-Start circuitry
- Over-and undervoltage Protection Circuit of the secondary supply voltages. In case the load decreases under a certain threshold level the SMPS will switch into stand-by-mode (in stand-by SMPS is in the so called "reduced frequency mode"; this is nominal about 20 kHz).

The (VBATT (+140 Volts for A/P and +130 Volts for LATAM output gives a stabilized (VBATT for 25" and 29" tubes in normal operation and approx. 140 Volts DC for A/P and 130 Volts for LATAM in stand-by mode. IC7541 (=TDA8139) having a stable regulator for +5 Volts and +8 Volts supply and also a built-in protection circuit for +5 Volts and +8 Volts supply is present. If pin 4 is low in the standby mode, the IC disables the supply output voltage of (8Volts, so IC7225 (TDA8374C) line output stage will be shut "down". If no 8 Volts supply is present transistor 7542 will switch on, this will cut off the +VBATT; it also performs a fast CRT discharge. If the power is switched off, pin 6 of IC7541 will be low and the protection circuit starts to shut down the +5 Volts and +8 Volts.

9.1.2 Output voltages

Power output secondary output voltages:

- 14 Volts for the audio amplifier
- 13 Volts for the audio processing
- 5 Volts for the control part (is also available in standby/ reset)
- 8 Volts for the video processing and Nicam
- 140 Volts for A/P and (130 Volts for LATAM for the line output stage and the tuning system

9.1.3 The switching periods of TS7518

The duty cycle of the power supply depends on T-on of FET TS7518 and this is controlled by pin 3 of IC7520. This IC detects the variations of the (VBATT via sensing-winding 8-9 at the primary side of T5545. The switching period of TS7518 can be divided into three main phases: Duty cycle T-on, T-off and T-dead

- During T-on, FET 7518 will switch on and conducts, so the energy that is extracted from the mains is stored into the primary winding 2-5 of transformer T5545 with a linear increasing primary current. The slope depends on the voltage across C2508. Via T-on regulation by pin 3 of IC7520 the duty cycle of the SMPS and so the (VBATT is controlled.
- During T-on FET 7518 will switch off and does not conduct, so all energy "inside" the transformer is supplied to the load via secondary windings of T5545 and the secondary

diodes (D6550, D6560 and D6570,D6590). The current through the secondary side of the transformer decreases with a linear slope of T5545.

- During T-dead FET 7518 does not conduct and no energy is extracted from the mains of supplied side, this gives space for T-on and T-off regulation.

9.2 Primary side

9.2.1 Mains input and degaussing

- Mains voltage: this voltage is filtered by L5500 and 5502, rectified by a diode bridge rectifier 6505 and smoothed by C2508 to the DC input voltage for the SMPS at pin 5 of T5545 (e.g. 300V DC for 220V AC mains).
- Multi voltage: Only small adaptations of some component values are needed to achieve this.
- Degaussing : R3504 is a dual PTC (2PTC's in one housing). When switching "on" the set, the PTC is cold and low-ohmic. This would give very high degaussing current. After degaussing the PTC will be heated up and become high-ohmic; during normal operation the degaussing current is very low.

9.2.2 Start up and take over

- Start-up : Via the start-up circuitry 3510, 3530 and 3529 the voltage coming from 220V AC mains is used to start-up IC7520 via the supply pin 1. As long as pin 1 has not reached 14.5 Volts, IC7520 does not start up and only sinks 0.3mA. If Vpin 1 reaches the 14.5 Volts, IC7520 will start (FET 7518 will conduct) and pin 1 sinks a typical supply current of about 17 mA. This supply current cannot be delivered by the start-up circuitry, so a take-over circuit must be present. If no take-over will take place the voltage on pin 1 will decrease and IC7520 will switch off; in that case the restart procedure will start again.
- Take over of IC7520: During start-up a voltage across winding 8 - 9 is built up. At the moment the voltage across winding 8 - 9 reaches approx. (14.5 Volts, D6540 start conducting and takes over the supply voltage Vpin 1 of IC7520 (take over current is approx. 17mA).

Note: This power supply is a SMPS (= Switched Mode Power Supply) and not a SOPS (= Self Oscillating Power Supply).

9.3 Control circuitry; see page 37 for blockdiagram.

9.3.1 IC7520 control mechanisms

IC7520 controls the T-on time of FET 7518 in all three operation modes:

- "Secondary-output-sensing" controls the secondary output voltages via the feedback voltage pin 14.
- "1-prim current sensing" controls both the secondary output voltages and the maximum 1-prim via the current sense voltage pin 7.
- "Demagnetization control" prevents the transformer T5545 from going into saturation via the so called "DEMAG" function at pin 8; this will cause slow-start operation.

9.3.2 Secondary output voltage's feedback (pin 14 of IC7520)

Voltage feedback for T-ON control : Regulation of the SMPS is via pin 14. Winding 2 - 5 has the same polarity as winding 16 - 18, so variations of the +VBATT can be sensed and feed-back to pin 14. The control voltage of winding 2 - 5 during off period of TS7518 the Opto-coupler 7581 conducted rectified by D6540, smoothed by 2540 and coupled via 7581 and

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adjustable by 3572. For the information at pin 14 of IC7520 controls each portion of energy transferred to the secondary side such that the output voltages remain nearly independent of load variations.

9.3.3 I-prim sensing (pin 7 of IC7520)

The current sense voltage at pin 7 is a measure for the I-prim through FET7518. The I-prim is converted into a voltage by R3518. The current sense voltage at pin 7 is used to control both the secondary output voltages and the maximum Iprim (see peak current limiting).

9.3.4 Demagnetization control (via pin 8 of IC7520)

Winding 8 - 9 has the same polarity as the secondary windings that are supplying the load. As a result the voltage across this winding is negative during T-on, positive during T-off and oscillating during T-dead. The so called demagnetization (block " DEMAG " in IC7520) function at pin 8 of IC7520 is used for blocking the output pin 3 during the time that there is still energy in the transformer (1sec not zero). This is realized by delaying the T-on until the demagnetization is completely finished. In this way the currents and voltages at the moment of switching "on" the FET are controlled.

9.3.5 IC7520 control

The error amplifier (block A) compares the feedback voltage at pin 14 with an internal reference voltage of 2V5. The output voltage V error-out of this error amplifier is fed to another comparator (block B). This comparator also compares the V error-out and the current sense voltage at pin 7. When the current sense voltage at pin 7 becomes higher than the output-voltage of the error amplifier V error-out, the comparator B gives a spike (the output of comparator B is the so called current sensing output-voltage Vcs out).

9.3.6 Flip flop

Flip flop (in block C) drives the output pin 3 via a buffer amplifier (block D). The flip flop is set by the positive edge of the output of the oscillator, V osc and reset by the spike V cs out. As a result the pulse at pin 3 becomes "high" --> T-on starts and by the positive edge of Vosc from the internal oscillator and "low" --> T-on stops by the spike of Vcs out . The T-on start will be delayed in case the transformer is not yet demagnetized; see the slow-start procedures.

9.3.7 Stable load and increasing / decreasing load

In case of a stable load, the feedback voltage at pin 14 and so also the maximum current sense voltage at pin 7 remains the same; as a result the T-on and the duty cycle will remain the same. In case of an increasing load, the secondary output voltage will decrease. The voltage on pin 14 would also like to decrease which causes a V error -out to increase. As a result comparator B will give the pulse later; pin 3 will be "high" for a longer period (longer T-on so the duty cycle increase) and so the secondary output voltages will be increased (corrected). This will give a new balance of feedback voltage at pin 14 and the internal 2V5 reference voltage, at a new and larger duty cycle. As a result of the longer T-on, the maximum I-prim increases, so more energy will be stored in the transformer. In this way more energy will be supplied to the load. In case of a decreasing load, the secondary output voltage increases. The voltage on pin 14 would like to increase which causes V error-out to decrease. As a result comparator B will give the pulse earlier, Vpin 3 will be "high" for a shorter period (shorter T-on so the duty cycle decrease) and so the secondary output voltages will be decreased (corrected). This will give a new

balance off feedback voltage Vpin 14 and the internal 2V5 reference voltage, at a new smaller duty cycle. As a result of the shorter T-on, the maximum I-prim decreases, so less energy can be stored in the transformer. In this way less energy will be supplied to the load. In case the demagnetization of the transformer is not yet finished, the positive edge from the oscillator, which will start a new cycle, will be overruled via buffer block D as being the starting point of T-on will be delayed and frequency of the SMPS will go down. This procedure is used during start-up.

9.3.8 Peak current limiting

Peak current limiting is realized by an internal clamp at pin 7. This can never exceeds 1V DC and so the maximum value of I-prim, maximum current through FET 7518, is determined. In case load needs more than the maximum power, by then the I-prim is already at his maximum level so the SMPS will go in overload protection (see also foldback principle explained at overload protection).

9.3.9 Cycle-by-cycle control

The T-on control is regulated on a cycle-by-cycle basis see block C in IC7520. This means that in every cycle the T-on is determined again. By doing so the secondary voltages control, peak current limitation and all protections is very accurate and fast.

9.3.10 Slow-start

As soon as Vpin 1 > 14.5 Volts DC the SMPS will start-up. This will be done by a slow-start procedure where both the frequency and the duty cycle will be built up. The following three phenomena's will take place during start-up:

- The frequency will slowly increase to the nominal frequency of 40 kHz for normal operation and 20 kHz for standby). This is realized via the demagnetization function at pin 8; via this "DEMAG" function, FET 7518 will only conduct and T-on will become "high", when T5545 is totally demagnetized.
- The voltage at pin 5 determines the foldback point. As during start-up this pin 5 is gradually built-up, the foldback point will also gradually increase, see foldback principle explained at overload protection.
- The duty cycle will slowly increase beginning with the absolute lowest possible duty cycle. The maximum duty cycle is determined by C2530 at pin 11 of IC7520, as C2530 is unchanged at start-up.

9.3.11 Standby mode

In standby mode the load will get under a certain threshold level, decrease; see description of standby on the secondary side. The SMPS will determine this threshold level and switch to the so called "reduced frequency mode" at about 20 kHz; this minimal load threshold level is determined by R3532 at pin 12. In the L7.3 chassis the SMPS does not have a burst mode in standby but only a reduced frequency mode of about 20 kHz. In normal operation mode the internal oscillator is about 40 kHz. This frequency is controlled by C2531 at pin 10 of IC7520 and by R3537 at pin 16 of IC7520. In standby mode the internal oscillator is about 20 kHz.; this frequency is controlled by R3536 at pin 15 of IC7520.

9.3.12 FET 7518 gate regulation

D6514 prevents pin 3 of IC7520 of becoming negative because this will destroy the IC due to stray inductance in the gate part.

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The safety resistor R3525 limits the drive current to the gate of FET 7518. In a stable situation V_{pin} 14 is typical 2V4.

9.3.13 Typical values for the L7.3 chassis

Mains Voltage:	
90 - 276V multi voltage	150 - 276V single voltage

Mains frequency: 50 Hz , 60 Hz

Power consumption	in normal mode:
25"	75W +/- 10%
29"	90W +/- 10%

Power Consumption in stand-by mode: 11W

9.4 Protections

9.4.1 Over voltage protection of the secondary voltages

After start-up the supply voltage pin 1 will be taken over by positive winding 8 - 9 and after start up pin 1 is the measuring point for the secondary output voltage. After start-up via an internal switch pin 1 is internally tapped (voltage divider) to a voltage which can be measured at pin 6; pin 6 is also a measuring point for the secondary output voltages. If the voltage at pin 6 > 2.5 Volts, the logic in IC 7520 will shut down the output pin 3. This 2.5 Volts threshold at pin 6 is equivalent to 16 Volts DC which is equivalent to a voltage at the supply voltage (V BATT of approx. 108 Volts DC in normal operation and 130 Volts DC in standby. After switching "off", the IC starts up again because of over voltage protection; see slow-start.

REMARK: In case an overvoltage situation is sensed at the secondary output voltages, the SMPS will go into protection; in case the overvoltage situation remains present the SMPS will start slow, etc.; this is a very good audible hick-up mode.

9.4.2 Under voltage protection of the secondary voltages

If the supply voltage at pin 1 < 9 Volts DC the output pulse at pin 3 will be shut down.

Note: The DC voltage at pin 1 is equivalent to a voltage at V BATT of approx. 70V DC in normal operation and the voltage at pin 1 of 7.5 Volts is equivalent to a voltage at (V BATT of approx. 55V DC in normal operation and 65V DC in standby. In case an over voltage situation is sensed at the secondary output voltages, the SMPS will first switch "off" the pulses and later the complete IC 7520 will be switched "off". In case the IC 7520 is switched "off" also the SMPS will switch "off". In case the under voltage situation remains present, the SMPS will give under voltage protection like slow-start, etc; this is a very good audible hick-up mode.

9.4.3 Unload protection

In case the load goes down e.g. the line goes down because of stand-by mode or some failure in the line circuitry this will be

detected by IC7520 via I-prim and secondary output voltages sensing. In case the load decreases below a certain threshold the SMPS will switch in "reduced frequency mode" of about 20 kHz ; this threshold is determined by the voltage level at pin 12 of IC 7520.

REMARK: In case of an unload situation the set will switch to "low frequency mode" or standby mode. Whether this unload situation of the SMPS is caused by the standby command or by a failure e.g. in the line circuitry this can be determined by switching on the set again which the remote control. In case of standby mode the TV will switch "on" again; in case of unload situation the set will NOT switch "on".

9.4.4 Overload (short-circuit) protection

If the secondary load becomes too high, I-prim also becomes too high; this is sensed by the current sense voltage at pin 7. This voltage at pin 7 is not allowed to exceed 1 Volts DC and it has current limiting. As the I-prim is limited, the secondary output voltages will drop and supply voltage at pin 1 will also drop. As soon as pin 1 < 9Volts DC the driving pulses at pin 3 will stop. As a result of these two mechanisms in case of an overload the secondary voltages will drop very fast, this is called the foldback mechanism. The foldback point can be adjusted by pin 5 of IC7520 and this point is adjusted to a maximum tolerable output power of 85W at 90 Volts AC and 165W at 276Volts AC. After this foldback the IC starts up again (see slow-start); in case the overload situation remains present, the SMPS will give foldback again, slow-start, etc.: as a result in case of short-circuit or overload the TV will be in a good audible hick-up mode.

9.5 Secondary side

9.5.1 Output voltages

See 9.1.2 for output voltages.

9.5.2 Regulator IC TDA 8319 (7541)

This is a low-dropout regulator. This IC has an output voltage of +5 Volts , +8 Volts and RESET signal; the input is 11 Volts.

The Pin configuration of this IC is listed below:

- Pin 1 of the IC is input 1 and the output 1 pin 9 that is +5 Volts DC.
- Pin 2 of the IC is input 2 and the output 1 pin 8 that is +8 Volts DC.
- Pin 3 of the IC is a timing capacitor input and controls the turn-on delay of the reset signal.
- Pin 4 of the IC is a disable pin connected to standby pin of the microcontroller, if the voltage below 0.5 Volts the +8 Volts output will be disabled.
- Pin 5 of the IC is a common ground .
- Pin 6 of the IC is a reset voltage output connected to the microcontroller. This reset output is developed with respect +5 Volts output level of 4.95 Volts.
- Pin 7 of the IC is a program pin for output 2, in this circuit this output pin 8 is programmed to +8.2 Volts by resistors 3552, 3553 and 3554.

Other features of this IC:

- Built-in thermal protection.
- Built-in over current and short circuit protection for +5 Volts and +8Volts.
- Low dropout voltage

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9.6 SINGLE CHIP PROCESSOR IC7225 (=TDA8374)

Introduction :

In this chip most of the video, audio and sync circuits are integrated and in the diagrams the IC is split up into 5 parts: 5A,5B,5C,5D and 5E.

- IC7225-5A, video detector ,see diagram A4.
- IC7225-5B, source select en PAL demodulator ,see diagram A6.
- IC7225-5C, video control, see diagram A4.
- IC7225-5D, horizontal and vertical sync, see diagram A3.
- IC7225-5E, mono sound demodulator, see diagram A4.

9.6.1 IC7225-5A, IF video detector, see diagram A4

Tuning system

For the tuner 1000 there are two possibilities: VST type or PLL type and in both cases the tuner is controlled by the uC. The VST tuner is controlled via V_TUNE, AFC and the BS1, BS2 band switching signals; the PLL tuner is fully I2C controlled. IC 7225-5A contains the IF amplifier and the IF detector, the IF signal is present at the output pin 11 of the tuner. BS1 and BS2 (pin 17-18) switching signals used for band switching of a VST tuner.

	BS1	BS2
VHF1	0	1
VHF2	1	0
UHF	1	1

IF bands pass filter , the IF bands pass characteristic is determined by the band-pass of the SAW filter 1015, listed below you will find the possibilities :

- For PAL BG sets a SAW filter with 5.5 MHz bandwidth is used (33.4 to 38.9 MHz).
- For PAL 1 sets a SAW filter with a bandwidth of 6.0 MHz is used (32.9 to 38.9 MHz).
- For PAL BG/SECAM BG/LL' sets a SAW filter with 6.5 MHz band switch is used to enable BG/I/LL' reception (33.9 to 40.4 MHz).
- For PAL BG/SECAM BG/DK sets a SAW filter with a bandwidth of 6.5 MHz is used (32.4 to 38.9 MHz).

IF-demodulator

IF-demodulation is done with reference circuit L5260 at pin 3 and 4 of IC7225-5A; AGC control of tuner via pin 54 IC7225-5A. Top sync level is used for AGC inside IC7225-5A. AGC adjustment is done by 1(C control via entry into the SAM service menu. C2202 at pin 53 determines time constant of the AGC. Base band CVBS signal at pin 6 of IC7225-5A (normal - 3V2) is fed to the sound trap filters (1206, 1207, 1208, 1209 and 5206) and returns to pin 13 of IC7225-5B for source selection and video processing (diagram A6). The NTSC_SW signal from the control microprocessor switches on transistors 7216 and 7217 to activate filter 1209 when an NTSC signal is received. Demodulation for sound IF takes place for the various signals as follows: For single mono signal, the CVBS_Sound signal goes to filter 1104 for 4.5 MHz and filter 1102 for 5.5MHz. The sound_IF is connected to pin 1 of IC7225-5E for demodulation.

9.6.2 PAL/NTSC Processing (IC7225-5B, see diagram A6)

IC7225-5B contains : source selector and PAL/NTSC colour decoder.

The main functions are :

- Source Selection
The input selector has CVBS_INT< CVBS_EXT1,Y_CVBSEXT2, C_EXT2 as inputs which can be selected via the 12C bus.
- Colour Decoder; the main functions are :
 - PLL/VCO
The PLL operates during the burst key period. It synchronizes the VCXO oscillator with the frequency and phase of the incoming chroma burst. For alternating burst (PAL standards), a loop filter enables the PLL to synchronize with the average burst (180 degrees phase).
 - PAL/NTSC Identification
 - Secam Interface
The 0 and 90 degrees reference signals from the VCO oscillator are supplied via the HUE phase circuit to the (B-Y) and (R-Y) burst demodulators respectively. The demodulated burst from the (B-Y) burst demodulator is supplied both to ACC detector and NTSC indent circuit; the NTSC indent circuit is an integrator. Once the integrator output exceeds the NTSC killer level, an NTSC burst has been identified (IDN=1). Therefore the IDN information provided the ASM (Automatic System Manager) with NTSC colour indent information. The demodulated burst from the burst from the (R-Y) burst demodulator is supplied to the PAL ident circuit via a AL switch; the PAL ident circuit is also an integrator. Once the integrator output exceed the PAL killer level, a PAL burst has been identified (IDP=1). The IDP information provides the ASM with PAL-colour ident information.
The SECAM interface allows bi-directional communication between IC7241 (TDA8395) and the ASM for SECAM identification. If the VCO oscillator is oscillating at 4.43 MHz, the reference frequency of 4.43mhz is superimposed on a DC level and is supplied from pin 33 of IC7225 to pin 1 of IC7241. If PAL/NTSC signals are identified, the reference frequency is continuously available at pin 33 and superimposed on a 1.5V DC level. When SECAM is identified by IC7241, the reference signals which is generated in packets form and superimposed on a 5V DC level is supplied to pin 33 of IC7225. The ident signal (IDS=1) is supplied to the ASM.
 - ASM (Automatic System Manager)
(R-Y)/(B-Y) demodulation
The ASM is able to decode PAL/NTSC colour standards and in combination with IC7241(TDA8395), multi standard applications can be realized. The different possibilities are controlled by the 12C bus input commands which are communicated to the ASM via the 12C bus. The 12C bus input commands also indicates which crystals must be connected to pins 34 and 35. This is essential for correct calibration of the horizontal oscillator.
For PAL/NTSC standards, the (B-Y)/(R-Y) baseband signals are extracted from the chroma signal by the (B-Y)/(R-Y) demodulators, filtered and supplied via the output switch to pins 29 and 30. If SECAM is identified by the SECAM decoder IC7241 and no PAL/NTSC is identified by the ASM, then the ASM will open the switch (B-Y)/(R-Y). This implies (B-Y)/(R-Y) signals from the SECAM decoder will be directly connected to pin 29 and 30 without extra loading.

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9.6.3 Video controller (IC7225-5C, see diagram A4)

The main functions are :

- Y/(B-Y)/(R-Y) signal processing/matrix
- RGB signal selection/processing/output stages
- Black stretcher
- Beam current limiter (BCL,PWL)
- Automatic black current stabilization (ABS) .

Y/(B-Y)/(R-Y) signal processing/matrix

The luminance at pin 27 is internally AC coupled to the luminance clamp, which is operational during burst key period. The luminance signal is supplied to the RGB matrix circuit via the black stretcher. The (B-Y)/(R-Y) signals supplied to pins 31 and 32 are clamped during the burst key period. The signals are then supplied to the colour difference matrix to select the desired matrix formv either PAL, NTSC or Japanese NTSC matrix. The selected matrix is controlled via the 12C bus. For RGB matrix the signal outputs from the colour difference matrix are added with the luminance signal in colour to generate the internal RGB signals.

RGB signal selection/processing/output stages

Within the RGB signal selection there are three modes:

- RGB (internal)
- Fast RGB insertion (R_TXT G_TXT B_TXT on pins 23, 24 and 25)
- OSD-mode

Teletext

Two different types of microprocessors IC7600/7601 are used: one with teletext(TXT) and one without teletext.

The micro-controllers are:

P83Cx66	42 pins	Normal	ROM size varies
SAA529x	52 pins	Control with Teletext	ROM size varies
P83CX69	52 pins	Control with Close caption	ROM size varies

Type of micro-controller	Type of tuning	System	Countries
P83Cx66	VST	PAL/SECAM	AP
	PLL	NTSC	Latam
SAA529x	VST	PAL/SECAM	AP
P83Cx169	PLL	BiNorma/ TriNorma	Latam

The oscillator to be used is 12MHz; a crystal oscillator will be used for all the micro-controllers.

- In case of TXT this function is integrated together with the control part in one and the same uC. This uC is indicated in the diagrams with the outer pin numbering.

- In case of no TXT another (C is used with less pins. This uC is indicated in the diagrams with the internal pin numbering.

In the description below, the pin numbers are the numbers mentioned outside the housing of IC 7600 (IC7600 with TXT and IC7601 without TXT). In case of the (C with integrated teletext function the CVBS-TXT signal is fed to pin 23, the TXT and OSD- information are combined at pins 32,33 and 34.

Before the RGB signal selection the RGB signals are clamped to similar DC level during burst key period. The selection is controlled by the RGB insertion input voltage at pin 26.

Voltage at pin 26	selected RGB signals
< 0.3V	RGB(internal)
0.9V < V < 3V	(R_TXT G_TXT B_TXT (fast insertion at pins 23, 24 and 25)
> 4V	OSD can be inserted at the RGB-out pins

The contrast, brightness and white-point RGB adjustment on the selected RGB signals are controlled by 12C commands. After the adjustment, three RGB black levels are added to the RGB signals. These three signals are blanked when the RGB blank signal is active. The RGB output stages supply the buffered RGB signals pins 21, 20 and 19.

Black Stretcher

The black stretcher circuit, which is only operational during line scan, extends the gray signal level towards the actual black level (= actual black level measured during burstkey). The amount of extension is dependent on the difference between actual black level and the darkest part of the incoming video signal. The darkest part of the videosignal is registered in the capacitor at pin 39 by means of an internal discharge current of approximately 130uA. The black stretcher is made inactive if the voltage at pin 39 is set to ground. This is done by turning on transistor 7220, the command blackstraw coming from the microprocessor (if power button on the remote control unit is activated).

Beam Current Limiter

The beam current limiter circuit functions as an average white limiter as well as peak white limiter. The average white limiter needs external circuitry comprising of 7265, and associated components to function; the peak white limiter is an internal detection circuit. The function of the beam current limiter (average white / peak white limiter) reduces the contrast and brightness of RGB signals. For beam current limiting, the difference in beam current (1BCL) and an internal charge current (1CHARGE) is stored in capacitor 2460 on pin 22. If $1BCL > 1CHARGE$ voltage (VBCL) at pin 22 will decrease. For beam current limiter contrast reduction begins if $VBCL < 3.5V$ and brightness reduction begins if $VBCL < 2.5V$. VBCL is normally about 4V if the beam current limiter is not active .

The contrast and/or brightness reduction of the RGBout is proportional to the voltage decrease at pin 22.

9.6.4 Horizontal synchronization IC 7225-5D and the line output stage (see diagram A3)

Protections:

The line deflection incorporates protections like:

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- X-ray protection. If the EHT will become too high, the tube will radiate too much X-ray, this is detected by monitoring the flyback pulses at pin 11 of the LOT.
- Beam current protection. If the beamcurrent becomes too high the Bimos is able to reduces this by making the EHT_info line low. The circuitry around transistor 7481 in circuit diagram A2, will detect this and create a PROT signal.
- The E/W amplifier has two protections. One protection for overcurrent, for example short circuit of E/W FET and one protection for overvoltage; like interrupted capacitor in lower half of the diode modulator.

Start-up of the horizontal oscillator via the (8V gives a start-up current to pin 37; if the voltage on pin 37 exceeds 5V6 the horizontal oscillator starts to oscillates at approx. 25kHz. Only if the supply pin of IC 7225 (= pin 12 at IC 7225-5A in diagram A4) becomes 8V the line frequency will change to 15625 Hz. The horizontal synchronization separator separates horizontal pulses out of CVBS signals and synchronizes the free-running horizontal sawtooth generator. The horizontal oscillator sawtooth is converted into a square wave voltage with variable duty cycle. This square wave at pin 40 is fed to the line output stage. The time constant of the synchronization circuit is automatically internally determined by IC 7225-5D. Pin 41 is SANDCASTLE output and also HORIZONTAL FLYBACK input.

- The SANDCASTLE has an output current a some mA, the amplitudes of sandcastle pulse is : burst 5V3, line blanking 3V and during frame blanking 2V.
- If the input acts as a HORIZONTAL FLYBACK pulse, the input has a current of 100-300 mA. This horizontal flyback pulse compares the phase of flyback pulse with phase of the horizontal oscillator; if the phase is not correct the duty cycle of horizontal oscillator will be adjusted.
- Flash protection: The BCI info is applied to pin 42 of IC7225-5D. If due to a flash in the picture tube the voltage at pin 42 is > 6V, the horizontal drive will be switched off immediately. If the voltage is again < 6V the horizontal drive will be switched on again via the slow start procedure.
- EHT over voltage protection. The BCI info is also applied to pin 50 of IC7225-5D. First the BCI will compensate vertical picture amplitude variations due to beam current variations. The control range is between 1.2V and 2.8V. However if the voltage at pin 50 exceeds 3.9V the EHT overvoltage protection will be activated and the horizontal drive is switched off.

The line output circuitry

In principle the line output stage pin 40 of IC 7225-5D drives the line output stage consisting of TS 7445 and transformer 5445 via drivers TS 7440-7441. The line output stage supplies the deflection current and the following supply voltages (see also the power supply block diagram) :

Output voltage (See diagram A2) LOT (5445 Line output transformer) output voltage

- EHT, + 200V, Vg2, focus and ff for the picture tube.
- 200V for the CRT drive
- + 5V for the control and tuner supply
- + 9V for the tuner supply
- + 11V for the control and vertical drive output
- - 11V for the vertical drive output

Vertical synchronization IC 7225-5D and the frame amplifier IC 7401

The vertical oscillator (= 50Hz) is controlled by the incoming video signal. The vertical output is driven in anti-phase via the pins 46 and 47. At pin 41 the so called "Sandcastle" pulse is

present; the sandcastle pulse is applied to several parts of the circuits for timing purposes.

Frame amplifier

In principal the frame output stage IC 7401 (TDA9302H) is used for the vertical deflection. This IC is controlled at pins 1 and 3 by the vertical control signal of IC 7225-5D and a deflection current is generated at pin 5. The vertical flyback signal is generated at pin 3 of this IC.

Protection of IC 7401; depending on +11V and -11V supply to drive the vertical deflection CRT. In case of supply voltage +11V or -11V needs a too high current, the VFL pin 3 of IC 7401 voltage will drop < 6.5V. As a result the VFL will feedback this to pin 37 of 7600 micro processor and this will switch the supply to stand-by mode.

9.6.5 Sound detector (IC7225-5E, diagram A4)

Single FM-mono sound for demodulation will take place in IC7225-5E. No adjustment are required because of automatic PLL tuning from 4.2 to 6.8 MHz .

9.7 Information of NVRam, tuners, video and audio I.C.'s.

The following components are also applied in this chassis:

- IC7620 in circuit diagram A5: NVRAM : ST24W04B1 (Non-Volatile Memory). The ST24W04B1 is a 4k bit EEPROM device. The enable pins E1 and E2 selects between maximal four different ST24W04B ICs. Last status of a set is stored in the EEPROM and can be retrieved whenever there is a power interruption
- Tuner 1000 in circuit diagram A4: UV1355 or UV1336 is applied :
The list of tuners is as follows:
For 25" and 29" AP, tuner type UV1335 is used; Type of tuning is VST.
For 25" and 29" Latam, tuner type UV1336 is used; Type of tuning is PLL.
- Audio IC's:
 - IC7310: TDA9860, Audio processor IC
 - IC7315: TDA9850, BTSC DBX decoder with SAP
 - IC7312: AN 7312, AVC= automatic Volume Correction
 - IC7202: MSP3410D, Nicam stereo decoder
 - IC7202: MSP3400C, 2CS Korea and 2CS German decoder
 - IC7313: HEF4052, switch function IC
 - IC7311: TDA7057A , 2X3W stereo sound amplifier

For European and A/P sets we can have 2CS, mono/stereo/dual sound: this is decoded by IC7202(=MSP3400/MSP3410) on Nicam sound panel "C". Also Nicam mono/stereo/dual sound is decoded by IC7202.

For A/P, Latam and USA we use the BTSC decoding ; see IC7315 in circuit diagram A8.

NTSC/SECAM sound system IC (BTSC decoding IC)

General description of BTSC specifications

The BTSC sound system is based on the AM-FM method and is capable of broadcasting:

- MONO information {only MONO Carrier modulated}
- STEREO information {MONO C -> 1/2(L+R) and STEREO SC -> 1/2(L-R)}
- DUAL information {MONO C -> language A and SAP SC -> language B}

9 Circuit description new circuits

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- STEREO & SAP information
- STEREO: {MONO C -> 1/2(L+R) and STEREO SC-> 1/2(L-R)} and SAP:{second audio channel}

The single FM-mono sound decoding will take place in IC7225-E; see circuit diagram A4.

IC7312 (= AN7318S) in diagram A7 is an Automatic Volume Correction (AVC) present. Automatic volume correction (AVC) function provides a constant output level of -18dBFS for input levels between 0dBFS and -24dBFS. In this way it is possible

to adjust different sound sources (eg Terrestrial/SAT channels, SCART) to equal volume level. The increased volume of advertising is compensated as well. The volume correction step width is quasi continuously. The filter maximum is performed with an attack time of 16ms and a programmable decay time i.e 8,4,2 sec and 20ms

IC7311: TDA7057AQ in circuit diagram A7 is a 2X3W stereo sound amplifier.

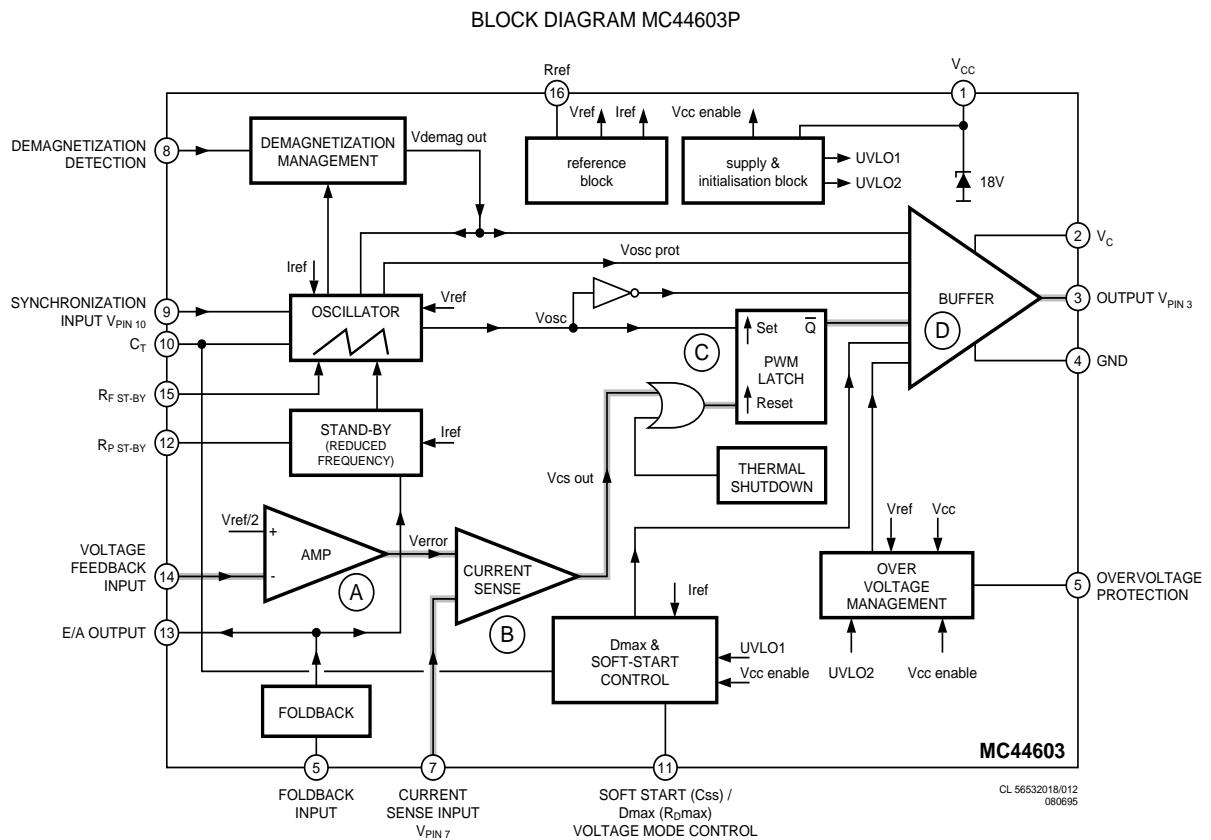
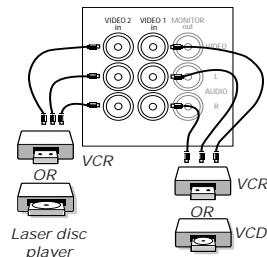


Figure 9-1

Connecting peripheral equipment

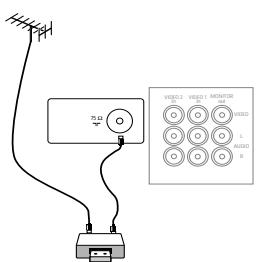
Equipment such as VCR, Laser disc player, VCD etc. could be connected to the video and audio (AV) sockets at the back of the TV. Switch off the TV and equipment before making any connection.

Connection for playback



1. Connect to AV sockets
 - connect the corresponding sockets of the equipment to that of the TV.
 - to view the playback, select a channel named AV1 (if connection is made to sockets at VIDEO 1 in) or AV2 (if connection is made to sockets at VIDEO 2 in).

Note : If you have connected a computer game set to VIDEO 1 in, select a channel named GAME1 to playback. If your connection is made to VIDEO 2 in, then select GAME2 to playback.



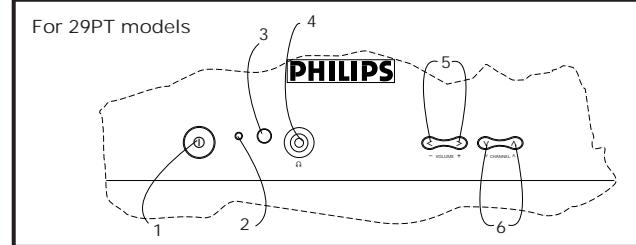
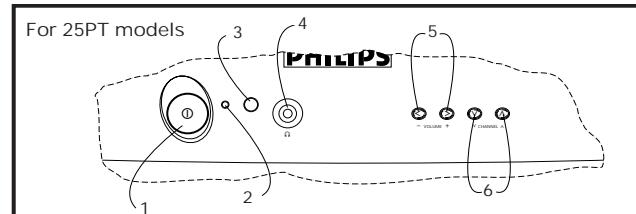
2. Connect to aerial socket (only for VCR)

The playback on your VCR is considered a TV channel by your TV if you connect via the aerial socket. You must tune in to your VCR's test signal and assign the channel number 0 to it. Refer to your VCR's instruction manual for more details.

 - connect the RF cable to the VCR's "RF in" and connect the "RF out" of the VCR to the aerial socket of the TV.
 - select channel 0 and tune in to your VCR's signal.
 - to view the playback, select channel 0.

5

The TV's controls



1. Mains power

Switch on or off the TV.
2. Red light indicator

When light is on, it indicates that the TV is on standby. Note :If no signal is detected by the TV after 10 minutes, it will switch to standby automatically.
3. Remote control sensor

For the remote control to work, it must be activated within the operating range of this sensor.
4. Headphone socket

For connection of headphones.
5. Volume adjustment

To adjust volume level. Press these 2 keys simultaneously will call up the 1st level menu. Press these 2 keys again will exit menu. Works as cursor left (VOLUME —) or right (VOLUME +) in a menu.
6. Channel selection

To select a lower or higher channel number. Works as cursor up (CHANNEL ▲) or down (CHANNEL ▼) in a menu.

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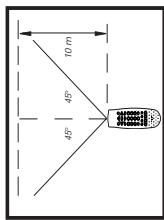
10 Direction for use

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Using the remote control

For the remote control to work effectively, it has to be operated within the recommended operating range to the remote control sensor on the TV.



Press : **Result :** In TV mode : Display channel number and sound mode
In a menu : Exit menu.

Press : **Result :** Standby
Press : **Result :** External source

Press : **Result :** Mute
Press : **Result :** Menu

Press : **Result :** TV mode : Select a higher or lower channel number
In a menu : Scroll up or down.

Press : **Result :** TV mode : Adjust the volume of the TV set.
In a menu : Select or execute.

Press : **Result :** Digit
Press : **Result :** Surf or alternate channel

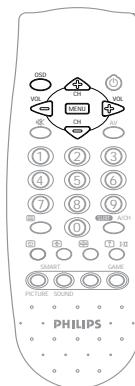
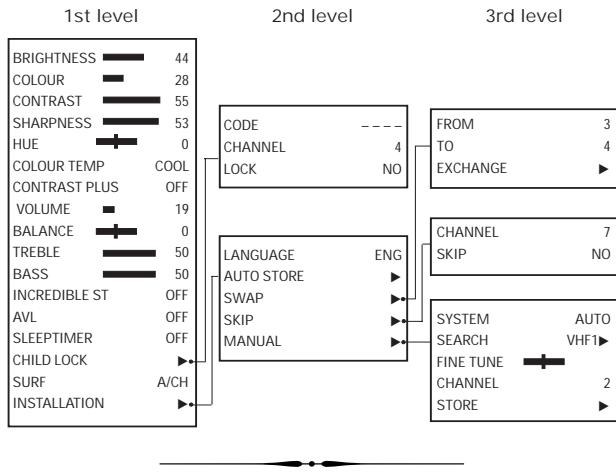
Press : **Result :** Sound mode

Press : **Result :** Game
Press : **Result :** Smart controls

...using the remote control

Press :	Result :
	TV mode : Adjust the volume of the TV set. In a menu : Select or execute.
	Key in numerals e.g. confidential code in child lock and in channel selection. For a 2-digit channel, the second numeral must be entered before the dash disappears.
	Surf mode : Add or delete channel from the surf list. View channel in the surf list. A/CH mode : Return to the previous channel.
	Refer to the section on "Teletext".
	Switch from stereo to mono sound (for stereo transmission) or choose between first language or second language (for bilingual transmission).
	Select preset channels for games.
	Select the presets for sound and picture.

Menus

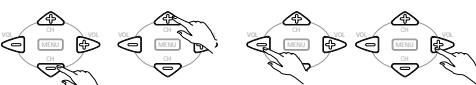


To call up the 1st level menu :

- Press **[MENU]** key.

To use the menus:

- Press the cursor keys.



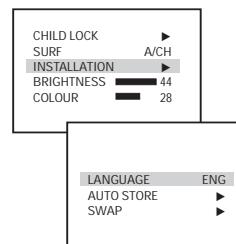
To exit from a menu:

- Press **[MENU]** key to go back to the previous level.
OR
- Press **OSD** key to exit.

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Installing the TV

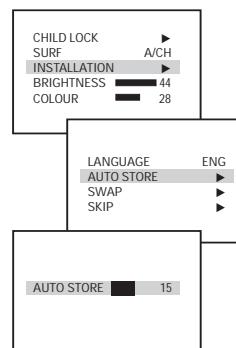
Selecting the menu language



1. Call up 1st level menu with the **[MENU]** key.
2. Scroll up with the **▲** key to highlight **INSTALLATION**.
3. Select **INSTALLATION** with the **◀** or **▶** key.
LANGUAGE is highlighted.
4. Select the language of your choice with the **◀** or **▶** key.
From now on, all menus and screen information will be in the language that you have chosen.
5. Exit with the **OSD** key.

Tuning in the TV channels

There are 2 ways to tune in channels : automatically (by AUTO STORE) or manually (by MANUAL menu).



Auto store

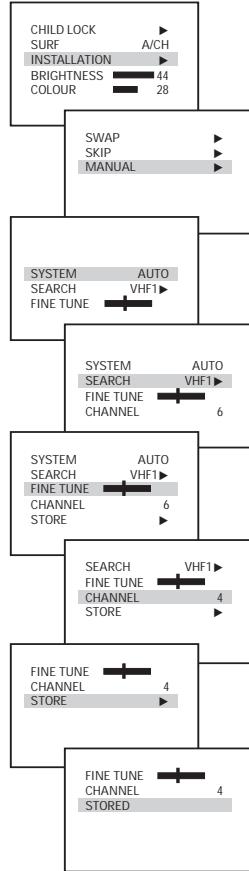
Use to tune in channels automatically.

1. Call up 1st level menu with the **[MENU]** key.
2. Scroll up with the **▲** key to highlight **INSTALLATION**.
3. Select **INSTALLATION** with the **◀** or **▶** key.
4. Scroll down with the **▼** key to highlight **AUTO STORE**.
5. Select **AUTO STORE** with the **◀** or **▶** key.

A tuning bar appears and the TV will automatically store all available channels. Channels will be stored starting from the next channel number .

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...installing the tv



Manual

This menu enables you to search and store every available channel manually.

- Call up 1st level menu with the **MENU** key.
- Scroll up with the **▲** key to highlight **INSTALLATION**.
- Select **INSTALLATION** with the **◀** or **▶** key.
- Scroll down with the **▼** key to highlight **MANUAL**.
- Select **MANUAL** with the **◀** or **▶** key.
- Scroll down with the **▼** key to highlight **SEARCH**.
- Select the band (UHF, VHF1 or VHF3) with the **◀** key.
- Press the **▶** key to start searching. Searching stops once a channel is available. If you decide to store the channel, proceed to the next step. If not, press the **▶** key to start searching again.
- If you wish to fine tune the channel, scroll down to highlight **FINE TUNE**. Press the **◀** or **▶** key to fine tune.
- Scroll down with the **▼** key to highlight **CHANNEL** and assign a channel number to the channel that you found.
- Scroll down with the **▼** key to highlight **STORE** and press the **◀** or **▶** key to store the channel.
- Exit with the **OSD** key.

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...installing the tv

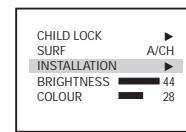
How to deal with poor picture reception

You can use these 3 ways to deal with poor picture reception of your TV

- Skip channels
- System selection (for multi and dual system sets only)
- Fine tuning

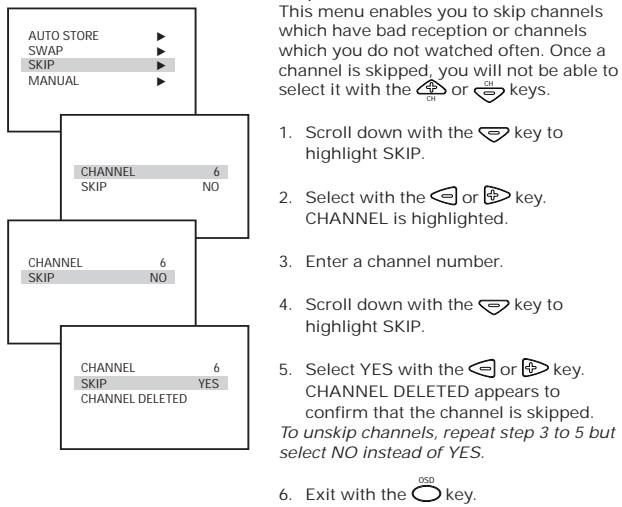
Do the following steps to go into **INSTALLATION** menu.

- Call up 1st level menu with the **MENU** key.
- Scroll up with the **▲** key to highlight **INSTALLATION**.
- Select **INSTALLATION** with the **◀** or **▶** key.



Skip channels

This menu enables you to skip channels which have bad reception or channels which you do not watch often. Once a channel is skipped, you will not be able to select it with the **◀** or **▶** keys.



- Scroll down with the **▼** key to highlight **SKIP**.

- Select with the **◀** or **▶** key. **CHANNEL** is highlighted.

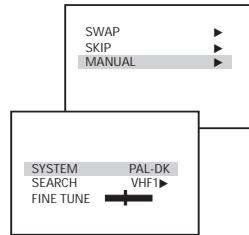
- Enter a channel number.

- Scroll down with the **▼** key to highlight **SKIP**.

- Select **YES** with the **◀** or **▶** key. **CHANNEL DELETED** appears to confirm that the channel is skipped. To unskip channels, repeat step 3 to 5 but select **NO** instead of **YES**.

- Exit with the **OSD** key.

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...installing the tv**System selection**

For multi system sets:

This feature enables you to select either PAL-BG, PAL-I, PAL-DK, SECAM-DK, NTSC M or AUTO. If AUTO is selected, it means that the system is automatically selected according to the current transmission.

For dual system sets:

This feature enables you to select either PAL-DK or PAL-I.

For single system sets:

This feature is not selectable.

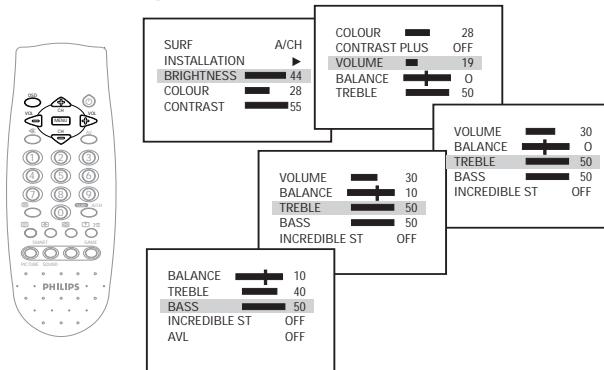
1. Scroll down with the key to highlight MANUAL . SYSTEM is highlighted.
2. Press the or key to select a suitable system.
3. Exit with the key.

Sound settings**Personal settings**

You can do adjustment on VOLUME, BALANCE, TREBLE and BASS of a picture via the 1st level menu. Adjustment on treble and bass will automatically be stored in the PERSONAL mode of the SMART SOUND feature.

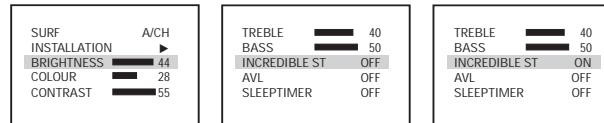
How to do adjustment

1. Call up 1st level menu with the key.
2. Scroll up or down with the or key to highlight the item that you wish to adjust.
3. Select or adjust with the or key.
4. Exit with the key.

**Incredible stereo**

A feature to enhance stereo sound. Adjustment done will automatically be stored in the PERSONAL mode of the SMART SOUND feature.

1. Call up 1st level menu with the key.
2. Scroll down with the key to highlight INCREDIBLE ST.
3. Select ON with the or key.
4. Exit with the key.

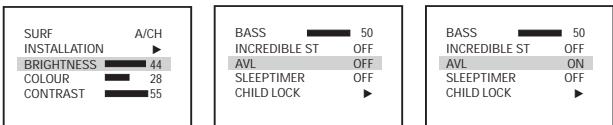


...sound settings

AVL-Auto Volume Leveller

This feature if switched on will automatically adjust any sudden changes in the TV's volume to a preset level.

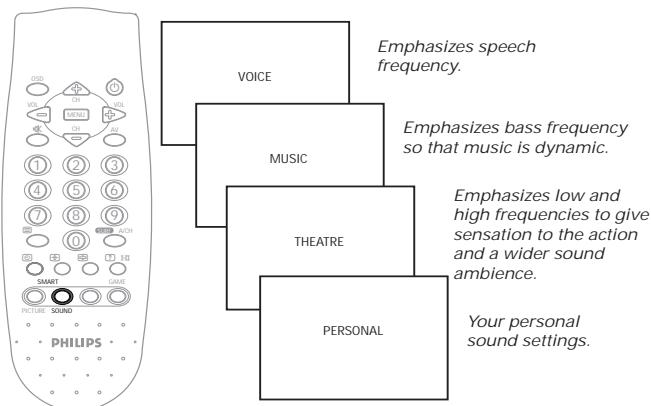
1. Call up 1st level menu with the **[MENU]** key.
2. Scroll down with the **▼** key to highlight AVL.
3. Select ON with the **◀** or **▶** key.
4. Exit with the **OSD** key.



Smart Sound

These are some sound presets on the TV for your viewing pleasure. Press the **SMART SOUND** key repeatedly to select VOICE, MUSIC, THEATRE or PERSONAL*.

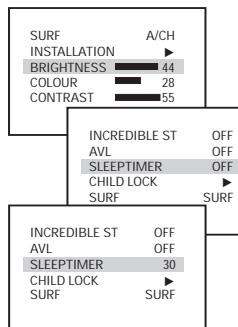
(Note *: "NORMAL" for certain model)



Other features

Sleptimer

Sets timer to switch TV to standby in steps of 15 minutes to a maximum of 120 minutes. To disable timer, set to "OFF".



To set timer

1. Call up 1st level menu with the **[MENU]** key.
2. Scroll down with the **▼** key to highlight SLEPTIMER.
3. Select time period with the **◀** or **▶** key.
4. Exit with the **OSD** key.

Child Lock

This feature enables you to lock channels which you do not wish others e.g. children to watch. You have a choice to lock all channels (inclusive of AV1 or AV2) or individual channel (up to a maximum of 5 channels). Once a channel is locked:

- if you call channels up with the CHANNEL **▼** or **▲** keys on the TV, there will be no picture and sound.
- access to the INSTALLATION sub-menu is disabled

You can only call up channels with your remote control. A message "LOCKED" and "CODE - - -" appears on the screen each time you try to call up a channel with the controls on the TV. To bypass the lock mode, you will need to use your remote control to key in the 4-digit confidential code that you have entered when you locked it.

10 Direction for use

...other features

- To lock channels
- Call up 1st level menu with the **[MENU]** key.
 - Scroll down with the **⇨** key to highlight CHILD LOCK.
 - Press **⇨** or **⇨** key to enter menu.
 - Key in the universal code 8888.
If you do not key in the code, you will not be able to change anything on this menu.
 - Press **⇨** or **⇨** key to select ALL (to lock all channels) or enter a channel number (to lock individual channels).
 - Scroll down with the **⇨** key to highlight LOCK.
 - Press **⇨** or **⇨** key to select YES to lock the selected channel/s.
 - Repeat steps 5 to 7 for other channels which you wish to lock. Once you have completed, proceed to the next step.
 - Scroll up with the **⇨** key to highlight CODE. Key in your own 4-digit confidential code. "CONFIRM CODE" appears.
 - Key in the 4 digits again to confirm and use it whenever you want to access to locked channels.
 - Exit with the **⇨** key.

...other features

- To unlock channels
- Call up 1st level menu with the **[MENU]** key.
 - Scroll down with the **⇨** key to highlight CHILD LOCK.
 - Press **⇨** or **⇨** key to enter menu.
 - Key in the 4-digit confidential code which you have entered when you activate CHILD LOCK mode.
If you key in an incorrect code, all items in the child lock menu can be selected, but it is not possible to change any one of them. If you key in a correct code, CHANNEL will be automatically highlighted.
Tips : If you forget your confidential code, enter the universal code 8888.
 - Key in a channel number which you wish to unlock.
 - Scroll down with the **⇨** key to highlight LOCK.
 - Press **⇨** or **⇨** key to select NO to unlock the selected channel.
 - Repeat steps 5 to 7 for other channels which you wish to unlock. Once you have completed, proceed to the next step.
 - Exit with the **⇨** key.

11 Abbreviations

(R-Y)_OUT	R-Y output from chroma demodulator	RAM	Random Access Memory
uC	Microcomputer	RESET1	Reset signal for the uC
AQUA	Aquadaq layer on the outside of the picture tube	RF_AGC	Automatic gain control signal I for tuner
AV_MUTE	Signal to mute the sound on the Audio-out cinch	RIGHT_OUT	Audio right ou
AVL_AV2/AVL	Switching signal from UP to the Auto Volume leveller on the ITT on BTSC Panel or Rear Cinch I/P 2 width CVBS. R and Sound	ROM	Read Only Memory
B_TXT OSD	Blue TXT or OSD signal from (C to the video controller IC7225-5C	SAM	Service Alignment Mode
BASS	Control signal for BASS	SANDCASTLE	Sand castle signal from IC7225-5D to delay line IC7255 and SECAM chrominance decoder IC7241.
BCI	Beam Current information	SCL	Clock line of the 12C-bus
BG/1_or_BG/0 K	Monochrome TV system sound carrier + 5.5 MHz (BG), Sound carrier + 6 MHz(I), Sound carrier + 6.5 MHz (DK)	SDA	Data line of the 12C-bus
BL_TXT OSD	Fast blanking signal to IC7725-5C to display OSD and TXT	SDM	Service Default Mode; predefined mode for faultfinding
BLACKSTR_SW	Black stretch switch	SECAM_REF	SECAM reference
BS1	TV band selection 1 signal	SID/STA/LL	Sound identification / stereo available / france system "L"
BS2	TV band selection 2 signal	SIF	Sound IF signal for FM demodulator
CHROMA_O/ NTSC_SW	Switch on signal for NTSC chroma oscillator (3.575 MHz)	STANDBY	Switching signal from uC "low" for standby (power supply will be switched to stand-by mode), "high" for normal operation
CHROMA_1/BG/L	Switch on signal for BG/L chroma oscillator (3.582 MHz)	TREBLE	Treble control signal
CHROMA_1/ STATUS	Switch on signal for NTSC chroma oscillator (3.579 MHz)	V_TUNE	Tuning voltage for tuner
CHROMA_2/ STATUS	Signal to select the correct system in case of trinorma	VFB	Vertical flyback pulse
CVBS_EXT1	CVBS external 1 input signal	VFL	50Hz vertical flyback pulse used to inform the uC that flyback takes place. This is important for OSD and TXT
CVBS_EXT2	CVBS external 2 signal	VG2	Voltage on grid 2 of the picture tube
CVBS_IN	CVBS internal 1 (from tuner)	VOLUME	Control signal (from uC, but on DC level via RC nework) for volume control of sound processing in sound panel
CVBS_OUT	CVBS output signal		
CVBS_OUT_1	CVBS output signal 1		
CVBS_SOUND	CVBS for inter carrier sound detector		
CVBS_TXT	CVBS for TXT processing in uC		
DISCHARGE	To have a fast discharge after switching off the set		
EAR	Earth		
EEPROM	Electrical Erasable Programmable Read Only Memory		
ESD	Electrical Static Discharge		
ff	Filament (heater voltage) from LOT to the picture tube		
FL_A	Filament voltage for CRT		
G_TXT OSD	Green TXT or OSD signal from the uC to IC7225-5C		
GND	Ground		
GRD_LOT	Ground of LOT		
HOR.FLYBACK	Horizontal flyback pulse used for looking the horizontal oscillator		
12C	Digital Control bus of the microcomputer		
IF	Intermediate frequency signal for sound processing		
INT/EXT	Switching signal for Internal or external audio + video switching		
L_EXT1	Audio left external 1		
LEFT_OUT	Audio left out		
MOD_L_1N1	SCART I/P 1 in left		
MOD_L_1N2	SCART I/P 2 in left		
MOD_R_1N1	SCART I/P 1 in righ		
MOD_L_1N2	SCART I/P 2 in left		
MONO/STROBE/			
BG_L	Strobe signal for HEF 4094 on multi-mono sound panel		
MONO_OUT	Audio mono out		
NTSC	NTSC colour system		
PAL/SECAM	PAL or SECAM colour system		
R_TXT OSD	Blue TXT or OSD signal from the uC to the video controller IC7225 5C		

12 Spareparts list

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Mainpanel

Various

0136Δ	4822 492 70289	SPRING K12	2349	4822 126 13473	μF 220nF 80-20%	2684	4822 126 13061	μF 220nF 20% 25V	5471	4822 157 11547	1000μH 10% 8X10
0139Δ	4822 276 13603	SWITCH, MAINS	2351	4822 126 13482	μF 470nF 80/20%				5480	5322 157 51687	FXDIND A 04 39μH PM10 (UAW)
0140Δ	4822 256 92053	FUSE HOLDER			16V				5500Δ	4822 157 11523	LINE 5mH /2A
		CLICK (PROM)	2354	4822 126 13473	μF 220nF 80-20%	3171	4822 051 20394	390k 5% 0.1W	5502	4822 157 11538	10mH 2A
0141Δ	4822 265 20723	B2P3-VH	2355	4822 126 14043	μF 1μF 20% 16V	3178	4822 051 20394	390k 5% 0.1W	5515	4822 157 11477	LAL02TB2R2J
0142Δ	4822 265 20723	B2P3-VH	2357	4822 126 13482	μF 470nF 80/20%	3234Δ	4822 052 10228	2Ω 5% 0.33W	5516	4822 157 60171	HF70BTBL3.5X9R
0151	4822 256 10336	LED HOLDER			16V	3249	4822 051 20106	10M 5% 0.1W	5518	4822 526 10704	100mH z
0177	4822 492 70788	FIX IC	2359	4822 126 13482	μF 470nF 80/20%	3277	4822 117 11437	8k2 1% 0.1W	5520	4822 526 10704	100mH z
0189	4822 402 10844	PCB RELIEF			16V	3342	4822 051 20475	4M7 5% 0.1W	5540	4822 157 71249	IND FXD LAL02 A 4U7 PM5 A
		BRACKET	2385	4822 126 14043	μF 1μF 20% 16V	3346Δ	4822 051 20121	120Ω 5% 0.1W	5545Δ	4822 146 10964	TFM SMT 130V
0191	4822 267 31014	HEADPHONE	2387	4822 126 13482	μF 470nF 80/20%	3403	4822 111 31051	3Ω3 5%	5545Δ	4822 157 11529	SMT LAYER WIRE
0191	4822 267 51391	F PHONE 3.5MM			16V	3405Δ	4822 050 21802	1k8 1% 0.6W	5550	4822 157 60171	HF70BTBL3.5X9R
		STEREO	2388	4822 122 32646	μF 5.6nF 10% 50V	3405	4822 050 22202	2k2 1% 0.6W	5551	4822 157 71401	27μH
0210Δ	4822 492 70289	SPRING K12	2401	5322 121 42386	μF 100nF 5% 63V	3407Δ	4822 050 21802	1k8 1% 0.6W	5572	4822 157 60171	HF70BTBL3.5X9R
0211Δ	4822 492 70289	SPRING K12	2402	5322 121 42386	μF 100nF 5% 63V	3407	4822 050 22202	2k2 1% 0.6W	5573	4822 157 60171	HF70BTBL3.5X9R
0231	4822 265 10453	9P	2403	4822 124 40255	μF 100μF 20% 63V	3409Δ	4822 052 10158	12Ω 5% 0.33W	5574	4822 157 11408	2.2μH 10% 8X10
1000Δ	4822 210 10737	UV1355/I	2409	4822 121 42408	μF 220nF 5% 63V	3430Δ	4822 052 10478	4Ω7 5% 0.33W	5603	4822 157 11524	8.2μH 5% 2.3X3.4
1000Δ	4822 210 10771	UV1355/S/I	2409	4822 121 42868	μF 220nF 5% 50V	3443	4822 051 20684	680k 5% 0.1W	5620	4822 157 11525	6.8μH 5% 2.3X3.4
1000Δ	4822 210 10803	UV1336B/AIS	2422	5322 122 32331	μF 1nF 10% 100V	3451Δ	4822 053 11479	47Ω 5% 2W			
1000Δ	4822 210 10804	UV1336B/AFS	2426	5322 121 42386	μF 100nF 5% 63V	3453	4822 117 12822	4Ω7 5% 5W			
1000Δ	4822 210 10814	UV1336B/A/F	2448Δ	4822 122 31173	μF 220pF 10% 500V	3454	4822 053 11333	33k 5% 2W	6040	5322 130 34955	BA482
1007Δ	4822 212 11691	FRAME 29"	2450	4822 124 41739	μF 47uF 20% 160V	3462	4822 117 12514	6k8 5% 5W	6112Δ	4822 130 83757	BAS216
1015	4822 242 10357	OFWK2960M	2451Δ	4822 122 30103	μF 22nF 80% 63V	3465Δ	4822 052 10109	10Ω 5% 0.33W	6114Δ	4822 130 83757	BAS216
1015	4822 242 10874	45.75000 MHz	2451	4822 126 13513	μF 8.2nF 10% 50V	3480	4822 050 22703	27k 1% 0.6W	6263Δ	4822 130 83757	BAS216
1015	4822 242 81556	B39380-K2953-M100	2456	4822 121 43526	μF 47nF 5% 250V	3481Δ	4822 050 24303	43k 1% 0.6W	6264Δ	4822 130 83757	BAS216
1015	4822 242 81964	B39389-G1984-M100	2457Δ	4822 121 10518	μF 250nF 390nF 5%	3482Δ	4822 050 24303	43k 1% 0.6W	6265Δ	4822 130 83757	BAS216
1102Δ	4822 242 10316	SFSF6,5MHz B-TF21	2458	4822 124 41984	μF 10μF 250V	3482	4822 050 25603	56k 1% 0.6W	6401	4822 130 42488	BYD33D
1104	4822 242 10314	SFSH5,5MHz B-TF21	2460	4822 126 12638	μF 6.8nF 10% B 50V	3485Δ	4822 052 10478	4Ω7 5% 0.33W	6402Δ	4822 130 31024	BZXT9-B18
1104	4822 242 10362	SFSH6,0MHz B-TF21	2461	4822 122 31175	μF 1nF 10% 500V	3486Δ	4822 052 11108	1Ω 5% 0.5W	6403	4822 130 42488	BYD33D
1104	4822 242 10363	SFSH4,5MHz B-TF21	2462	4822 121 43526	μF 47nF 5% 250V	3487Δ	4822 052 11108	1Ω 5% 0.5W	6421	4822 130 34258	BZXT9-B56
1206	4822 242 81572	TPS6.0MB-TF21	2463Δ	4822 126 12239	μF 560pF 10% 2KV	3488Δ	4822 052 10478	4Ω7 5% 0.33W	6460Δ	4822 130 41275	BY228
1207	4822 242 81712	TPWAO4B	2464	4822 126 14053	μF 1nF 10% 2KV	3490Δ	4822 052 10338	3Ω3 5% 0.33W	6461Δ	4822 130 41602	BYW95C
1207	4822 242 81978	TPS4.5MB-TF21	2466	4822 121 40483	μF 10nF 10% 400V	3491Δ	4822 052 11108	1Ω 5% 0.5W	6465	4822 130 42606	BYD33J
1208	4822 242 81301	TPS6.5MB-TF21	2467Δ	4822 121 40488	μF 22nF 10% 400V	3492Δ	4822 052 11108	1Ω 5% 0.5W	6466	4822 130 42606	BYD33J
1209	4822 242 81978	TPS4.5MB-TF21	2468Δ	5322 121 42532	μF 18nF 10% 400V	3501	4822 117 12118	47Ω 20% 0.5W	6470	4822 130 30862	BZXT9-B9V1
1275	4822 242 10327	HC49/U 3M575611	2469	4822 121 41511	μF 33nF 10% 400V	3502Δ	4822 053 21225	2M2 5% 0.5W	6480	4822 130 34379	BZXT9-B27
1275	4822 242 10356	4,433619MHz HC49/U	2470	4822 121 51319	μF 1μF 10% 63V	3505	4822 052 60151	SURGE PROTECT	6485	4822 130 42488	BYD33D
1275	4822 242 10695	4 433 619 MHz	2471	4822 121 51252	μF 470nF 5% 63V	3507	4822 116 82776	2Ω	6486	5322 130 31938	BYV27-200
1277	4822 242 10355	3,579545MHz HC49/U	2472	5322 124 41379	μF 2.2μF 20% 50V	3509	4822 117 11488	33k 5% 3W	6487	4822 130 42488	BYD33D
1277	4822 242 10776	3.579 545 MHz	2475	4822 126 14096	μF 560nF 5% 250V	3510	4822 117 11488	33k 5% 3W	6488	4822 130 42488	BYD33D
1278	4822 242 10326	HC49/U 3M582056	2480Δ	4822 121 40518	μF 100nF 10% 250V	3511	4822 117 12568	RESISTOR 7 WATT(MOX)	6489	4822 130 31938	BYV27-200
1300	4822 242 10359	CSB503F58	2481Δ	4822 121 43823	μF 470nF 5% 50V	3513	4822 117 11719	180k	6490	5322 130 31938	BYV27-200
1500	4822 070 35002	218005,(5A)	2484	5322 121 42386	μF 100nF 5% 63V	3514	4822 050 28204	820k 1% 0.6W	6505Δ	4822 130 10741	GBU6J
1572Δ	4822 071 51002	19372(1A)	2485	4822 124 11534	μF 4.7μF 20% 250V	3515Δ	4822 051 20121	12Ω 5% 0.1W	6505	4822 130 11185	GBU4J
1600	4822 276 13775	SWITCH	2486	4822 124 80791	μF 470nF 16% 20V	3516	4822 052 11139	1k5 1% 0.1W	6507	4822 130 42606	BYD33J
1601	4822 276 13775	SWITCH	2487	4822 124 11769	μF 220pF 20% 50V	3517	4822 050 21604	160k 1% 0.6W	6508	4822 130 42606	BYD33J
1602	4822 276 13775	SWITCH	2488	4822 124 80791	μF 470nF 16% 20V	3517	4822 101 11186	47Ω 30% LIN 0.1W	6511	4822 130 41487	BYV95C
1603	4822 276 13775	SWITCH	2489	4822 124 80062	μF 470nF 20% 35V	3601	4822 116 90885	8k2X6	6514Δ	4822 130 31024	BZXT9-B18
1670	4822 218 11573	GP1U28QP	2490	4822 122 31177	μF 470nF 10% 500V	3602	4822 117 12168	2k2 X 6	6515	4822 130 42606	BYD33J
1681	4822 242 10694	12 000 000 MHz	2491	4822 121 40589	μF 470nF 27V	3603	4822 117 12167	8k2 X 12	6517	4822 130 42606	BYD33J
			2500Δ	4822 122 13589	μF 470nF 27V	3605	4822 051 20683	68k 5% 0.1W	6550	4822 130 80209	LTL307P
			2501	4822 121 70141	μF 33nF 5% 400V				6551	4822 130 60508	BCU4J
2002	4822 124 80791	μF 470μF 16V 20%	2502Δ	4822 126 12793	μF 2.2nF 10% 2KV	3606	4822 117 12167	8k2 X 12	6552	4822 130 10754	BY229X-600
2050	4822 126 13486	μF 15pF 2% 63V	2504Δ	4822 126 12793	μF 2.2nF 10% 2KV	3607	4822 117 12167	8k2 X 12	6556	4822 130 10256	EGP20DL-5300
2128	5322 126 10465	μF 3.9nF 10% 50V	2505Δ	4822 126 11524	μF 1.5nF 10% 1KV	3608	4822 117 12167	8k2 X 12	6570	4822 130 10256	EGP20DL-5300
2198	4822 122 33805	μF 330pF 10% 63V	2508Δ	4822 121 10486	μF 330pF 20% 450V	3609	4822 117 12167	8k2 X 12	6590	4822 130 10256	EGP20DL-5300
2199	4822 122 33805	μF 330pF 10% 63V	2508Δ	4822 124 41748	μF 220nF 20% 400V	3610	4822 117 12168	2k2 X 6	6653	4822 130 34382	BZXT9-B8V2
2209	4822 126 13473	μF 220nF 20%-80%	2510Δ	4822 126 11524	μF 1.5nF 10% 1KV	3611	4822 117 12167	8k2 X 12	6663Δ	4822 130 80209	LTL307P
2215	4822 126 13061	μF 220nF 20% 25V	2517	5322 122 32331	μF 1nF 10% 100V	3612	4822 117 12167	8k2 X 12	7001	5322 130 60508	BC857B
2224	4822 124 11576	μF 220μF 20% 25V	2518Δ	4822 122 50116	μF 470pF 10% 1KV	3613	4822 117 12167	8k2 X 12	7002	5322 130 60508	BC857B
2275	4822 126 13486	μF 15pF 2% 63V	2524	4822 122 33805	μF 330pF 10% 63V	3614	4822 117 12167	8k2 X 12	7003	5322 130 60508	BC857B
2278	4822 126 13486	μF 15pF 2% 63V	2530	4822 124 11571	μF 1μF 20% 50V	3615	4822 117 12167	8k2 X 12	7004	5322 130 60508	

7520	4822 209 15684	MC44603AP
7541	4822 209 15829	TDA8139
7542	5322 130 60508	BC857B
7570	4822 209 81397	TL431CLPST
7581Δ	4822 209 71634	TCDT1101G
7600	4822 209 15831	P87C766BDR/01
7600	4822 209 16308	P87C770AAR/40
7600	4822 209 16312	SAA5297PS/051
7600	4822 209 16459	P83C366BDR/016
7603	4822 130 60511	BC847B
7608	4822 130 60511	BC847B
7610Δ	4822 209 73852	PMBT2369
7620	4822 209 15822	ST24W04B6
7650	4822 130 60511	BC847B
7651	4822 130 60511	BC847B
7681	4822 130 60511	BC847B
7682	4822 130 60511	BC847B

CRT panel

-II-

2830	4822 121 51473	μF 470nF 20% 63V
2840	4822 124 11565	μF 10μF 20% 250V
2841	4822 126 13599	μF 3.3nF 10% 500V
2843Δ	4822 126 12278	μF 3300pF 10%) 2KV
2845	4822 122 31175	μF 1nF 10% 500V

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3832	4822 117 12516	680Ω 2%0.5W
3834	4822 117 12516	680Ω 2%0.5W
3836	4822 117 12516	680Ω 2%0.5W
3840Δ	4822 052 10109	10Ω 5% 0.33W
3841Δ	4822 052 10108	1Ω 5% 0.33W
3842Δ	4822 052 10108	1Ω 5% 0.33W
3843	4822 117 11896	1k5 20% 0.5W
3845	4822 117 13016	1M A/50V MAX 115V

Audio panel

-Q-

7830	4822 209 16321	TDA6107Q/N1
1204	4822 242 10769	18.432 000 MHz

-II-

2214	4822 126 13486	μF 15pF 2% 63V
2216	4822 126 13061	μF 220nF 20% 25V
2217	4822 126 13061	μF 220nF 20% 25V
2220	4822 126 13061	μF 220nF 20% 25V
2221	4822 126 13061	μF 220nF 20% 25V
2225	5322 122 32286	μF 3.3pF 5% 50V
2226	5322 122 32286	μF 3.3pF 5% 50V
2239Δ	4822 122 33172	μF 390pF 5% 50V
2240Δ	4822 122 33172	μF 390pF 5% 50V

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3243	4822 117 11437	8k2 1% 0.1W
3244	5322 117 12487	1k RC12G 1% 0.125W
3247Δ	4822 052 10109	10Ω 5% 0.33W

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5206	4822 157 11515	12μH 5% 2.3X3.4
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-Q-

7202	4822 209 15832	MSP3410D-PP-B4
7202	4822 209 16307	MSP3400C-C8
7209	4822 130 60511	BC847B
7214Δ	4822 130 40981	BC337-25
7215	4822 130 60511	BC847B
7216	4822 130 60511	BC847B