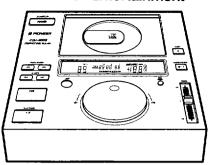
Service Manual



The Art of Entertainment



ORDER NO. **RRV 1087**

The chapter 1 of this Service Manual will not be reprinted. On your additional orders, we may supply only the chapter 2. For chapter 1, please make copies and attach to the chapter 2 at your side if necessary.

COMPACT DISC PLAYER

CDJ-500

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Type	Mo	del	Power Requirement	The voltage can be converted	
CDJ-500G CDJ-500		Power nequirement	by the following method.		
KUC	0	_	AC 120V	_	
SELM		0	AC 110V/ 120V/ 220-230V/ 240V	With the voltage selector	

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CHAPTER 1

1.1 SAFETY INFORMATION

This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

WARNING

Lead in solder used in this product is listed by the California Health and Welfare agency as a known reproductive toxicant which may cause birth defects or other reproductive harm (California Health & Safety Code, Section 25249.5).

When servicing or handling circuit boards and other components which contain lead in solder, avoid unprotected skin contact with the solder. Also, when soldering do not inhale any smoke or fumes produced.

NOTICE

(FOR CANADIAN MODEL ONLY)

Fuse symbols - (fast operating fuse) and/or - (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

REMARQUE

(POUR MODÈLE CANADIEN SEULEMENT)

Les symboles de fusible (fusible de type rapide) et/ou —————— (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

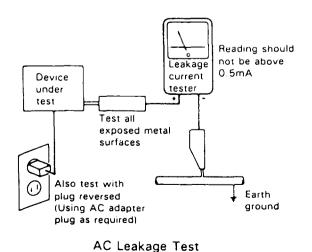
r(FOR USA MODEL ONLY)-

1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5mA.



ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a Δ on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which dose not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

(FOR EUROPEAN MODEL ONLY)

AVATTAESSA JA SUOJALUKITUS OHITETTAESSA OLET ALTTIINA NÄKYMÄTTÖMÄLLE LASERSÄTEILYLLE. ÄLÄ KATSO SÄTEESEEN.

ADVERSEL:

USYNLIG LASERSTRÅLING VED ÅBNING NÅR SIKKERHEDSAFBRYDERE ER UDE AF FUNKTION UNDGA UDSAETTELSE FOR STRÅLING.

· VARNINGI -

OSYNLIG LASERSTRÅLNING NÄR DENNA DEL ÄR ÖPPNAD OCH SPÄRREN ÄR URKOPPLAD, BETRAKTA EJ STRÅLEN.

CDJ-500 / SELM type



Kuva 1 Lasersateilyn varoitusmerkki

WARNING! -

DEVICE INCLUDES LASER DIODE WHICH EMITS INVISIBLE INFRARED RADIATION WHICH IS DANGEROUS TO EYES. THERE IS A WARNING SIGN ACCORDING TO PICTURE 1 INSIDE THE DEVICE CLOSE TO THE LASER DIODE.



Picture 1 Warning sign for laser radiation

- IMPORTANT -

THIS PIONEER APPARATUS CONTAINS LASER OF CLASS 1. SERVICING OPERATION OF THE APPARATUS SHOULD BE DONE BY A SPECIALLY INSTRUCTED PERSON.

- LASER DIODE CHARACTERISTICS -MAXIMUM OUTPUT POWER: 5 mw WAVELENGTH: 780-785 nm

CDJ-500 / SELM type CDJ-500 / SELM type CDJ-500 / SELM type THIS DEVICE COMPLIES WITH PART 15 OF THE FCC RULES. VARO OPERATION IS SUBJECT TO THE FOLLOWING TWO COMDITIONS: (1) THIS DEVICE MAY NOT CAUSE HARMFUL INTERFERENCE AND (2) THIS DEVICE MUST ACCEPT ANY INTERFERENCE RECEIVED. Avattaessa ja suojalukitus ohitetta-essa olet aittiina näkymattomaile lasersateilylle. Ala katso sateeseen. CLASS 1 LASER PRODUCT VARNING! INCLUDING INTERFERENCE THAT MAY CAUSE UNDESIRED Osynlig lasersträlning när denna del är öppnad och spärren är urkopplad. Betrakta ej strälen. pawnin CERTIFICATION-THIS PRODUCT COMPLIES WITH DHHS RULES 21 CFR. SUBCHAPTER J. PART 1040 AT DATE OF MANUFACTURE CDJ-500 / SELM type ADVARSEL Usynlig lasersträling ved åbning når sikkerhed saf Brydere er ude af funktion. Undgå udsættelse for stråling. VORSICHT! UNSICHTBARE LASER-STRANLUNG TRITT AUS, WENN DECKEL (ODER KLAPPE) GEÖFFNET ISTI NICHT DEM STRANL AUSSETZEN Additional Laser Caution -1.Laser Interlock Mechanism ately to ON(Low level). The interlock also does not function in the test mode *.

*Refer to page 1-22.

ON/OFF switching of the switch for detecting a loaded state, and ON/OFF switching of the clamp completion, close completion and shutter switches are detected by the system microprocessor. The design of this interlock mechanism prevents laser diode oscillation when even one of the clamp completion, close completion and shutter switches is OFF(High level).

Accordingly, the interlock will no longer function and the laser diode will oscillate if all three of these switches are set deliber-

Laser diode oscillation will continue, if pin 1 of M51593FP

(IC101) on the PRE AMP BOARD ASSY mounted on the pickup assembly is connected to GND, or pin 19 is connected to low level (ON), or else the terminals of Q101 are shorted to each other (fault condition).

2. When the cover is opened, close viewing of the objective lens with the naked eye will cause exposure to a Class 1 laser beam.

1.2 SPECIFICATIONS

1. General

SystemCompact disc digital audio system
Power requirements AC 120V, 60Hz (KUC only)
Power requirements AC 110/120/220-230/240 V
(switchable) 50/60 Hz (SELM only)
Power consumption 23 W (KUC only)
Power consumption21 W (SELM only)
Operating temperature+5°C - +35°C
(+41°F - +95°F)
Operating humidity 5% - 85%
(There should be no condensation of moisture.)
Weight4.1 kg (9 lbs 1 oz) (KUC only)
Weight4 kg (8 lbs 13 oz) (SELM only)
Dimensions320(W) X 362.1(D) X 98.5(H) mm
12-5/8(W) X 14-1/4(D) X 3-7/8(H) in

2. Audio section

Frequency response	4 Hz - 20 kHz (EIAJ)
Signal-to-noise ratio	106 dB or more (EIAJ)
Dynamic range	96 dB or more (EIAJ)
Channel separation	98 dB or more (EIAJ)
Total harmonic distortion	0.004% or less (EIAJ)
Output level	2.0 V
Channels	2-channel (stereo)

3. Video section

Video output level	1 Vp-p nominal, sync.
	negative (KUC only)
Video output impedance	75 Ω , unbalanced
	(KUC only)

4. Other terminals

CONTROL	miniature jack
VHF ADAPTER OUTPUT	
(VIDEO/AUDIO)	both RCA jacks with DC jack
	(KUC only)

5. Functions

- Disc compartment with top loading type
- Pick-up protector
- Sliding type speed control (TEMPO control knob)
- Starting playback immediately
- Loop playback
- Master tempo control
- Relay playback (using the CONTROL jack)
- Auto cue (ON/OFF)
- Cue point monitor
- Back cue
- Beat search with the Jog dial
- Frame search with the Jog dial
- Manual search with the Jog dial
- High-speed manual search
- Cyclical track search
- Switching of the TIME display (TIME/REMAIN)

6. Accessories

•	Operating instructions1
•	Output cable1
•	Screws for rack mounting4
•	Spacers for rack mounting4

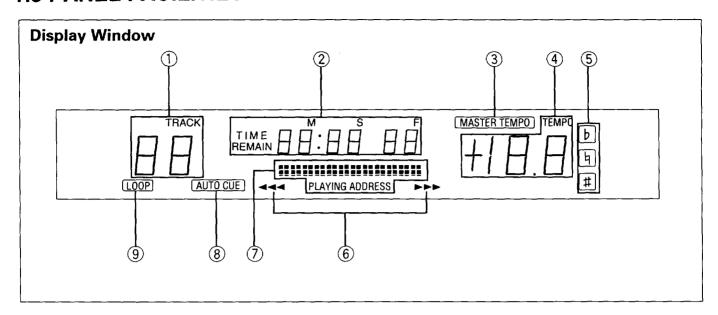
NOTE:

Specifications and design subject to possible modification without notice, due to improvements.

Maintenance:

In order to ensure the safe and correct function of this unit, we recommend regular maintenance. Extended service life can be expected if maintained properly.

1.3 PANEL FACILITIES



1 TRACK number display

The track number being played is displayed.

2 Time display (TIME/REMAIN)

The elapsed playback time of the track being played (TIME) or remaining playback time of the track being played (REMAIN) is displayed in minutes (M), seconds (S), or frames (F).

3 MASTER TEMPO indicator

This indicator lights up when the MASTER TEMPO function is carried out.

4 Playback tempo (TEMPO) display

The playback tempo being adjusted with the TEMPO control knob is displayed (up to $\pm 10\%$ in steps of 0.1%).

⑤ Pitch indicator (b, 4, #)

- b: The pitch of playback is decreased.
- ነ: Normal pitch of playback
- #: The pitch of playback is increased.

⑤ Jog dial operation indicator (◄◄◄ / ►►►)

The indicator lights according to the rotating direction or speed of the Jog dial.

Playback address display (PLAYING ADDRESS)

The elapsed playback time or remaining playback time of the track being played is roughly indicated with the full-scaled bar graph.

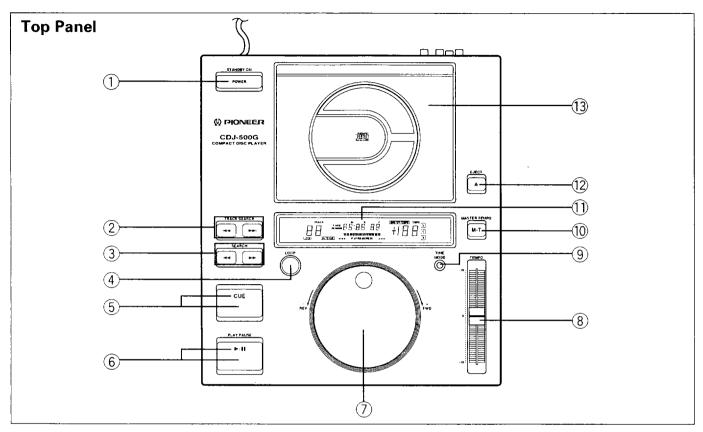
- When no disc is in the disc compartment.....goes off
- When displaying the elapsed playback timelights up from the left side
- When displaying the remaining playback timegoes off from the left side
- When the remaining playback time is less than 15 secondsblinks
- When the remaining playback time is less than 5 seconds......blinks rapidly

(8) AUTO CUE indicator

This indicator lights up when the AUTO CUE function is carried out.

(9) LOOP indicator

This indicator lights during loop playback.



1) POWER STANDBY/ON switch

This is the switch for electric power.

ON: When set to the ON position, the power is supplied and the player becomes operational.

STANDBY: When set to the STANDBY position, the main power flow is cut and the player is no longer fully operational. A minute flow of the power feeds the player to maintain operation readiness.

② TRACK SEARCH buttons (I◄◄, ▶►I)

Each time the button is pressed, playback advances or reverses to the beginning of the track.

③ SEARCH buttons (◄◄, ▶►)

While the button is kept pressed, playback rapidly advances or reverses toward the end or beginning of the disc.

4 LOOP button

After the cue point is memorized, pressing this button in playback mode repeats the section between the cue point and the point where the LOOP button was pressed without intervals of sound.

(5) CUE button/indicator

Three functions are available.

[Cue Point Memory]

When this button is pressed in pause mode, the player memorizes that point. You can start immediately playback from that point (Playback standby mode). The player enters playback standby mode.

When the new cue point is memorized, the last stored cue point is cleared.

[Back Cue]

When this button is pressed in playback mode, the sound is muted and playback is paused at the cue point.

[Cue Point Monitor]

When this button is pressed after the cue point is memorized, the sound can be heard for about 0.7 seconds maximum from the cue point.

When the CUE point is memorized, the CUE indicator lights up. When the player enters pause mode to write the new cue point, the CUE indicator blinks.

(6) PLAY/PAUSE button/indicator (▶/II)

Each time this button is pressed, the player switches between playback mode and pause mode. The ►/II indicator lights during playback, and blinks in pause mode.

7 Jog dial (+ FWD/- REV)

Three functions are available.

[Beat Search operation]

When the Jog dial is slowly rotated in playback mode, the playback tempo changes according to the rotating speed of the Jog dial. When rotating the Jog dial clockwise, the playback tempo becomes faster. When rotating the Jog dial counterclockwise the playback tempo becomes slower.

[Multi-Speed Search operation]

When the Jog dial is rapidly rotated in playback mode, playback advances or reverses while the sound is output. Rotate the Jog dial clockwise for fast-forward operation. Rotate the Jog dial counterclockwise for fast-reverse operation. The searching speed changes according to the rotating speed of the Jog dial.

[Frame Search operation]

Press PLAY/PAUSE during playback, and rotate the Jog dial in pause mode. Frame search can be carried out by 1 frame (1 second is equal to 75 frames).

8 TEMPO control knob

The playback tempo can be changed with this knob. The center clicked position is for normal playback tempo. If the knob is moved toward you (+ side), the music tempo will become fast. If the knob is moved away from you (– side), the music tempo will become slow.

(9) TIME MODE button

Two functions are available.

[Switching the time display]

Each time this button is pressed, the time display changes between the elapsed playback time of the track (TIME) and the remaining playback time of the track (REMAIN).

[Switching ON/OFF of the Auto Cue and Auto Pause function]

When this button is kept pressed for more than 2 seconds, the Auto Cue function (*1) is set to ON or OFF. When the Auto Cue function is set to ON, the AUTO CUE indicator in the display window lights up. The Auto Pause function (*2) is also set to ON or OFF with this button.

- (*1) Auto Cue function: When a disc is loaded or track search is carried out, the player memorizes the cue point at which the sound is output.
- (*2) Auto Pause function: When a track being played is finished, the player automatically enters pause mode. Playback is paused just before the cue point.
- The setting of the time display or the ON/OFF setting of the Auto Cue function will be stored in the player even if the power is turned off.

(10) MASTER TEMPO button (M • T)

Each time this button is pressed, the MASTER TEMPO function (*3) is set to ON or OFF. When the MASTER TEMPO function is set to ON, the MASTER TEMPO indicator in the display window lights.

- (*3) MASTER TEMPO function: Even though the playback tempo is changed with the TEMPO control knob, the playback pitch is kept fixed.
- 11 Display window

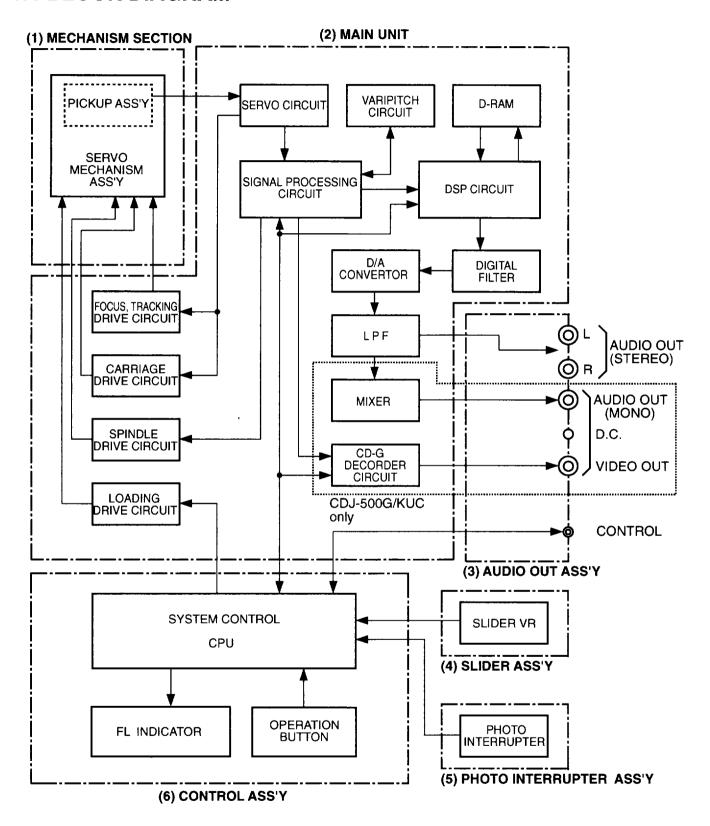
① EJECT button (▲)

Press this button to open the disc compartment door.

(13) Disc compartment door

Press the EJECT button to open the door. To close the door, push the door in.

1.4 BLOCK DIAGRAM



Explanation of Block Diagram

(1) Mechanisms section (servo mechanism ass'y,loading mechanism unit)

The unit comprises a pickup, servo mechanism ass'y, loading mechanism unit, open switch and clamp switch.

(2) Main unit

The main unit comprises a servo circuit, driver circuits, varipitch circuit, signal processing circuit, DSP circuit and audio circuit.

The pickup outputs RF signals, focus error signals, and tracking error signals. The servo circuit controls the focus servo, tracking servo and carriage servo using these signals.

The signals needed for the spindle servo are generated from the EFM to be sent to the signal processor from the servo circuit.

There are five driver circuits, for focusing, tracking, carriage operation, spindle operation and loading.

The varipitch circuit comprises an external crystal oscillation circuit and a PLL circuit, and sends data to the DSP synchronized with the clock in accordance with instructions from the microcomputer. Data processed at the DSP is sent to the digital filter and DAC. The data is converted by a D/A converter, and output as audio signal after passing through a low-pass filter.

* Sub-code signals output from the signal processing circuit are input to the CD-G decoder circuit. After error correction, these signals are image-processed, and output as NTSC video signals. (CDJ-500G/KUC type only)

(3) Audio out ass'y

Unbalanced analog audio signals and video signals (in the NTSC mode) are output from the RCA pin jack that has a control terminal.

* This assembly has terminals for RF adapter-compatible audio output and DC output (CDJ-500G/ KUC type only).

(4) Slider ass'y

This has sliding variable resistors having a stroke of 100 mm. A voltage of 0 to +5V is output depending on the position of the slider knob.

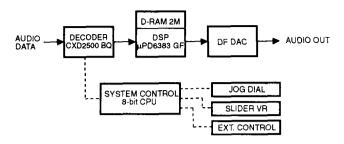
(5) Photo interrupter ass'y

This ass'y outputs a pulse proportional to rotation from the photo-interrupter that is interlocked with the jog controller.

(6) Control ass'y

This ass'y comprises control buttons, a display and a system control microcomputer. The voltage output from the slider ass'y is input to the A/D port of the microcomputer, and converted to 8-bit digital signals. These instructions are sent to a decoder, and are used for controlling speed. The microcomputer also controls the display circuit, decoder and DSP.

1.5 CIRCUIT DESCRIPTION



The above circuit achieves the following functions.

(1) Starting playback immediately

As jumping of signal tracks is repeated when the CD is paused, a time lag of about 0.3 seconds at the most occurs when rewinding (when sound begins to be output after shift from pause to play). This time lag must be eliminated to make an instant start in order to join songs together by a cut-in. This instant start (about 0.03 seconds) is achieved by storing about the first 0.7 seconds of the song to memory, and playing the data in memory when pause is canceled. From 0.7 seconds onwards, the song data is continuously added to the end of this memory and is used as ring memory in order to continuously play the song. Memory management is executed by a high-performance DSP under the control of an 8-bit microcomputer.

(2) Beat search by jog controller

When joining songs together, it is extremely difficult to match the rhythm (speed and timing) with the BPM of two songs by adjusting the tempo using the slider. A jog controller is used to achieve the operation of manually accelerating or decelerating the disc slightly on an analog turntable.

The pulse generated by the pulse encoder according to the rotation of the jog controller is computed by a microcomputer to control the varipitch function of the decoder LSI.

Turning the dial clockwise accelerates the playback speed up to +12.7%. Turning the dial counterclockwise decelerates the playback speed up to -12.7%. Stopping the dial restores the playback speed to its original set speed.

(3) Master tempo

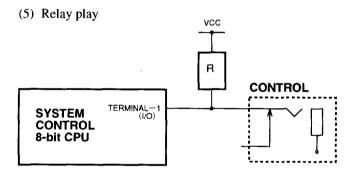
The slider voltage is converted to digital values by an A/D converter. A microcomputer controls the varipitch function of the decoder LSI to change the playback speed to $\pm 10\%$ in 0.1% steps.

The playback pitch (interval) simultaneous with changes during variable speed, which results in vocals sounding unnatural. So, key control is processed by the DSP to eliminate interval changes from the original pitch.

(4) Loop playback

"Break beats" are generated by the DJ (Disk jockey) by repeatedly playing the same phrase on two turntables. The same effect as this can be achieved on a single CD player. This is called "loop playback."

First, the initial 0.7 seconds or so of the song data to be loop-played is stored to memory. The search time can be aurally eliminated by outputting the data stored to memory during searching from the end address to the start address during loop playback, which allows repeat playing without any break in sound.



Relay play is achieved by alternately connecting the player control terminals of two players to which auto cue is set.

Though terminal-1 is normally in an input standby state, the play start command (Low pulse) is output when the song being played ends, and terminal-1 stands by for instant start at the beginning of the next song.

Instant start is executed when the play start command is received during instant start standby.

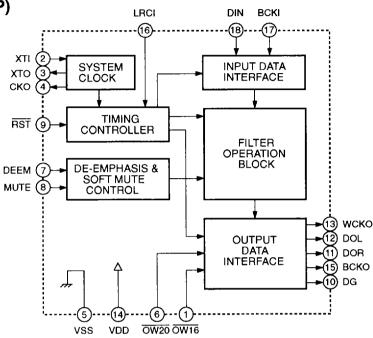
Note: The relay play does not operate correctly unless the audio input jacks of two players are connected to the audio input jack of one audio mixer or one amplifier.

1.6 IC INFORMATION

Note: The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

■ IC401 (SM5841CP)

Block Diagram



Pin Function

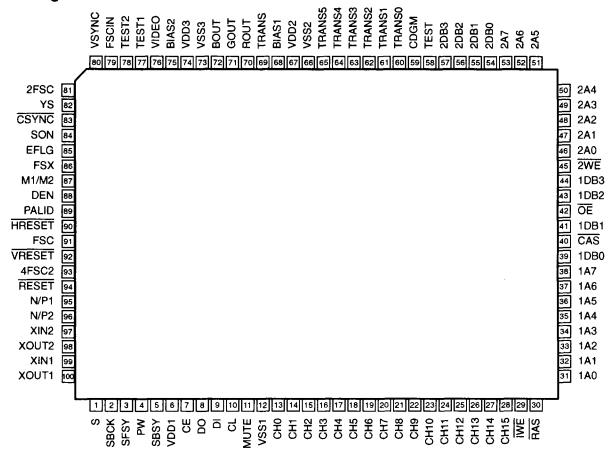
Pin No.	Pin Name	I/O		F	nction		
1	OW16	I	Select terminal 1	Setting		OW.	20
			of output bits	Setting		Н	L
			NO.	OW16		output (NS-ON)	
1						output (NS-ON)	18-bit output (NS-OFF)
						shaper ON	
				NS-	FF,noise	shaper OFF (tes	t mode)
2	XTI	I	Oscillator				
3	XTO	О	Oscillator				
4	CKO	0	Oscillator clock (fi	requency san	as XTI)		
5	VSS	-	GND	GND			
6	OW20	I		Select terminal 2 of output bits NO. (20 bits at OW20=Low)			
			(18 or 16 bits at OW20=High) Note: See OW16 table above.				
7	DEEM	I	De-emphasis signal input (de-emphasis OFF when DEEM=Low)				
			(de-emphasis ON when DEEM=High)				
8	MUTE	Ī	Mute signal input (soft mute OFF when MUTE=Low)				
			(soft mute ON when MUTE=High)				
9	RST	I	System reset (initialize) terminal				
10	DG	0		Deglitch output			
11	DOR	0		Rch data output			
12	DOL	0		Lch data output			
13	WCKO	0		Output word clock terminal			
14	VDD	-	Power (5V) terminal				
15	BCKO	0	Output bit clock te				
16	LRCI	I		Sample rate (fs) clock terminal of input data			
17	BCKI	I	Input bit clock terminal				
18	DIN	I	Input data terminal				

Note:

^{•&}quot;fs" in the description refers to the sample frequency of the input data.

■ IC501 (LC7870NE) (CDJ-500G/ KUC only)

Pin Assignment



Pin Function

Pin No.	Pin Name	I/O	Function
1	S	I	DSP select for CD High: LC7861N/67, Middle: LC7860K/63, Low: LC7868/69
2	SBCK	0	Sub-code R/W read clock
3	SFSY	I	Sub-code frame synchronizing signal
4	PW	I	Sub-code R/W data
5	SBSY	I	Sub-code block synchronizing signal
6	VDD1		Digital system power terminal
7	CE	I	Control pin at serial input or output
8	DO	0	Serial data output
9	DI	I	Serial data input
10	CL	I	Clock at serial data I/O
11	MUTE	I	Control signal when disabling sub-code data
12	VSS1	-	Digital system GND
13	CH0	I	
14	CH1	11	
15	CH2	I	
16	СНЗ	I	Enabled at channel select terminal High
17	CH4	I	
18	CH5	I	
19	СН6	I	

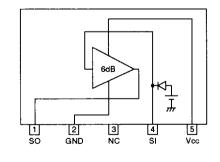
Pin No.	Pin Name	I/O	Function
20	CH7	I	
21	CH8	I	
22	CH9	ı	
23	CH10	I	
24	CH11	Ī	Enabled at channel select High
25	CH12	Ī	
26	CH13	I	
27	CH14	Ī	
28	CH15	ı	
29	1WE	0	No.1 DRAM control terminal
30	RAS	0	No.1, No.2 DRAM common control terminal
31	1A0	0	
32	1A1	0	
33	1A2	0	
34	1A3	Ö	No.1 DRAM address terminal
35	1A4	0	A TOTA OF THE STATE OF THE STAT
36	1A5	0	
37	1A6	0	
38	1A7	0	
39	1DB0	I/O	No.1 DRAM data terminal
40	CAS	0	No.1, No.2 DRAM common control terminal
41	1DB1	I/O	No.1 DRAM data terminal
42	ŌĒ	0	No.1, No.2 DRAM common control terminal
43	1DB2	I/O	No.1 DRAM data terminal
44	1DB2	I/O	No.1 DRAW data termina
45	ZWE	0	No.2 DRAM control terminal
46	2A0	0	140.2 DRAW Control Communication
47	2A1	0	
48	2A1 2A2	0	
49	2A2 2A3	0	No.2 DRAM address
50	2A3 2A4	0	NO.2 DRAW address
		<u> </u>	
51	2A5	0	
52	2A6	0	
53	2A7	0	
54	2DB0	I/O	No 2 DD AM data tamminal
55	2DB1	I/O	No.2 DRAM data terminal
56	2DB2	I/O	
57	2DB3	I/O	Today
58	TEST	I	Test pin
59	CDGM	0	High output at CDG disc
60	TRANS0	0	
61	TRANS1	0	To a second district and second
62	TRANS2	0	Transparency digital output
63	TRANS3	0	
64	TRANS4	0	
65	TRANS5	0	COMP.
66	VSS2	-	Analog system GND
67	VDD2	-	Analog system power
68	BIAS1	0	Capacitor connection terminal for ripple elimination
69	TRANS	0	6-bits DAC output for define-transparency

Pin No.	Pin Name	I/O	Function
70	ROUT	0	R datas 6-bits DAC output
71	GOUT	0	G datas 6-bits DAC output
72	BOUT	0	B datsa 6-bits DAC output
73	VSS3	_	Analog system GND terminal
74	VDD3	-	Analog system power terminal
75	BIAS2	0	Capacitor connection terminal for ripple elimination
76	VIDEO	0	Composite video output terminal (8-bits DAC output)
77	TEST1	I	Test pin
78	TEST2	I	
79	FSCIN	I	Sub-carrier clock input terminal
80	VSYNC	0	Vertical synchronizing signal output terminal
81	2FSC	0	2FSC output terminal
82	YS	0	Superimpose control output
83	CSYNC	О	Composite synchronizing signal output
84	SON	I	Superimpose ON/OFF terminal
85	EFLG	0	Error status monitor terminal
86	FSX	0	Error status monitor trigger terminal
87	M1/M2	I	256 DRAM 1 or 2 select terminal High: 1, Low: 2
88	DEN	I	DISC information display enable terminal High: BGC, Low: Enable
89	PALID	I	External control terminal at superimpose PAL
90	HRESET	I	Horizontal timing external control terminal
91	FSC	0	Sub-carrier clock output. NTSC mode: 3.579545 MHz, PAL mode: 4.433619 MHz
92	VRESET	I	Vertical timing external control terminal
93	4FSC2	I	External clock input terminal at superimpose
94	RESET	I	Reset input terminal
95	N/P1	I	NTSC/PAL select terminal (RGB encoder) High: NTSC, Low: PAL
96	N/P2	I	NTSC/PAL select terminal (CDG decoder) High: NTSC, Low: PAL
97	XIN2	I	Crystal oscillator connection terminal 17.734476 MHz (for PAL)
98	XOUT2	0	
99	XIN1	I	Crystal oscillator connection terminal 14.31818 MHz (for NTSC)
100	XOUTI	0	

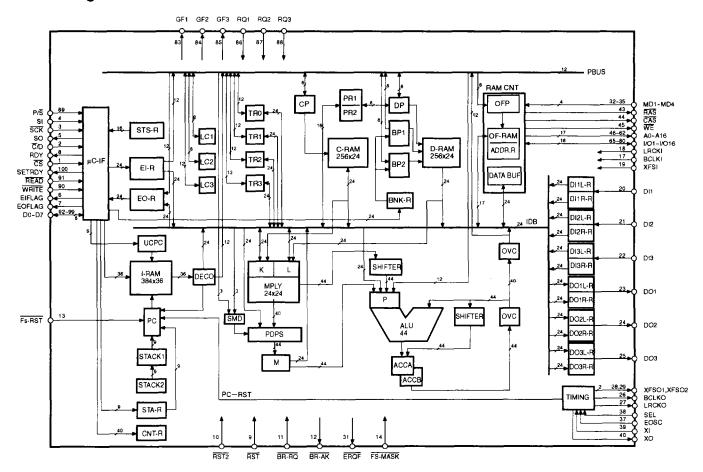
■ IC20 (M5290P) ● Block Diagram

EMITTER OUT(+) COLLECTOR NC OUT(+) (GND) DELAY CAPACITY vcc REF. IN RESET OUT CURRENT OVERHEAT LIMITER PROTECTER REF. VOLTAGE GND (9 ON/OFF CONTROL ON/OFF(1) BALANCE ADJUSTMENT COLLECTOR NC -5V OUT(-) (GND) OUT

■ IC503 (MM1031XS) ● Block Diagram



■ IC302 (µPD6383GF) ● Pin Assignment



Pin Function

Pin No.	Pin Name	I/O	Function
1	CS	Ĭ	Input of µPD6383GF chip select signal.
			When \overline{CS} is active, data can be transferred to and from the host CPU via SI and SO.
			SI input is latched by the leading edge of \overline{CS} .
2	C/D	I	Input of command/data designation signal of SI input
			High: Data
			Low: Command
3	SCK	I	Input of clock for serial data transfer
			SI input and SO output are synchronized with SCK.
4	SI	I	Serial input of commands or data
5	SO	0	Serial output of content of external output register (EO-R) or instruction RAM. This is
			an N-ch open drain pin.
6	EIFLAG	0	Output of status flag of external input register (EI-R)
7	EOFLAG	0	Output of status flag of external output register (EO-R)
8	RDY	0	Output of SI input acceptable status signal. This is an N-ch open drain pin.
9	RST	I	Input of reset signal

Pin No.	Pin Name	I/O	Function					
10	RST2	I	Input of reset signal for rewriting the instruction RAM with the register content.					
11	BR-RQ	I	Input of break request signal from host CPU. BRRQ flag is set by BR-RQ. This is used					
	_		in the emulator mode. Pull up in regular modes.					
12	BR-AK	О	Signal output that indicates that µPD6383 operation has been broken. This is reset by					
			the BRAKST instruction. This is used in the emulator mode.					
13	Fs-RST	I	Input of program counter reset signal. This is used in the emulator mode. Pull up in					
			regular modes.					
14	Fs-MASK	I	Input of PC-RST inhibit signal. This is used in the emulator mode. Pull up in regular					
			modes.					
15	VDD	-	+ power supply to logic circuit (+5V $\pm 10\%$)					
16	GND	-	Ground					
17	BCLKI	I	Input of bit clock for DI1 to DI3 input signals and DO1 to DO3 output signals					
18	LRCKI	I	Input of L/R channel designation signal for DI1 to DI3 input signals and DO1 to DO3					
			output signals					
19	XFsI	Ī	Input for generating the bit clock in the slave mode. XFsI input is divided and output					
			from BCLKO.					
20 22	DI1	I	Serial input of audio signal					
	DI3							
23 25	DO1	О	Serial output of audio signal					
	DO3							
26	BCLKO	O	Output of bit clock for A/D, D/A converter control. In the master mode, the system					
			clock is divided before output. In the slave mode, the signal formed from XFsI input is					
			output. The signal is automatically formed by designation of the XFsI input signal					
			from the control register.					
27	LRCKO	O	Output of L/R channel designation signal for A/D, D/A converter control. In the mas-					
			ter mode, the system clock is divided before output. In the slave mode, the signal					
			formed from input of LRCKI is output.					
28	XFsO1	O	Output of clock for A/D, D/A converter control. In both of the master and slave modes,					
			the system clock is divided before output.					
			The division rate is designated by the control register.					
29	XFsO2	О	Output of clock for A/D, D/A converter control. In the master mode, the system clock					
			is divided before output. In the slave mode, the signal formed from XFsI input is					
			output.					
			The division rate is designated by the control register.					
30	TEST	I	Test input. Pull down.					
31	ERQF	0	Output of ERQ flag					
32 35	MD1	I	Input of select external RAM type for digital delay and connection conditions.					
	MD4							
36	GND	-	Ground					
37	EOSC	I	Input of external clock. When the external clock is not used (SEL is High), either pull					
20	CEI		up or connect to GND.					
38	SEL	I	Input of system clock select signal.					
			High: XI/XO					
39	VI	Ţ	Low: EOSC Connection of crystal for oscillation (input side of inverter)					
	XI	I	Connection of crystal for oscillation (input side of inverter)					

Pin No.	Pin Name	I/O	Function				
40	XO	0	Connection of crystal for oscillation (output side of inverter)				
41	GND	-	Ground				
42	VDD	-	+ power supply to logic circuit (+5V ±10%)				
43	RAS	0	Output of row address strobe of external RAM for digital delay				
44	CAS	0	Output of column address strobe of external RAM for digital delay				
45	WE	0	Output of write enable signal of external RAM for digital delay.				
46	A0	0	Output of address of external RAM for digital delay.				
1	ı		A16 is MSB, and A0 is LSB. Row address and column address are output at DRAM				
62	A16		selection.				
63	VDD	-	+ power supply to logic circuit (+5V ±10%)				
64	GND	-	Ground				
65	I/O1	I/O	Data I/O of external RAM for digital delay. I/O16 is MSB, and I/O1 is LSB.				
80	I/O16						
81	GND	-	Ground				
82	VDD	<u> </u>	+ power supply to logic circuit (+5V ±10%)				
83	GF1	0	Output of general flag. GF1 to GF3 can be set, reset and toggled by instructions.				
85	GF3						
86	RQ1	I	Input of general flag. RQ1 to RQ3 can be controlled from the host CPU, and can be				
88	RQ3		verified by the COND field in instructions.				
89	P/S	I	Input of parallel/serial select of the host CPU interface (µC-IF)				
			High: Parallel				
			Low: Serial				
90	WRITE	I	Input of host CPU data write signal on parallel interface				
91	READ	I	Input of host CPU data read signal on parallel interface				
92 99	D ₀	I/O	Data I/O on parallel interface				
9'9	Ď7						
100	SETRDY	0	Acceptable status signal output of command set or data set. This is used in the emula-				
			tor mode. Set to open in regular modes.				

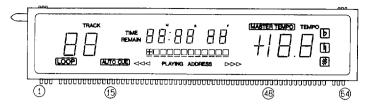
■ IC701 (PD4543A) • Pin Function

Pin No.	Port	Pin Name	I/O	Active	Function	
1	FIP6	GRID7	0	L	FL grid output 7	
2	FIP5	GRID6	0	L	FL grid output 6	
3	FIP4	GRID5	0	L	FL grid output 5	
4	FIP3	GRID4	0	L	FL grid output 4	
5	FIP2	GRID3	0	L	FL grid output 3	
6	FIP1	GRID2	0	L	FL grid output 2	
7	FIP0	GRID1	0	L	FL grid output 1	
8	VDD	VDD	-	-	VDD connection	
9	SCK0	DCLK	0	L	Serial clock (for DSP and CDG)	
10	SO0	DDAT	0	L	Serial data output	
11	P25	CLOSE	Ī	-	Door close detection switch input	
12	P24	SHUT	I	-	Door shutter close detection switch input	
13	P23	JOG2	I	-	Jog dial pulse input	
14	SCK1	CLOK	0	Н	Serial clock (for CXD2500)	
15	SO1	DATA	• 0	Н	Serial data output (for CXD2500)	
16	SI1	SQSO	I	-	Sub-code Q serial data input	
17	RESET	RESET	I	-	CPU reset (Low: reset)	
18	P74	LDON	0	Н	Laser diode output (High: OFF, Low: ON)	
19	P73	DOWN	0	L	Servo-mechanism unit down output	
20	AVSS	GND ref		-	A/D converter ground potential	
21	P17	UP	0	L	Servo-mechanism unit up output	
22	P16	OPEN	I	-	Servo-mechanism unit down completed switch input	
23	P15	CLMP	I		Clamp completed switch input	
24	P14	INSD	I	-	Slider inside switch input (Low: inside)	
25	P13	FCOK	I	-	Focus OK input (High: OK, Low: No Good)	
26	P12	XLAT	0	Н	LSI control data latch pulse	
27	P11	CT	I	-	Slider center tap voltage input	
28	P10	ADIN	I	-	Slider voltage input	
29	AVDD	VDD	-	-	A/D converter analog power supply	
30	AVREF	VDD ref	-	-	A/D converter reference voltage	
31	P04	SENS	I	-	LSI operating status input	
32	XT2	XT2	-	-	Not used	
33	VSS	GND	-	-	GND connection	
34	X1	X1	-	-	Main system oscillation (4.194304 MHz)	
35	X2	X2	-	-	Main system oscillation (4.194304 MHz)	
36	P37	GFS	I	-	Frame sync lock input (High: OK, Low: No Good)	
37	P36	RST2	0	Н	DSP reset 2 output (Low: reset)	
38	P35	RDY	I	- T	DSP ready input (Low: ready)	
39	P34	Ĉ/D	0	Н	DSP command/data designation (Low: command)	
40	P33	CS	0	Н	DSP chip select (Low: select)	
41	P32	RQ2	0	L	DSP memory read (High: read)	
42	P33	RQ1	0	L	DPS memory write (High: write)	
43	P34	GF1	0	Н	DSP data compare input (High: comparing)	
44	P03	CE	0	L	CDG chip enable (High: enable)	
45	INTP2	SCOR	Í	-	Sub-code sync interrupt input	
46	P01	GRES	О	Н	CDG decoder reset (Low: reset)	
47	INTP0	JOG1	I	- 1	Jog dial pulse interrupt input	

48	
SO	
51 P70 CONT1 I/O H External control I/O 52 VDD VDD - - VDD connection 53 P127 GND I - GND connection 54 P126 KD2 I - Key scan data input 55 P125 KD1 I - Key scan data input 56 P124 KD0 I - Key scan data input 57 P123 EPCS O H EEPROM chip select output (Low: output fill coke) 58 P122 EPCK O L EEPROM serial data output fill coke output follows output fill coke fill coke output fill coke fill coke output fill coke fill coke fill coke fill coke	onnection)
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76 PIOI SECO O I El indicator comment outrat 0	
76 P101 SEG8 O L FL indicator segment output 8	
77 P100 GRID11 O L FL grid output 11	
78 FIP9 GRID10 O L FL grid output 10	
79 FIP8 GRID9 O L FL grid output 9	
80 FIP7 GRID8 O L FL grid output 8	

■ V701 (DEL1021) FL Display

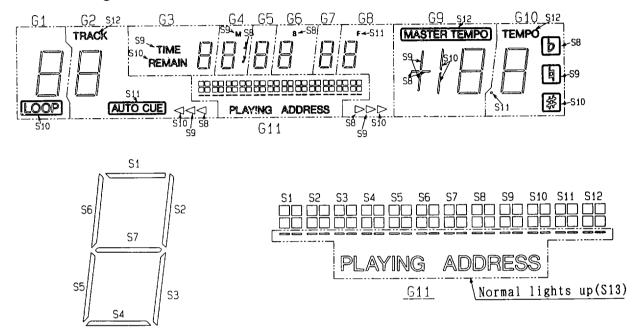
Outline Diagram



Pin Assignment

Pin No.	1	2	3	4	5	6	7	8	9	10	11	12	13	14
Assignment	F	F	F	NΡ	G1	G2	G3	G4	G5	G6	G7	G8	G9	G10
Pin No.	15		- 47	48	49	50	51	52	53	54	55	56	57	58
Assignment	G11	N	Ρ	S13	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10
Pin No.	59	60	61	62	63	64								
Assignment	S11	S12	NΡ	F	F	F								
F:Filament	G1	~G11	:Gri	d .	S1~	S13:	Ano	de	NP:	No p	in			

● Anode Grid Assignment



Anode Grid Assignment and Pin Assignment

G1~G11

				Г	r · ·				r		
	G1	G2	G3	G4	G 5	G6	G 7	G8	G9	G10	G11
S1	SI	S1	SI	S1	S1	SI	S1	\$1	SI	SI	88
S2	\$2	S2	\$2	\$2	S2	S2	S2	\$2	\$2	\$2	88
\$3	S3	\$3	S3	S3	S3	S3	S3	23	\$3	\$3	88
S4	S4	S4	S4	S4	S4	S4	S4	S4	S4	S4	88
S 5	S 5	S5	S5	S5	S5	S5	\$5	S5	S5	S5	88
S6	\$6	S6	S6	S6	S6	S6	S6	\$6	S6	S6	88
S7	S7	S7	S7	S7	S 7	S7	S 7	\$7	\$7	S7	88
\$8		⊲		•		8		▷	+		88
S9		◁	TIME	м				\triangleright	ı.ţ.		88
S10	LOOP	⊲	REMAIN					\triangleright		*	88
Sil		AUTO CUE						F		D	88
S12		TRACK							(MASTER TEMPO)	TEMPO	88
S13											PLAYING ADDRESS

1.7 ADJUSTMENTS

Adjustment Methods

If a disc player is adjusted incorrectly or inadequately, it may malfunction or not work at all even though there is nothing at all wrong with the pickup or the circuitry. Adjust correctly following the adjustment procedure.

Adjustment Items/Verification Items and Order

If the specified values cannot be obtained or no adjustment is possible by performing the verifications or adjustments described in steps 1-4, the pickup block may be defective.

Step	Item	Test Point	Adjustment Location
1	Focus offset verification	TP1, Pin6(FCS. ERR)	None
2	Tracking error balance verification	TP1, Pin2(TRK. ERR)	None
3	Pickup radial/tangential direction	TP1, Pin1(RF)	Radial tilt adjustment screw,
	tilt adjustment		Tangential tilt adjustment screw
4	RF level verification	TP1, Pin1(RF)	None
5	Focus servo loop gain adjustment	TP1, Pin5(FCS. IN)	VR152(FCS. GAN)
		TP1, Pin6(FCS, ERR)	
6	Tracking servo loop gain adjustment	TP1, Pin3(TRK. IN)	VR151(TRK. GAN)
		TP1, Pin2(TRK. ERR)	
7	MSB adjustment	AUDIO OUT(JA801)	VR402(MSB ADJ.L)
		L or R terminal	VR401(MSB ADJ.R)
8	3.58MHz Free-run Frequency adjustment	TP3	VC501(FSC)

Abbreviation table

FCS. ERR: Focus Error
TRK. ERR: Tracking Error
FCS GAN: Focus Gain
TRK GAN: Tracking Gain
FCS. IN: Focus In
TRK. IN: Tracking In

Measuring Instruments and Tools

- 1.Dual trace oscilloscope (10:1 probe)
- 2.Signal generator
- 3.Test disc (YEDS-7)
- 4.8cm disc (With at least about 20 minutes of recording)
- 5.Low-pass filter (39k Ω +0.001 μ F)
- 6.Resistor (100k Ω)
- 7.Ball point hexagon wrench (GGK1002)
- 8.Standard tools

Test Point and Adjustment Variable Resistor Positions VR151 VR152 TP1 G836280 CN201 IC207 VR402 VR402 VR401 Short from the reverse side.

Figure 1. Adjustment Locations

Notes

- 1.Use a 10:1 probe for the oscilloscope.
- 2.All the knob positions (settings) for the oscilloscope in the adjustment procedures are for when a 10:1 probe is used.

Test Mode

This model has a test mode so that the adjustments and checks required for service can be carried out easily. When this model is in test mode, the keys on the control panel work differently from normal. Adjustments and checks can be carried out by operating these keys with the correct procedure. For this model, all adjustments are carried out in test mode.

[Setting these models to test mode]

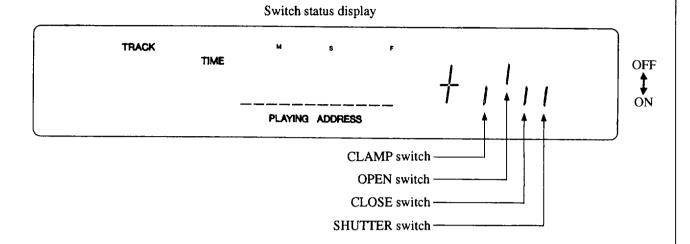
How to set this model into test mode.

- 1. Turn off the power switch.
- 2. Short the test mode jumpers with the door closed. (see Figure 1.)
- 3. Turn on the power switch.

When the test mode is set correctly, the display is different from what it usually is when the power is turned on. If the display is still the same as usual, test mode has not been set correctly, so repeat Steps 1-3.

The display (FL indicator) is as follows when the test mode is entered.

The TEMPO display is as follows. Switches flipped down are ON, and switches flipped up are OFF. The switches are arranged from left to right: CLAMP switch, OPEN switch, CLOSE switch and SHUTTER switch. When the servo-mechanism unit is up, "+" is displayed. When the servo-mechanism unit is down, "-" is displayed.



[Release from test mode]

Here is the procedure for releasing the test mode:

1. Press the CUE key and stop all operations.

2. Turn off the power switch.

[Operations of the keys in test mode]

Code	Key Name	Function in Test Mode	Explanation
▶ ►	TRACK SEARCH FWD	Focus servo close	The laser diode is lit up and the focus actuator is lowered, then raised slowly and the focus servo is closed at the point where the objective lens is focused on the disc. With the player in this state, if you lightly rotate the stopped disc by hand, you can hear the sound the focus servo. If you can hear this sound, the focus servo is operating correctly. If you press this key with no disc mounted, the laser diode lights up, the focus actuator is pulled down, then the actuator is raised and lowered three times and returned to its original position.
> /II	PLAY/PAUSE	Spindle servo ON	Starts the spindle motor in the clockwise direction and when the disc rotation reaches the prescribed speed (about 500 rpm at the inner periphery), sets the spindle servo in a closed loop. Be careful. Pressing this key when there is no disc mounted makes the spindle motor run at the maximum speed. If the focus servo does not go correctly into a closed loop or the laser light shines on the mirror section at the outemost periphery of the disc, the same symptom is occurred.
► /II	PLAY/PAUSE	Tracking servo close/ open	Pressing this key when the focus servo and spindle servo are operating correctly in closed loops puts the tracking servo into a closed loop, displays the track number being played back and the elapsed time on the front panel, and outputs the playback signal. If the elapsed time is not displayed or not counted correctly or the audio is not played back correctly, it may be that the laser is shining on the section with no sound recorded at the outer edge of the disc, that something is out of adjustment, or that there is some other problem. This key is a toggle key and open/close the tracking servo alternately. This key has no effect if no disc is mounted.

Code	Key Name	Function in Test Mode	Explanation
*	SEARCH REV	Carriage reverse (outwards)	Moves the pickup position toward the outer diameter of the disc. When this key is pressed with the tracking servo in a closed loop, the tracking servo automatically goes into an open loop. Since the motor does not automatically stop at the mechanical end point in test mode, be careful with this operation.
>>	SEARCH FWD	Carriage forward (inwards)	Moves the pickup position toward the inner diameter of the disc. When this key is pressed with the tracking servo in a closed loop, the tracking servo automatically goes into an open loop. Since the motor does not automatically stop at the mechanical end point in test mode, be careful with this operation.
	CUE	Stop	Switches off all the servos and initialized. When this key is pressed, the pick-up does not return to the inside.
A	EJECT	Disc compartment door open	Open the disc compartment door. Pressing this key when the disc is turning stops the disc, then opens the door. When this key is pressed, the pick-up does not return to the inside.
	TIME MODE	Operation count display mode	The number of backed up clamp operations is displayed on the FL indicator up to 999999. In the test mode, the number of clamp operations is not counted.

Notes:

- 1. When a GFS error occurs as a result of playing a dirty or scratched disc, muting is turned on and off. However, the disc is not stopped, so sometimes the unit runs away. If this happens, either press the CUE key, or turn the power off.
- 2. Do not press the key during open/clamp, spindle kick and focus search operations. Make sure that each operation is completed before carrying out the next operation.

[How to play back a disc in test mode]

In test mode, since the operate independently, playing back a disc requires that you operate the keys in the correct order to close the servos.

Here is the key operation sequence for playing back a disc in test mode.

TRACK SEARCH FWD

Lights up the laser diode and closes the focus servo.

PLAY/PAUSE ▶/▮

Starts the spindle motor and closes the spindle servo.

____<u></u>

PLAY/PAUSE >/|| Closes the tracking servo.

Wait at least 2-3 seconds between each of these operations.

Verify the DC voltage at TP1, Pin 6 (FCS. ERR) is 0 ± 50 mV.

1. Focus Offset Verification

•Objective •Symptom when out of adjustment	Verify the DC offset for the focus error amp. The model does not focus in and the RF signal is dirty.						
•Measurement instrument connections		ne oscilloscope to FCS. ERR)	•Player state	Test mode, stopped (just the Power switch on)			
	[Settings]	5mV/division 10ms/division	•Adjustment location	None			
	\ 	DC mode	•Disc	None needed			

Note: If the specified values cannot be obtained or no adjustment is possible by performing the verifications or adjustments described in adjustment items 1-4, the pickup block may be defective.

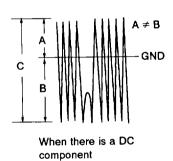
2. Tracking Error Balance Verification

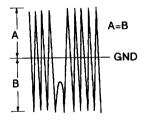
•Objective •Symptom when out of adjustment	To verify that there is no variation in the sensitivity of the tracking photo diode. Play does not start or track search is impossible.					
•Measurement instrument connections	TP1, Pin2(ne oscilloscope to TRK. ERR). This may be via a low	•Player state	Test mode, focus and spindle servos closed and tracking servo open.		
	_		Adjustment location	None		
	[Settings]	50mV/division 5ms/division DC mode	•Disc	YEDS-7		

[Procedure]

- 1. Move the pickup to midway across the disc(R=35mm)with the SEARCH FWD ▶▶ or ◀ key.
- 2. Press the TRACK SEARCH FWD ▶► key, then the PLAY/PAUSE ▶/ key in that order to close the focus servo then the spindle servo.
- 3. Line up the bright line (ground) at the center of the oscilloscopes screen and put the oscilloscope into DC mode.
- 4. Supposing that the positive amplitude of the tracking error signal at TP1, pin 2 (TRK ERR) is (A) and the negative amplitude is (B), the following expression is satisfied.

When A
$$\geqq$$
 B , $\frac{A-B}{C}\times\frac{1}{2}\leqq0.1$ When A < B , $\frac{B-A}{C}\times\frac{1}{2}\leqq0.1$





When there is no DC component

3. Pickup Radial / Tangential Tilt Adjustment

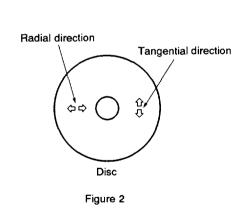
•Objective	To adjust the angle of the pickup relative to the disc so that the laser beams are shone straight down into the disc for the best read out of the RF signals.			
•Symptom when out of adjustment	Sound broken; some discs can be played but not others.			
•Measurement instrument connections	Connect the oscilloscope to TP1, Pin1(RF).	•Player state	Test mode, play	
	[Settings] 20mV/division 200ns/division AC mode	•Adjustment location	Pickup radial tilt adjustment screw and tangential tilt ad- justment screw	
		•Disc	8cm disc (However, those with approx. 20 min of audio sig- nal (music).)	

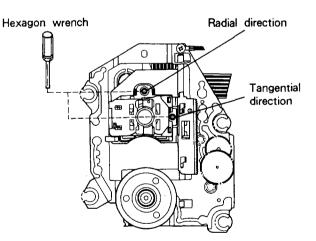
[Procedure]

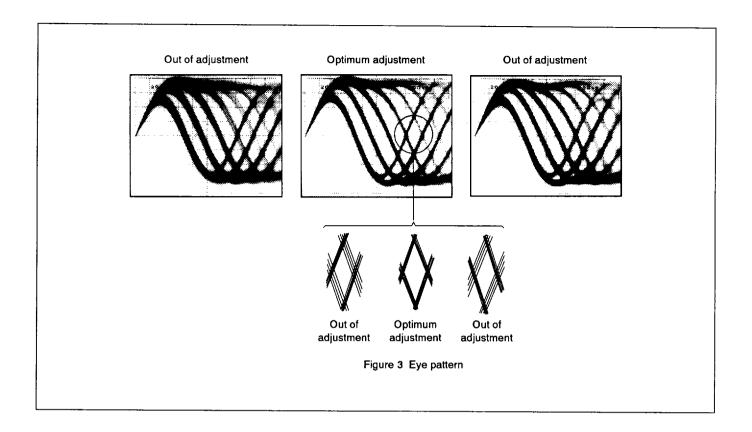
- 1. Press the SEARCH FWD ▶▶ or ◄◄ key to move the pickup to the external circumference of the disc.

 Press the TRACK SEARCH FWD ▶▶ key, the PLAY/PAUSE ▶/▮ key, then the PLAY/PAUSE ▶/▮ key in that order to close the respective servos and put the player into play mode.
- 2. First, adjust the radial tilt adjustment screw with the hexagon wrench (GGK1002) so that the eye pattern (the diamond shape at the center of the RF signal) can be seen the most clearly.
- 3. Next, adjust the tangential tilt adjustment screw with the hexagon wrench (GGK1002) so that the eye pattern (the diamond shape at the center of the RF signal) can be seen the most clearly. (Figure 3).
 - *The ball-point type hexagonal wrench is used because the disc will get in the way if a normal hexagonal wrench is used.
- 4. Adjust the radial tilt adjustment screw and the tangential tilt adjustment screw again so that the eye pattern can be seen the most clearly. As necessary, adjust the two screws alternately so that the eye pattern can be seen the most clearly.
- 5. When the adjustment is completed, lock the radial and tangential adjustment screw.

Note: Radial and tangential mean the directions relative to the disc shown in Figure 2.







4. RF Level Verification

•Objective	To verify th	To verify the playback RF signal amplitude		
•Symptom when out of adjustment	No play or no search			
•Measurement instrument connections	Connect th	e oscilloscope to RF).	•Player state	Test mode, play
	[Settings]	50mV/division	•Adjustment location	None
		10ms/division AC mode	•Disc	YEDS-7

[Procedure]

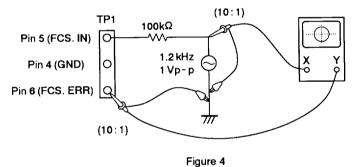
- 1. Move the pickup to midway across the disc(R=35mm)with the SEARCH FWD ►► or ◄ key, then press the TRACK SEARCH FWD ►► key, the PLAY/PAUSE ►/ key, then the PLAY/PAUSE ►/ key in that order to close the respective servos and put the player into play mode.
- 2. Verify the RF signal amplitude is 1.2 Vp-p \pm 0.2V.

5. Focus Servo Loop Gain Adjustment

•Objective	To optimize the focus servo loop gain. Playback does not start or focus actuator noisy.			
•Symptom when out of adjustment				
•Measurement instrument connections	See figure 4.	•Player state	Test mode, play	
	[Settings] CH1 CH2	•Adjustment location	VR152 (FCS. GAN)	
	20mV/division 5mV/division X-Y mode	•Disc	YEDS-7	

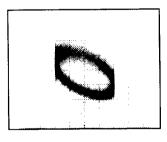
[Procedure]

- 1. Set the signal generator output to 1.2kHz and 1Vp-p.
- 2. Press the SEARCH FWD ▶ or ◀ key to move the pickup to halfway across the disc(R=35mm), then press the TRACK SEARCH FWD ▶ key, the PLAY/PAUSE ▶/ key, then the PLAY/PAUSE ▶/ key in that order to close the corresponding servos and put the player into play mode.
- 3. Adjust VR152 (FCS. GAN) so that the Lissajous waveform is symmetrical about the X axis and the Y axis.

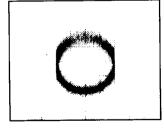


rigure 4

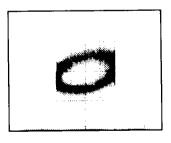
Focus Gain Adjustment



Higher gain



Optimum gain



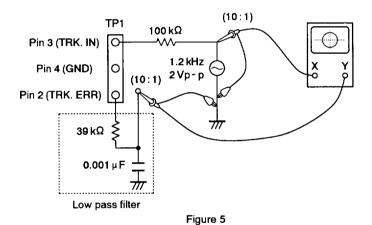
Lower gain

6. Tracking Servo Loop Gain Adjustment

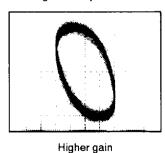
•Objective	To optimize the tracking servo loop gain.				
•Symptom when out of adjustment	Playback does not start, during searches the actuator is noisy, or tracks are skipped.				
•Measurement instrument connections	See figure 5.	•Player state	Test mode, play		
	[Settings] CH1 CH2	•Adjustment location	VR151 (TRK. GAN)		
	50mV/division 20mV/division X-Y mode	•Disc	YEDS-7		

[Procedure]

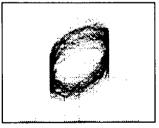
- 1. Set the signal generator output to 1.2kHz and 2Vp-p.
- 2. Press the SEARCH FWD ► or ◄ key to move the pickup to halfway across the disc(R=35mm), then press the TRACK SEARCH FWD ► key, the PLAY/PAUSE ►/ key, then the PLAY/PAUSE ►/ key in that order to close the corresponding servos and put the player into play mode.
- 3. Adjust VR151 (TRK. GAN) so that the Lissajous waveform is symmetrical about the X axis and the Y axis.



Tracking Gain Adjustment







Optimum gain

Lower gain

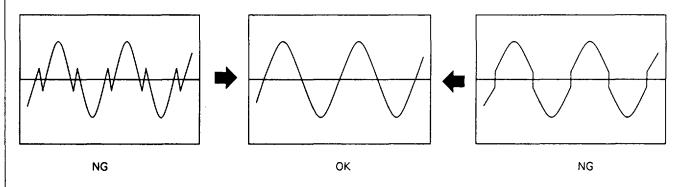
7. MSB Adjustment

•Objective	To optimize the MSB level of the DAC			
•Symptom when out of adjustment	Audio distortion			
•Measurement instrument connections	Connect the oscilloscope to AUDIO OUT L or R termi-	•Player state	Test mode, play	
	nal (JA801)	•Adjustment location	VR402(MSB ADJ.L) VR401(MSB ADJ.R)	
	[Settings] 5mV/division 0.2ms/division	•Disc	YEDS-7	

[Procedure]

- 1. Playback the track20 (-60dB, 1KHz, Lch, Rch) of the test disc (YEDS-7). Connect the oscilloscope to the Lch of the AUDIO OUT terminal (JA801), and observe the audio output waveform.
- 2. Adjust VR402 (MSB ADJ.L) so that the sine wave is obtained on oscilloscope.
- 3. Adjust VR401 (MSB ADJ.R) in the same way.

ZERO cross distortion waveform

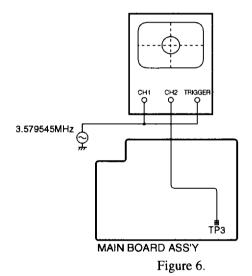


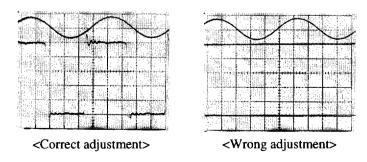
8. 3.58MHz Free-run Frequency Adjustment (CDJ-500G/KUC only)

•Objective	To optimize the master clock frequency of the IC501			
•Symptom when out of adjustment	Color phase shift of CDG output image, no color lock			
•Measurement instrument connections	See figure 6. [Settings]	•Player state	Test mode,stopped (Just the power switch on)	
	CH1 CH2 1V/division 1V/division	•Adjustment location	VC501	
	50ns/division	•Disc	None needed	

[Procedure]

- 1. Adjust the output of the signal generator to 3.579545 MHz.
- 2. Adjust VC501 so that both CH1 and CH2 waveforms on the oscilloscope stop. (tolerance: ±3Hz or less)



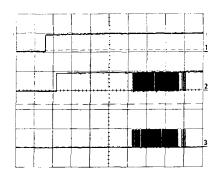


1.8 TROUBLESHOOTING

How to Check Operation of the DSP IC (IC302:μPD6383GF)

- (1) Checking the system clock frequency
 Make sure that the clock frequency is 24.576 MHz
 (±5%) at XO (pin No.40).
- (2) Checking data transfer of DSP internal initialization In order to operate the DSP, the initialize data (DSP internal registers and operation program) must be transferred from the system control microcomputer (IC701:PD4543A).

After turning the power on, make sure that \overline{RST} (pin No.9) becomes High (5V), and that the clock pulse and data are input for about 150 ms to \overline{SCK} (pin No.3) and SI (pin No.4).

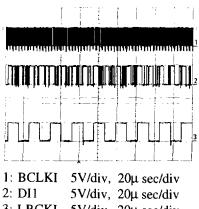


1: RST 5V/div, 50m sec/div

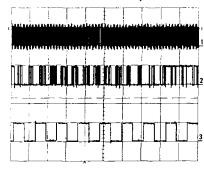
2: SCK 5V/div, 50m sec/div

3: SI 5V/div, 50m sec/div

- (3) Checking the DSP operation control terminal Make sure that a High signal (5V) is being input to RQ1 (pin No.86) and RQ2 (pin No.87) during operation of the player.
- (4) Checking the digital audio I/O terminal Make sure that the bit clock, data and LR clock are being input to BCLKI (pin No.17), DI1 (pin No.20) and LRCKI (pin No.18) during operation of the player. Likewise, make sure that the bit clock, data and LR clock are being output to BCLKO (pin No.26), DO1 (pin No.23) and LRCKO (pin No.27).



3: LRCKI 5V/div, 20µ sec/div

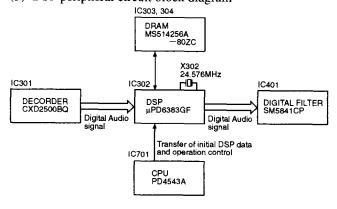


1: BCLKO 5V/div, 20µ sec/div

2: DO1 5V/div, 20µ sec/div

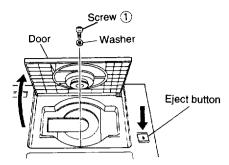
3: LRCKO 5V/div, 20µ sec/div

(5) DSP peripheral circuit block diagram

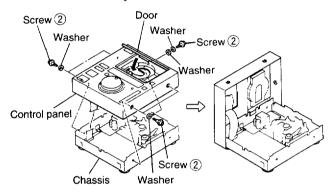


Disengaging The Control Panel

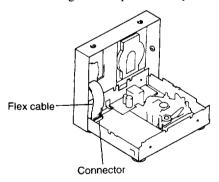
- 1. Turn on the power and press the Eject button to open the door (note 1).
- 2. Unplug the power cord.
- 3. Remove the screw ① which is under the door.



- 4. Remove the five screws ② on the side surface of the control panel.
- Close the door and then disengage the control panel from the chassis.
- 6. Insert the control panel under the chassis and secure it.



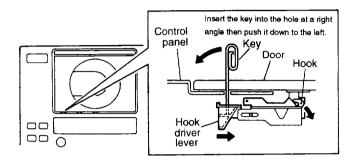
7. When disconnecting the flexible cable from the connector for maintenance, etc., be sure to unplug the power cord and then remove the flexible cable without twisting it by holding it with both hands and by both ends. (Even when the power is off, a residual electrical charge remains in the capacitors and the like. If the terminal of the flexible cable and the terminal of the connector accidentally touch damage to components may occur.)



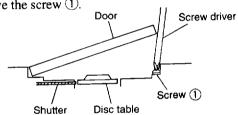
(Note 1) Important Opening the door and disengaging the control panel in an emergency

This model is equipped with a mechanism that enables you to open the door manually if it does not open even when the Eject button is pressed.

- 1. Unplug the power cord.
- 2. Prepare a key by using a zem clip (a metal rod with a diameter of approximately 1mm and a length of 50mm).



- 3. By pushing the key to the left after inserting it into the hole at a right angle, as shown in the figure above, you can open the door by moving the hook drive lever.
- 4. Open the door wide enough to insert a screw driver and remove the screw ①.



- (Note) Do not open the door any wider than necessary because, depending upon the position of the servo mechanism, the disc table may come into contact with the shutter, which may damage these components.
- 5. Be sure to close the door after removing the screw.
- 6. You can separate the control panel from the chassis by removing all the screws ② on the control panel.
- 7. The door opens completely when the hook drive lever on the control panel is moved from the inside of the control panel in the same direction as in 3.

Error display

The error No. is displayed on the FL indicator when a player operation error occurs during normal operation.

Error No.	E-72	E-83	E-91	
Error Name	TOC READ ERROR	PLAYER ERROR	MECHANICAL TIME OUT	
Description	TOC data on disc could not be read after 20 seconds.	The disc cannot be played normally. GFS No Good judgment: 2 sec x 3 times continuous. Focus No Good judgment: 0.1 sec x 3 times continuous.	Operation is not completed after maximum set operating time is exceeded. Clamp: 4 sec Open: 4 sec	
Probable Cause	Disc is dirty. Problem with the pick-up.	Disc is dirty. Disc is scratched.	Object is on door. Trouble at door open/close mechanism.	

1.9 PARTS LIST FOR EXPLODED VIEWS AND PACKING

NOTES:

1-36

- Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
- The A mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- Parts marked by "•" are not always kept in stock. Their delivery time may be longer than usual or they may be unavailable.

1) P	AC	KING		Mark		Description	Parts No.
•					32	Smoother	DNK2933
Mark	No.	Description	Parts No.		33	Spring J	DBH1252
	1	Cord with plug	VDE-010		34	Encorder disc ass'y	DXB1160
NSP	2	Vinyl bag	REG-013		35		
	3	Caution220V(SELM only)	ARR-003			Lever spring	DBH1254
	4	Operating instructions(KUC only)	DRB1161		36	Lever	DNK2937
	7	(English)	DRBITOT		37	Hook drive lever	DNK2945
	4		DDD1166		38	Hook ass'y	DXB1509
	4	Operating instructions(SELM only)					
		(English, French, Germany, Italy, D	utch, Swedish,		39	Switch holder A ass'y	DXB1510
		Spanish, Chinese)			40	Knob A	DNK2931
					41	Loop knob	DNK2943
NSP	5	Vinyl bag	VHL-014		42	Limiter	DNK2949
	6	Screw	IBZ30P180FMC		43	Power knob	
	7	Pad L	DHA1290		43	rowei kilou	DNK2942
	8	pad R	DHA1291				
	9	Mirror mat	VHL1006		44	TEMPO knob ass'y	DXA1680
	,	Will Of High	VIILIOOO		45	Eject knob ass'y	DXA1679
	• •	B II (IIIIG II)	DIIG1504		46	VR packing	DEB1291
	10	Packing(KUC only)	DHG1586		47	VR plate	DNF1485
	10	Packing(SELM only)	DHG1587		48	Time mode knob	DNK2944
NSP	11	Follow up card(KUC only)	DRY1032				
NSP	12	Vinyl bag(KUC only)	DHL1011		49	VR spring	DBK1088
	13	Spacer	DLA1637		50	Shutter	
		•		NOD			DNK2939
				NSP	51	Encorder disc	DNH1248
٥١ -	·vŦ	CDIOD CECTION			52	Lever switch	DSK1003
2) E	:XI	ERIOR SECTION			53	Door spring	DBH1255
•							
Mark	Nο	Description	Parts No.		54	Drive cam B ass'y	DXB1515
viai K	_	Roller holder ass'y	DXB1516		55	Drive cam C	DNH1927
	Ţ			NSP	56	Photo interrupter ass'y	DWX1438
	2	Door ass'y(KUC only)	DXA1714	1101	57	Control ass'y	DWG1415
	2	Door ass'y(SELM only)	DXA1715	NCD			
NSP	3	Yoke(FE)	PNB1216	NSP	58	Power SW ass'y	DWS1213
NSP	4	Clamp magnet	PMF1014				
				NSP	59	Eject SW ass'y	DWS1214
	5	Clamper holder	DNH1920		60	Slider ass'y	DXX2225
NSP	6	•			61	Rubber spacer B	DEB1272
NOL		Clamper (under)	DNK1791		62	Rubber cap	DEB1273
	7	Drive cam A ass'y	DXB1513	NSP	63	Knob base	DNK2950
	8	Carn roller	DLA1629	1431	05	NIOO base	DNK2930
	9	Door cushion A	DEC1777	NOD		TEMPO	D 1 C1500
				NSP	64	TEMPO cap	DAC1799
	10	Door cushion B	DEC1778	NSP	65	Eject cap	DAC1798
	11	Door cushion C	DEC1779		66	Float rubber B	DEB1270
	12	Shutter guide	DNK2940		67	Knob spring	DBH1256
				NSP	68	Mechanism ass'y	DXA1684
	13	Play knob	DAC1809	1.01	50		21111007
	14	Cue knob	DAC1810		69	40P F•F•C/60V	DDD1005
							DDD1085
	15	Spring	DBH1253		70	Main unit(KUC only)	DWX1437
	16	Net	DED1087		70	Main unit(SELM only)	DWX1441
	17	Door holder	DNK2952	NSP	71	Audio out ass'y(KUC only)	DWX1439
	[18]	Damper ass'y	REC1005	NSP	71	Audio out ass'y(SELM only)	DWX1442
						,,,	
	19	Cushion C	DEC1781		72	Transformer ass'y(KUC only)	DWR1185
		_					
	20	Cushion D	DEC1782		72	Transformer ass'y(SELM only)	DWR1186
	21	Shutter stop rubber	DEB1275	NSP	73	Transistor unit	DWX1484
	22	Slide knob	DNK2936		74	Plate	DNH1921
	23	Door cushion D	DEC1780		75	Shield plate	DNH1917
						•	
	24	Door stop rubber	DEB1274	A	76	Fuse(500mA)(KUC only)	VEK1009
			53114505	Å Å	76		
	25	Control panel ass'y(KUC only)	DXA1725	\ \ \ \ \ \ \		Fuse(315mA)(SELM only)	REK-095
	25	Control panel ass'y(SELM only)	DXA1727	47	77	Power transformer(120V)(KUC only)	
	26	Jog dial	DNK2932	Δ	77	Power transformer(110, 120;	DTT1105
	27	Thrust stay	DNH1401			220-230, 240V)(SELM only)	
				Δ	78	Power cord with plug(KUC only)	DDG1063
	28	Retainer	DNK1849	ш	, 0	- 5 2 Cold a. prag(1200 only)	
			V 13 V 14 1 0 0 4	•	78	Power cord with plug(CEI M only)	DDC1003
	29	Steel ball	VNX1006	Δ		Power cord with plug(SELM only)	PDG1003
	30	Thrust stay B	DNH1925		79	Damp cushion A	DEC1366
	31	Jog holder ass'y	DXA1693	NSP	80	Cable clamper	REC1182
-36		J = :)					

Mark NSP	No.	Description Rubber spacer	Parts No. DEB1150	(3)	MEC	CHANISM ASS'Y	
NSP	82	PCB support	VEC1508	Mark	No	Description	Parts No.
				NSP	1	Servo mechanism ass'y	DXA1685
NOD	83	Cushion A	DEC1775	1451	2	Binder	Z09-056
NSP	84	Holder	PNW2343		3	Float rubber	PEB1132
NCD	85	Cushion B	DEC1776		4	Float base(ABS)	PNW2032
NSP	86	Chassis	DNA1151		3	Clamp cam(ABS)	DNK3029
	87	PCB support	DEC1773		سعا		
NSP	88	Cord clamper	Z06-060		$\lceil 6 \rceil$	Rubber belt	PEB1193
NSF	89	Rear panel(KUC only)	DNC1359		7	Gear pulley(POM)	PNW2034
	89	Rear panel(SELM only)	DNC1360		8	Drive gear 2(PON)	PNW2369
	90	Rubber spacer C	DEB1276		9	Release plate spring	DBH1257
	<u>हिं</u>	Rubber spacer A	DEB1271		10	Release lever	DNK2938
		Tracoor spacer	232.271				
•	92	Float rubber B	DEB1270		11	Release lever spring	DBH1258
	93	Cushion	DEB1279		12	Switch holder B ass'y	DXB1526
	94	Insulator ass'y(B)	VXA1414		13	Connector ass'Y(2p)	DDE1082
NSP	95	Insulator	VNK1353		15		DSG1013
NSP	96	Cushion	VEC1297		13	Loading base(ABS)	DNK3030
					16	Lever switch	DSK1003
	97	Caution label(G)(SELM only)	VRW329		17	Motor pulley	PNW1634
	98	Insulator ass'y	VXA1454		18	DC motor/0.75W	PXM1010
	99	SH screw	PBA1033		19	Pickup ass'y-S	DXX2185
	100	Washer	WA26D047D025		20	Disc table(PC)	PNW1608
	101	E ring	YE12FUC			()	
	102	E ring	YE15FUC		21	Gear 3	PNW2054
		E ring	YE20FUC		22	Gear 2	PNW2053
	104	E ring	YE30FUC	NSP	23	Earth lead unit	PDF1074
	105	Screw	AMZ30P040FMC		24	Carriage base	PNW2445
	106	Screw	BPZ20P080FZK		25	Guide bar	PLA1094
	100				_		
	107	Screw	BPZ26P050FMC		26	DC motor ass'y(SPINDLE)	DXM1063
	108	Screw	BPZ26P060FNI		27	Gear 1	PNW2052
	109	Screw	BPZ26P060FZK	NOD	28	Pinion gear	PNW2055
	110	Screw	BPZ26P080FZK	NSP	<u>29</u>	DC motor(CARRIAGE)	PXM1027
	111	Screw	BPZ30P080FMC		30	DC motor ass'y	PEA1246
				NSP	31	Gear stopper	PNB1303
		Screw	IPZ26P060FMC	NSP	32	CDG mecha PCB unit	DWX1436
	113		ZMD26H030FBT	1451	33	PWB holder	PNW2057
		Screw	BBZ30P060FMC		34	Washer	WT16D032D025
		Screw	BBZ30P080FZK		35	Washer	WT12D032D025
	116	Screw	BBZ30P160FZK		55	77 40101	
		Control (EV (CELA) and A	UDW 220		36	Screw	PBA1048
	117	Caution label (F) (SELM only)	VRW-328 PRW1233		37	Screw	Z39-019
	118 119	• • • • • • • • • • • • • • • • • • • •	VRW1094		38	Screw	PMZ26P040FMC
	120	Caution label (SELM only)	DRW1603		39	Screw	BMZ20P060FMC
		Line voltage selector(SELM only)	RSB1009		40	Screw	BPZ26P080FMC
	121	Line voltage selector(SELIM omy)	K3B1009				
	122	Conector (2P)	DDE1082		41	Screw	JFZ17P025FZK
	123	Strain relief(KUC only)	VEC-201		42	Screw	JFZ20P030FNI
	123	Strain relief(SELM only)	CM-22B		43	Screw	BBZ26P060FMC
	124		DBA1079		44	Screw	BPZ20P060FMC
		Screw cap	DNK2959		45	Screw	BPZ26P100FMC
		•			16	Connector accivi(4D)	DEE1092
	126		DAP1045		46 47	Connector ass'y(4P) Connector ass'y(4P)	DEE1083 DKP2863
	127	Door reinforcement plate	DNH1946		48	Connector ass y(4P) Connector ass'y(11P)	DKP2863 DKP2861
	128	65 label(KUC only)	ORW1069		49	Release plate	DXB1511
	129	Panel	DAH1750		50	Washer	VEC1254
	130	Slide sheet	DAH1751		50	W district	VEC 1254
			XZG 40EDE		51	Flexible guide	DNK2946
		Ring	YS40FBT		٥.	Tremble garde	21112210
		Sheet A	DEC1801				
		Sheet B	DEC1802	N	OTF	1. Regular inspection / Clean	ing
		Sheet C	DEC1803 CPZ26P060FZK	17	VIL		
	133	Screw	CFZ20F000FZK			When performing regular in	
	126	Screw	BBZ30P180FMC			ticularly careful attention to the	he parts with the part
		Screw	BMZ40P060FMC			No. in in the parts list.	
		Door ass'y-S(KUC only)	DXX2203				
		Door ass'y-S(SELM only)	DXX2203 DXX2204	N		2. Inspection / Cleaning	
	139		YS20FBT		•	The sliding sections of the c	lrive cam B and the
NSP		J bush	DLA1624			drive cam C (Cleaning / App	
	. 10	e eerth				-	-
	141	Washer	WA52D080D025		•	The lens in the PU section (C	leaning)
		Washer	WA42D080D025				
NSP		Cushion	VEC1224				
							1 07

1.10 PCB PARTS LIST

NOTES:

- Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
- The A mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- Parts marked by "®" are not always kept in stock. Their delivery time may be longer than usual or they may be unavailable.
- When ordering resistors, first convert resistance values into code form as shown in the following examples.
 - Ex. 1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J = 5%, and K = 10%).

560Ω →	56 × 10 ¹ →	561	RD1/8PM [5] 61 [1] J
		473	

Ex. 2 When there are 3 effective digits (such as in high precision metal film resistors). $5.62k\Omega \rightarrow 562 \times 10^{1} \rightarrow 5621 \dots RN1/4PC$ § 6 2 1 F

Mark 1	No. Description	Parts No.	Mari	No.	Description	Parts No.
LIST	OF ASSEMBLES		CAF	ACITO	ORS	
				C701		CEAS101M10
NSP	MOTHER BOARD ASS'Y(KUC TYPE) DWM1390			C705, C707, C708, C710-C713	CKCYF103Z50
NSP	MOTHER BOARD ASS'Y(SELM TYPE			C706, 0	2709	CKCYF473Z50
	- TRANSFORMER ASS'Y(KUC TYP)	•	RES	ISTOF	rs .	
	- TRANSFORMER ASS'Y(SELM TYPE	•		R719		RA4T473J
	- MAIN UNIT(KUC TYPE)	DWX1437		R720		RA6T223J
	- MAIN UNIT(SELM TYPE)	DWX1441		Other F	Resistors	RD1/6PM
	- AUDIO OUT ASS'Y(KUC TYPE		OTH	IERS		
	- AUDIO OUT ASS'Y(SELM TYP		011	CN603	CONNECTOR(40P)	HLEM40R
NSP	TRANSISTOR UNIT	DWX1484		CN606		2-173979-2
1431	- TRANSISTOR UNIT	DWA1404		J652	CONNECTOR ASSEMBLY	PDE1170
NICD	SUB BOARD ASS'Y	DWW1202		V701	FL INDICATOR TUBE	DEL1021
NSP		DWX1397		X701	CERAMIC RESONATOR	VSS1014
NOD	CONTROL ASS'Y	DWG1415				
NSP	POWER SW ASS'Y	DWS1213	TR	ANSE	FORMER ASS'Y	
NSP	- EJECT SW ASS'Y	DWS1214				
NSP	PHOTO INTERRUPTER ASS'Y	DWX1438	SEN	IICONI	DUCTORS	
	∟SLIDER ASS'Y	DXX2225		IC102		ICP-N15
			Ţ	IC101		ICP-N25
NSP	MECHANISM ASS'Y	DXA1684	Δ	IC20		M5290P
NSP	└CDG MECHA PCB UNIT	DWX1436	Λ	Q2		2SD1913
			213	Q3		2SD2144S
CON	TROL ASS'Y		Λ	D52		11ES2
			7.2	D54		MTZJ22A
SEMIC	CONDUCTORS		Δ	DII-D	14	RB100A
	2709	NJM79L06A				
	2702	NM93C46N			D FILTERS	
IC	C701	PD4543A	Δ	LI	FILTER	VTL-004
Q	701-Q703, Q708	2SA933S	CAF	ACITO	ORS	
Q	704-Q707	2SC1740S		C31, C	60	CEAS010M50
_	500 D510 D51 (D500			C61		CEAS100M50
	708-D713, D716-D723	1SS254			UC only)	CEAS221M50
	705, D706 701, D702	GL3HY43 GL5EG8		C52(10	0/100)(SELM only)	DCH1070 CEAS470M50
	703, D704	GL5EG8 GL5HY8		C33		CEAS470M30
_	755,275	0201110		C27, C	28	CEAS471M10
	HES AND RELAYS				000/16)	VCH1054
_	709	DSG1016			13, C15, C16(KUC only)	CKCYF103Z50
	704, S707	DSG1046			13-C17(SELM only)	CKCYF473Z50
	701, S708 702, S703, S705, S706	PSG1006		C29, C	30	CKCYF103Z50
3	102, 3103, 3103, 3100	RSG-143	A	C1 C2	(10000/400)(KUC only)	RCG-009
				C1, C2		VCG-048
				C25	(18000/16)	DCH1069

D1/2PM225J D1/4PM472J D1/6PM	D453, D454(KUC only) COILS AND FILTERS L301, L331, L401, L601, L602 L339(SELM only) L604, L605(SELM only) CAPACITORS C506, C507(KUC only) C310, C311, C331, C332 C383 C509(KUC only) C151-C153, C401 C501-C503(KUC only) C81, C82 C83, C84 C433, C434 C216, C217, C303, C330, C336	LAU010J LFA010K VTH1024 CCCCH050C50 CCCCH150J50 CCCCH220J50 CEAS010M50 CEAS101M10 CEAS101M10
D1/4PM472J D1/6PM	L301, L331, L401, L601, L602 L339(SELM only) L604, L605(SELM only) CAPACITORS C506, C507(KUC only) C310, C311, C331, C332 C383 C509(KUC only) C151-C153, C401 C501-C503(KUC only) C81, C82 C83, C84 C433, C434	CCCCH050C50 CCCCH150J50 CCCCH220J50 CEAS010M50 CEAS101M10 CEAS101M10
KC-061 NF-091 BB1185-F8 TZJ33B GG1007 EAS220M50	L604, L605(SELM only) CAPACITORS C506, C507(KUC only) C310, C311, C331, C332 C383 C509(KUC only) C151-C153, C401 C501-C503(KUC only) C81, C82 C83, C84 C433, C434	CCCCH050C50 CCCCH150J50 CCCCH220J50 CEAS010M50 CEAS101M10 CEAS101M10
KC-061 NF-091 BB1185-F8 TZJ33B GG1007 EAS220M50	CAPACITORS C506, C507(KUC only) C310, C311, C331, C332 C383 C509(KUC only) C151-C153, C401 C501-C503(KUC only) C81, C82 C83, C84 C433, C434	CCCCH050C50 CCCCH150J50 CCCCH220J50 CEAS010M50 CEAS101M10 CEAS101M10
NF-091 SB1185-F8 TZJ33B SG1007 EAS220M50	C506, C507(KUC only) C310, C311, C331, C332 C383 C509(KUC only) C151-C153, C401 C501-C503(KUC only) C81, C82 C83, C84 C433, C434	CCCCH150J50 CCCCH220J50 CEAS010M50 CEAS101M10 CEAS101M10
TZJ33B 5G1007 EAS220M50	C383 C509(KUC only) C151-C153, C401 C501-C503(KUC only) C81, C82 C83, C84 C433, C434	CCCCH220J50 CEAS010M50 CEAS101M10 CEAS101M10
TZJ33B 5G1007 EAS220M50	C151-C153, C401 C501-C503(KUC only) C81, C82 C83, C84 C433, C434	CEAS101M10 CEAS101M10
TZJ33B 5G1007 EAS220M50	C501-C503(KUC only) C81, C82 C83, C84 C433, C434	CEAS101M10
EAS220M50	C83, C84 C433, C434	
EAS220M50		CEASIOIMIO CEASIOIM25
	C216, C217, C303, C330, C336	CEAS220M25
		CEAS330M16
D1/6PMJ	C338, C339, C381, C382, C334	CEAS330M16
D1/6PM J	C385, C386, C411, C412 C508(KUC only)	CEAS330M16
	C249	CEAS330M16 CEAS330M25
	C415, C416	CEAS3R3M50
	C242, C243, C413, C414	CEAS470M50
	C512(KUC only) C302	CEAS471M10 CEAS471M6R3
L5HY8	C160, C162	CEAS4R7M50
	C309	CEASR47M50
G1006	C161 C157, C164, C169, C308	CFTXA104J50 CGCYX103K25
	C158, C159, C163, C230-C232	CGCYX104K25
D1/6PMJ	C236, C241, C246-C248, C301 C337	CGCYX104K25 CGCYX104K25
	C504, C505(KUC only)	CGCYX104K25
	C156, C168	CGCYX333K25
SG1016	C306, C417, C418	CGCYX473K25 CKCYB152K50
	C155	CKCYB182K50
KN1061	C170	CKCYB332K50
NP1575	C171, C172	CKCYB472K50
		CKCYF103Z50
	C167, C204, C205, C207	CEAS101M10 CKCYF103Z50
	C234, C235, C237-C240, C329	CKCYF103Z50
KA1372Q	C333, C335, C402, C403	CKCYF103Z50
-	• • • • • • • • • • • • • • • • • • • •	CKCYF103Z50 CKCYF473Z50
31687	C429, C430	CQMA152J50
C7870NE	CACL/VIIC and a	COMMA 202150
B81464-12P	• • • • • • • • • • • • • • • • • • • •	CQMA393J50 DCM1005
M1031XS	•	
		RD1/2PMFR47J
M4558DX	R238, R239, R244, R245	RN1/6PQ1002F
MASSONY	VR151, VR152(22k)	RCP1046
M4558DX M1700P-L	VR401, VR402(100k) Other Resistors	RCP1048 RD1/6PM
45841CP		**************************************
174LS624N	OTHERS CN11 JUMPER CONNECTOR(13P)	
	CIVIT JUMPER CONNECTOR (13P)	52147_1210
C74HCT04AF	CN101 CONNECTOR(12P)	52147-1310 12FMZ-ABT
274HCT04AF PD6383GF	CN101 CONNECTOR(12P) CN201 CONNECTOR(6P)	12FMZ-ABT RKP-533
C74HCT04AF	CN101 CONNECTOR(12P) CN201 CONNECTOR(6P) CN205 CONNECTOR(2P)	12FMZ-ABT RKP-533 2-173981-2
274HCT04AF PD6383GF	CN101 CONNECTOR(12P) CN201 CONNECTOR(6P)	12FMZ-ABT RKP-533
	EN1061 IP1575 EA1372Q ID2500BQ 6517 1687 7870NE 381464-12P M1031XS IS14256A-80ZC M2902N M4558DX M1700P-L IS841CP 74LS624N	C236, C241, C246-C248, C301 C337 C504, C505(KUC only) C156, C168 C307 C306, C417, C418 C155 CN1061 C170 C171, C172 C384(KUC only) C384(SELM only) C167, C204, C205, C207 C234, C235, C237-C240, C329 C333, C335, C402, C403 C510, C513 (KUC only) C384(SELM only) C1687 C321(SELM only) C321(SELM only) C321(SELM only) C429, C430 C461(KUC only) VC501(20pF)(KUC only) M4558DX M4558DX M4558DX M4558DX M4558DX M1700P-L M4558DX Other Resistors

Mark No. De	escription	Parts No.
CN402	CONNECTOR(9P)(KUC only) KP C9
	CONNECTOR(5P)(SELM onl	y) KPC5
	EARTH PLATE	VNF-091
X301	XTAL RESONATOR	PSS1008
X302	CERAMIC RESONATOR	DSS1054
X501	CRYSTAL RESONATOR	VSS1026
	(KUC only) PCB BINDER	VEF1008
PHOTO IN	NTERRUPTER ASS'	Υ
SEMICOND	JCTORS	
IC682		GP1A30R
CAPACITOR	15	CKCYF223Z50
C651		CKC 1 F223Z50
RESISTORS	;	
All Resist	tors	RDI/6PM
AUDIO O	UT ASS'Y	•
CAPACITOR	ne .	
		CKCYF103Z50
C804, C8	10(KUC only)	CKCYF103Z50
C809	V6	CKCYF473Z50
C801, C8	02	CQMA152J50
C803(KU		CQMA471J50
C815(SE)		CKCYB102K50
`	<i>.</i>	CRC I D102R30
RESISTORS		
All Resis	tors	RD1/6PM
OTHERS		
JA801 JA	CK	PKB1009
	CK(KUC only)	VKB1029
JA803 JA		RKN1004
	ARTH PLATE	VNF-091
TRANSIS	TOR UNIT	
0511100115	107000	
SEMICOND	UCTORS	00D 1074
∆ QI		2SB1274
CAPACITOR	e e	
CAPACITOR C54	13	CKCYF473Z50
C 54		CINCIITIBLO



Service Manual

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COMPACT DISC PLAYER

CDJ-500G

CHAPTER 2

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CHAF	PTER 2
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- 2.1 PACKING AND EXPLODED VIEWS2-2
- 2.2 SCHEMATIC AND PCB CONECTION

DIAGRAMS2-7

PIONEER ELECTRONIC CORPORATION 4-1, Meguro 1-Chome, Meguro-ku, Tokyo 153, Japan

PIONEER ELECTRONICS SERVICE INC. P.O.Box 1760,Long Beach, California 90801 U.S.A.

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PIONEER ELECTRONICS AUSTRALIA PTY.LTD. 178-184 Boundary Road, Braeside, Victoria 3195, Australia TEL:[03]580-9911

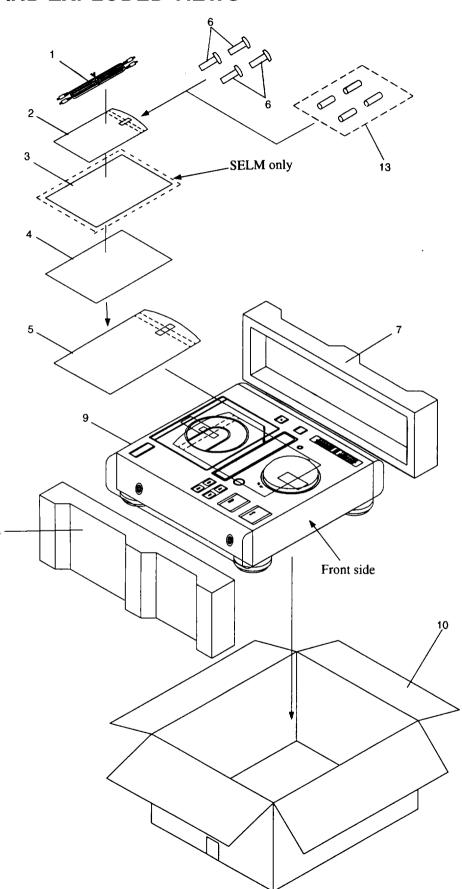
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2.1 PACKING AND EXPLODED VIEWS

(1) PACKING



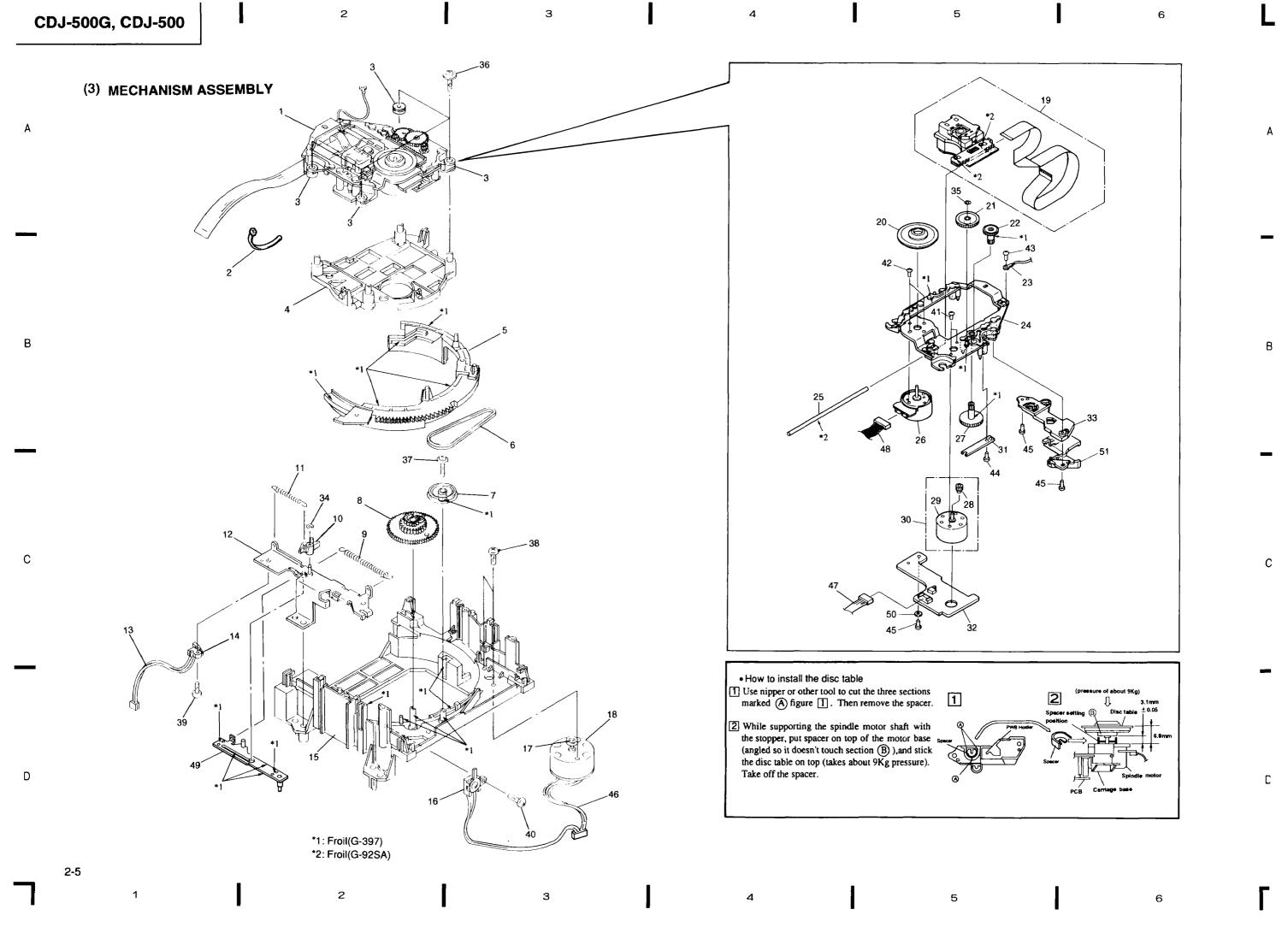
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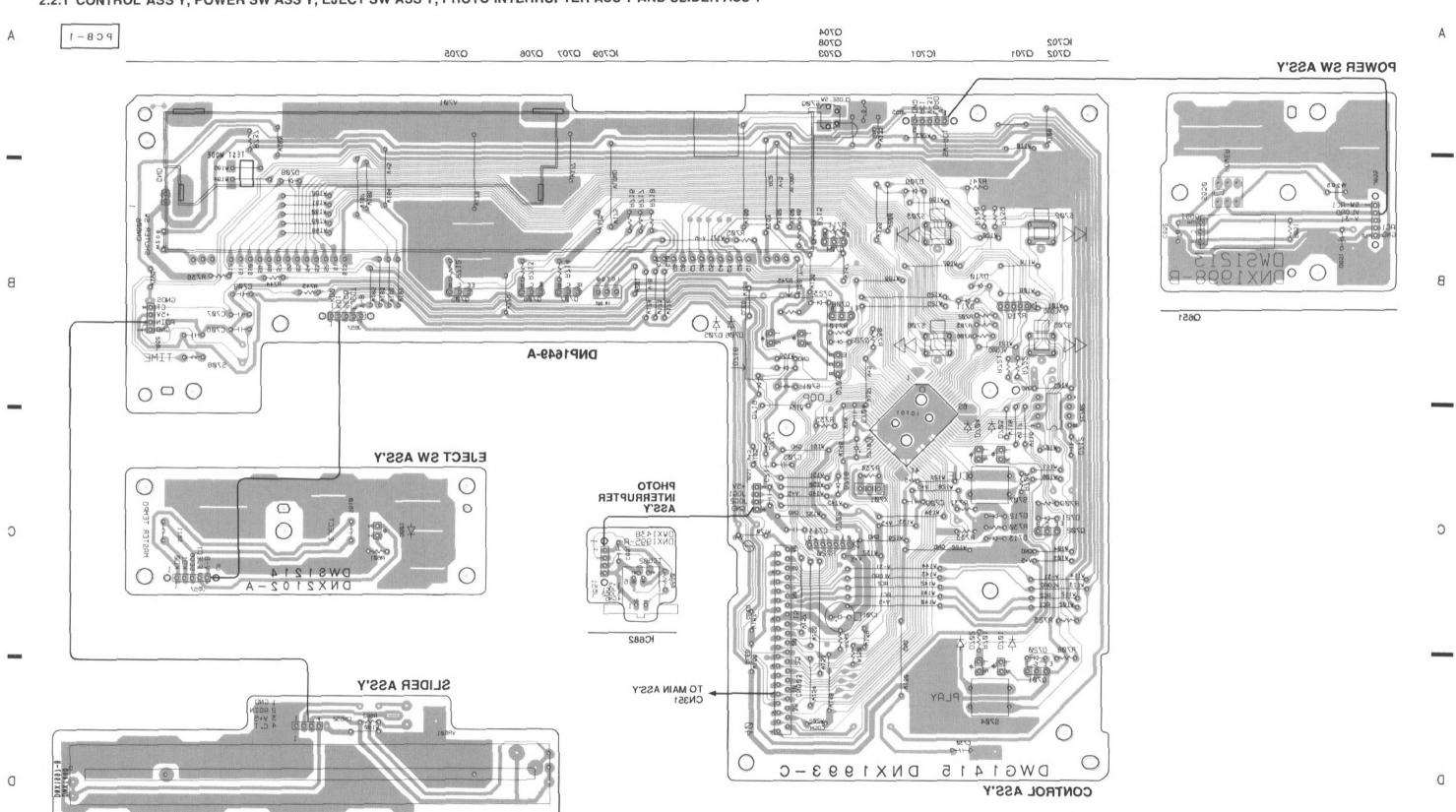
2-2

3



2.2 SCHEMATIC AND PCB CONNECTION DIAGRAMS

2.2.1 CONTROL ASS'Y, POWER SW ASS'Y, EJECT SW ASS'Y, PHOTO INTERRUPTER ASS'Y AND SLIDER ASS'Y



This diagram is viewed from the foil side.

2-8

- 1

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VR601

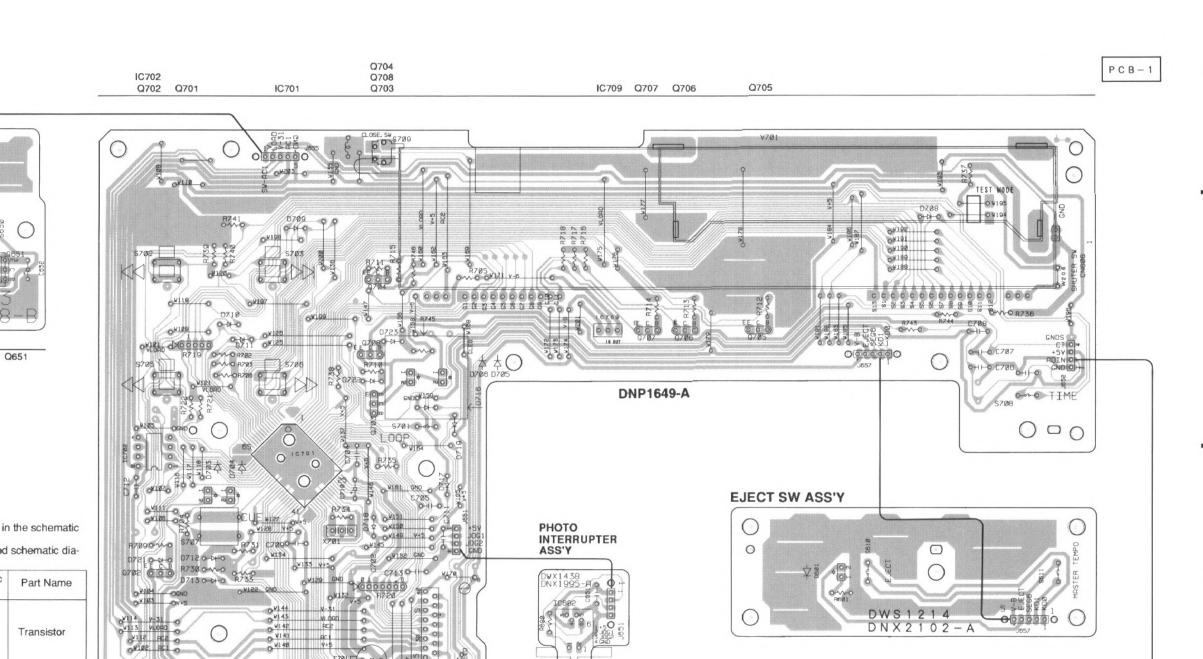
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POWER SW ASS'Y

00

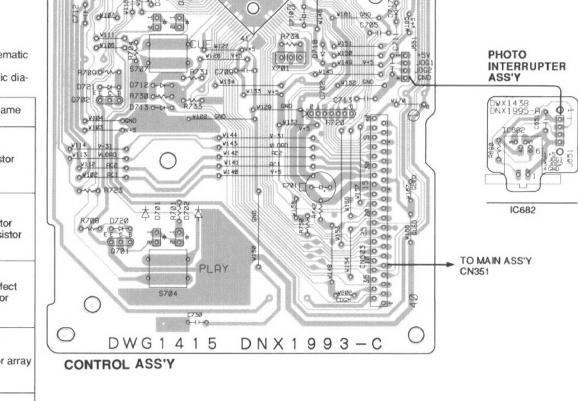


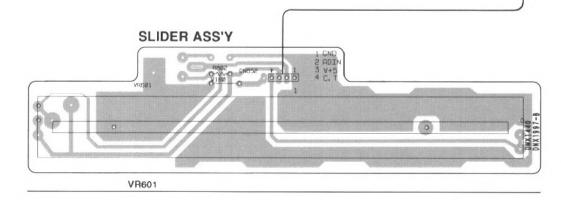
 Part numbers in PCB diagrams match those in the schematic diagrams.
 A comparison between main parts of PCB and schematic dia-

NOTE FOR PCB DIAGRAMS:

grams is shown below.

Symbol in PCB Diagrams	Symbol in Schematic Diagrams	Part Name
○ ○ ○ ○ B C E	B C E B C E	Transistor
●(○ ○ ○) B C E	B C E B C E	Transistor with resistor
© 0 0 D G S	D G S D G S	Field effect transistor
(000 <u>%000</u>)4		Resistor array
000		3- terminal regulator





This diagram is viewed from the mounted parts side.

2-9

D

2

3

4

5

6

В

D

CDJ-500G,

3



PICKUP Ass'y,
CONTROL Ass'y,
POWER SW Ass'y,
EJECT SW Ass'y,
PHOTO INTERRUPTER Ass'y,
SLIDER Ass'y

6

5

VOLTAGES(V) OF CONTROL ASS'Y SECTION

Note: Voltages are in the PLAY mode.

IC701(PD4543A)

Pin No.	Voltage(V)
1	0
2	0
3	4.8
4	0
5	0
6	0
7	0
8	5

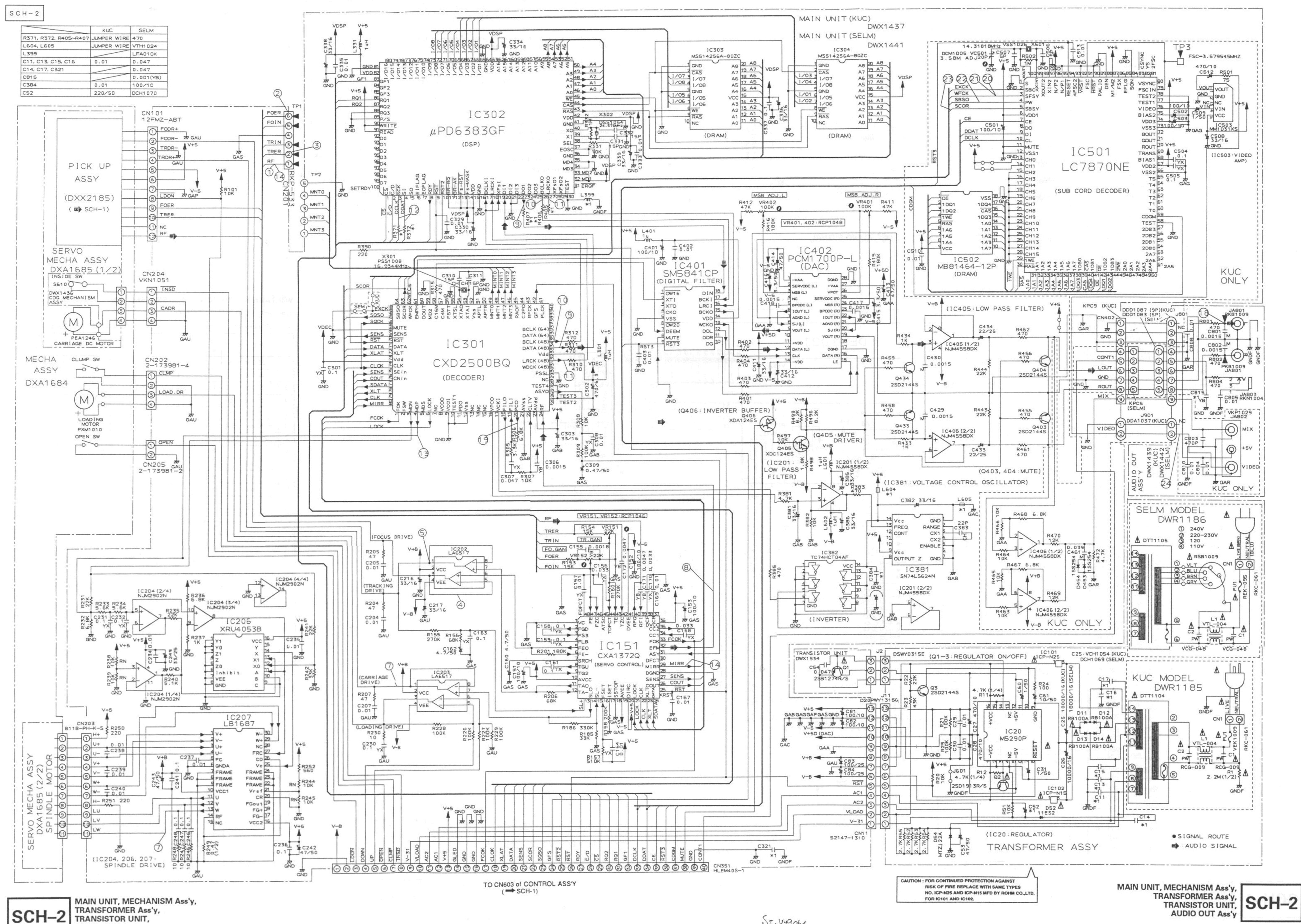
IC702(NM93C46N)

Pin	Voltage(V)	Pin	Voltage(V)
No.	voltage(v)	No.	voitage(v)
1	-25.3 to -25	41	4.9
2	-25.3 to -25	42	4.9
3	-25.3 to -24.9	43	0 to 4.8
4	-25.3 to -25	44	0.1
5	-25.3 to -25	45	0.1
6	-25.6 to -25.4	46	4.9
7	-25.4 to-25.2	47	0 to 4.9
8	5	48	0
9	4.9	49	0.1
10	0.1	50	0
11	4.9	51	4.8
12	0	52	5
13	0 to 4.9	53	0
14	4.8	54	0
15	0.2	55	0
16	4.3	56	0
17	4.9	57	0
18	0.1	58	0
19	0	59	4.8
20	0	60	0
21	0	61	-1.5
22	4.9	62	4.6
23	0	63	-1.5
24	4.9	64	-23 to -11
25	4.9	65	-14 to -1.1
26	4.9	66	-14 to -6.6
27	2.4	67	-6.3 to 1.8
28	2.4	68	-4.5 to 2.7
29	5	69	-6 to1.8
30	5	70	-10.2 to -0.2
31	2.4 to 2.6	71	-28.1
32	2.3 to 2.6	72	-18.9
33	0	73	-19
34	2.4	74	-21.6
35	2.2	75	-19
36	4.8	76	-18.4
37	4.9	77	-25.3 to -25
38	4.9	78	-25.3 to -25.1
39	4.9	79	-25.3 to -25

80

-25.5 to -25.3

2.2.2 MAIN UNIT, MECHANISM ASS'Y, TRANSFORMER ASS'Y TRANSISTOR UNIT AND AUDIO OUT ASS'Y



AUDIO OUT Ass'y

SI. 149061

AUDIO OUT Ass'y

Voltage(V)

2.5 2.5

2.5 2.5 0.6 0

0

0

0

IC207(LB1687)

WAVEFORMS

(RF)

(RF)

TP1-Pin 1: PLAY MODE

500mV/div 500nsec/div

TP1-Pin 1: TRACK SEARCH MODE

- GND

GND

- GND

- GND

500mV/div 200µsec/div

TP1-Pin 6: PLAY MODE

100mV/div 10msec/div

TP1-Pin 2: PLAY MODE

TP1-Pin 2: 10T-JUMP (*1) MODE

1V/div 10msec/div

(TRER) 1V/div 1msec/div

July 10 to the little

(FOER)

(TRER)

Note: The encircled numbers denote measuring points in the schematic diagram.

1V/div 200msec/div

IC202-Pin 9: PLAY MODE (FODR)

IC202-Pin 3: PLAY MODE (TRDR)

IC202-Pin 1: 10T-JUMP (*1) MODE

500mV/div 1msec/div

IC207-Pin 13: PLAY MODE (SPDR)

5V/div 50msec/div

(TRDR)

500mV/div 1msec/div

1V/div 1msec/div

(FODR)

IC202-Pin 9: FOCUS-IN (*2) MODE

*1 10T-JUMP

*2 FOCUS-IN

- GND

- GND

After switching to the play mode.
 Press the manual search key.
 Press the PLAY key without loading a disc.

IC201-Pin 4: TRACK SEARCH MODE

IC203-Pin 1: PLAY MODE

200mV/div 2sec/div

(CADR)

(CADR)

2V/div 500msec/div

8 IC151-Pin 32: PLAY MODE (EFM)

2V/div 500nsec/div

: PLAY MODE

Upper: IC301-Pin34

Lower: IC302-Pin23

5V/div 500nsec/div

: PLAY MODE
Upper : IC301-Pin35 (BCLK)

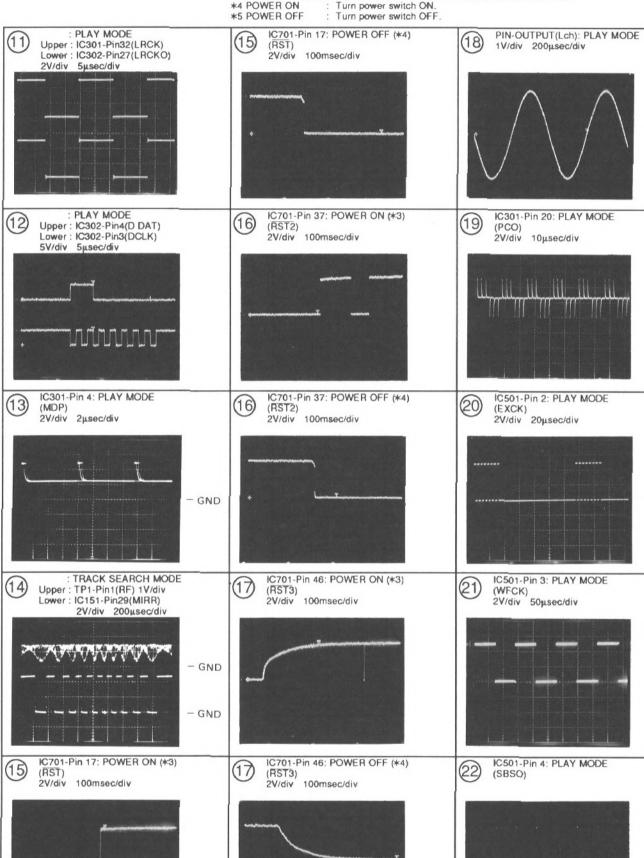
2V/div 5µsec/div

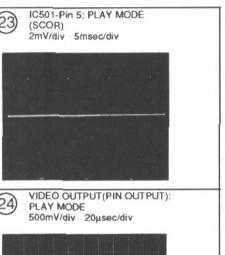
Lower: IC302-Pin26 (BCLKO)

- GND

*3 TRACK SEARCH: Track search from outermost track to innermost track. *4 POWER ON

Turn power switch ON. *5 POWER OFF : Turn power switch OFF.





VOLTAGES (V) OF MAIN UNIT SECTION Note: Voltages are in the PLAY mode.

IC20	1(NJM4558DX)
Pin No.	Voltage(V)
1	1.6
2	1.6
3	1.6
4	-10.3
5	0
6	0
7	0
8	9

IC20	3(LA6517)
Pin No.	Voltage(V)
1	0
2	9
3	0
4	-10.3
5	0
6	0
7	0 to 0.3
8	0

IC202(LA6517)		
Pin No.	Voltage(V)	
1	0	
2	9	
3	0.4	
4	-10.2	
5	0.4	
6	0.4	
7	0	
8	0	

	No.	, , , ,	1	NO.	L
	1	2.5	1	1	
	2	2.5	1	2	
	3	2.5	1	3	
	4	5	1 1	4	
	5	2.5	1	5	L
	6	2.5	1	6	
	7	2.5	1	7	
	8	3.6	1	8	
	9	0.6	1 1	9	
	10	2.5	1 1	10	
	11	0	1	11	
	12	0	1 1	12	
	13	0	1 [13	
	14	0	1 [14	
			-	15	
	IC206(XRU4053B)				
	Pin No.	Voltage(V)		18	
	1	0	1 [19	
	2	0	1 [20	
1	_		4 [21	

0

0

0

3.6

2.9

2.5 2.5

0

5

Voltage(V)

1.8

IC503(MM1031XS)

15

16

IC204(NJM2902N)

Voltage(V)

10	9
11	4.5
12	4.5
13	4.5
14	0
15	0
16	5
17	0.4
18	2.5
19	0.7
20	0
21	2.4
22	0
23	0
24	0
25	2.5
26	0
27	3.6
28	0
29	2.5
30	2.5
IC40	05(NJM4558DX

IC405(NJM4558DX)

Pin No.	Voltage(V)
1	0
2	0
3	0
4	-10.3
5	0
6	0
7	0
8	9.1

IC151(CXA1372Q)

	77(CAA1372Q)
Pin	Voltage(V)
No.	
1	0
2	0
3	0
4	0
5	0.4
6	0
7	0.3
8	0
9	0
10	5
11	0
12	0
13	0
14	0 to 0.3
15	0
16	-3.9
17	1.3
18	0
19	-5
20	5
21	4.9
22	4.8
23	4.9
24	0.2
25	4.9
26	0.1
27	4.8
28	0
29	0
30	0
31	2.5
32	2.6
33	4.9
34	-1.1
35	-1.1 -1.4
<u> </u>	5
36 37	-0.4
38 39	-2.3 0
40	0.8 -5
41	
42	0
43	0
44	0
45	0
46	0.1
47	0.1
48	0

IC301(CXD2500BQ)

Pin		Pin	
No.	Voltage(V)	No.	Voltage(V)
1	4.9	41	1.4
2	0	42	4.8
3	4.9	43	2.5
4	2.5	44	0
5	0	45	4.9
6	4.9	46	4.4
7	0	47	0
8	4.9	48	0
9	0	49	0
10	0	50	1.2
11	0	51	1.2
12	0	52	0
13	0	53	2.3
14	0	54	2.5
15	0	55	0
16	1.6	56	2
17	2.6	57	1.4
18	2.6	58	1.2
19	2.5	59	0
20	2.5	60	0
21	0	61	0
22	2.6	62	2.5
23	5	63	0.1
24	2.6	64	0.2
25	0	65	0.7
26	0	66	4.3
27	2.5	67	4.8
28	0	68	0
29	0	69	2.4 to 2.6
30	0	70	4.9
31	2.4	71	0.2
32	2.5	72	4.9
33	4.9	73	4.9
34	0	74	4.8
35	1.9	75	4.8
36	2.5	76	0.1
37	1.7	77	0.2
38	2.5	78	4.9
39	0	79	4.8
40	4.9	80	0

IC303(MS514256A-80ZC)

C3U3(MS514256A-8UZ				
Pin No.	Voltage(V)			
1	0			
2	4.6	ŀ		
3	2.4			
4	2.2			
5	0			
6	2.2			
7	2.4			
8	4.7			
9	4.4			
10	0			
11	0.9			
12	2.7			
13	2.4 to 2.6			
14	2.5			
15	4.9			
16	2.5			
17	2.4			
18	2.4			
19	2.4 to 2.6			
20	2.2 to 2.6			

C304(MS514256A-80ZC)				
Pin	Voltage(V)			
Vo .				
1	0			
2	4.6			
3	2.6			
4	2.3			
5	0			
6	2.4			
7	2.7			
8	4.7			
9	4.4			
10	0			
11	0.9			
12	2.7			
13	2.4 to 2.6			
14	2.5			
15	4.9			
16	2.5			
17	2.4			
18	2.4			
19	2.4 to 2.6			
20	2.2 to 2.6			

IC302(UPD6383GF)

Voltage(V)

4.8

4.9

4.9

0.1

0

0

0

4.9

4.9

Voltage(V)

2.4

2.4

2.4 to 2.6 2.2 to 2.6

2.4 to 2.5

2.4 to 2.5

2.3 to 2.6

2.3 to 2.6

2.2 to 2.7

53

10	4.9	60	0.5 to 4
11	4.9	61	2 to 4
12	4.9	62	2.5 to 4.8
13	4.9	63	4.9
14	4.9	64	0
15	4.9	65	2.4
16	0	66	2.7
17	2.2	67	2.6
18	2.5	68	2.3
19	2.2	69	2.2
20	2.4	70	2.4
21	0	71	2.4
22	0	72	2.2
23	2.5	73	0
24	3.1	74	0
25	3.1	75	0
26	2.5	76	0
27	2.5	77	0
28	0	78	0
29	0	79	0
30	0	80	0
31	4.9	81	0
32	4.9	82	4.9
33	4.9	83	0 to 4.8
34	0	84	0
35	0	85	0
36	0	86	4.9
37	0	87	4.9
38	4.9	88	0
39	0	89	0
40	2.6	90	5
41	0	91	5
42	4.9	92	4.9
43	4.4	93	4.9
44	4.6	94	4.9
45	4.7	95	4.9
46	3.1	96	4.9
47	2.7	97	4.9

2.4 to 2.6

2.5

2.5

98

100

4.9

4.9

4.8

IC381(SN74LS624N)

Pin No.	Voltage(V)
1	11.6
2	11.6
3	2.1
4	2.1
5	0
6	1.1
7	0
8	1.1
9	4.9
10	0
11	0
12	0
13	1.6
14	4.9

IC382(TC74HCT04AF)

	No.	Voltage(V)
	1	0
	2	4.9
	3	0
	4	4.9
	5	1.1
	6	2.2
	7	0
	8	4.9
	9	0
	10	4.9
	11	0
	12	4.9
Į	13	0
ı	14	4.9
_		

IC406(NJM4558DX)

	1C406(NJM4558DX)		
	Pin No.	Voltage(V)	
	1	0	
	2	0	
	3	0	
	4	-10.3	
	5	0	
	6	0	
	7	0	
	8	9.1	

IC401(SM5841CP)

Pin No.	Voltage(V)
1	4.8
2	2.2
3	2.5
4	2.2
5	0
6	4.8
7	0
8	0
9	4.9
10	3
11	1.5
12	1.6
13	3.6
14	4.9
15	1.1
16	2.5
17	2.5

IC502(MB81464-12P)

2.5

Voltage(V)

2.5 0 0

5	2.5
6	3.1
7	3.1
8	1.8
9	5
10	3.1
11	3.1
12	1.9
13	2.1
14	1.4
15	0
16	3.9
17	0
18	0
_	

IC402(PCM1700P-L)

Pin	\/alta==(\)()		
No.	Voltage(V)		
1	-5		
2	-1.8		
3	-4.5		
4	0		
5	2.9		
6	0		
7	0		
8	0		
9	0		
10	0		
11	5		
12	1.7		
13	1.7		
14	-5		
15	3.8		
16	1.7		
17	0		
18	0		
19	0		
20	0		
21	0		
22	0		
23	2.9		
24	-4.5		
25	-1.7		
26	-4.2		
27	5		
28	0		

IC501(LC7870NE)

Pin	Voltage(V)	Pin	Voltage(V)
No.		No.	
1	0	51	3.1
2	0.7	52	3.1
3	2.5	53	3.1
4	0.2	54	0
5	0.1	55	0
6_	5	56	0
7	0.1	57	0
8	0	58	0
9	0.1	59	0.1
10	4.9	60	0
11	0	61	0
12	0	62	0
13	5	63	0
14	5	64	0
15	0	65	0
16	0	66	0
17	0	67	5
18	0	68	2.8
19	0	69	3.9
20	0	70	3.9
	0	71	4
21			
22	0	72	4.4
23	0	73	0
24	0	74	5
25	0	75	2.9
26	0	76	4
27	0	77	0
28	0	78	0
29	4.9	79	2.8
30	2.5	80	4.9
31	1.4	81	3
32	2.1	82	4.9
33	1.9	83	4.5
34	3.1	84	0
35	1.8	85	0
36	3.1	86	4.9
37	3.1	87	5
38	3.1	88	0
39	0	89	5
40	3.9	90	5
41	0	91	2.8
42	2.5	92	5
43	0	93	2.5
44	0	94	4.9
45	1.5	95	5
46	4.9	96	5
47	2.1	97	0
			4.9
48	1.9	98	4.5

VOLTAGES(V) OF TRANSFORMER **ASS'Y SECTION**

Note: Voltages are in the PLAY mode. IC20(M5290P)

1C20(M5290P)				
Pin	Voltage(V)			
No.				
1	10.3			
2	0			
3	-9.6			
4	0			
5	-5 0			
6				
7	1.3			
8	4.9			
9	0			
10	1.2			
11	1.2			
12	5			
13	0			
14	8.3			
15	0			
16	9.1			

